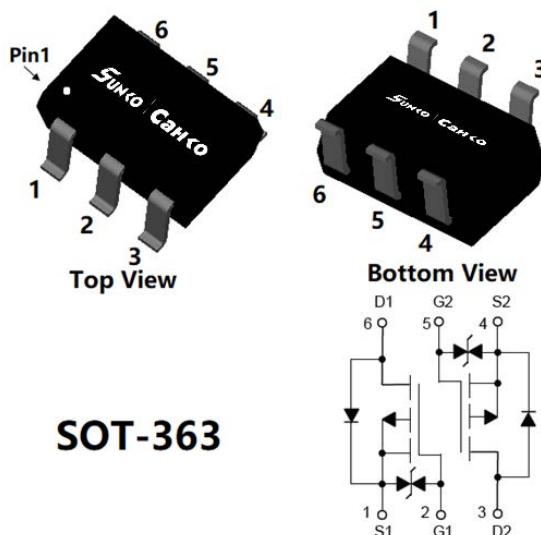


P-Channel and P-Channel Complementary MOSFET



Product Summary

- V_{DS} -60V
- I_D -0.32A
- $R_{DS(on)}$ (at $V_{GS}=-10V$) $<3\Omega$
- $R_{DS(on)}$ (at $V_{GS}=-4.5V$) $<3.5\Omega$
- Gate-Source ESD Rating Up to 2KV (HBM)

General Description

- P-Channel Switch with Low $R_{DS(on)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free

Applications

- Power management
- Portable equipment

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-source Voltage	V_{DS}	-60	V
Gate-source Voltage	V_{GS}	± 20	V
Drain Current $T_A=25^\circ C$	I_D	-0.32	A
$T_A=100^\circ C$	I_D	-0.2	
Pulsed Drain Current ^A	I_{DM}	-1.2	A
Total Power Dissipation ^B $T_A=25^\circ C$	P_D	0.41	W
$T_A=100^\circ C$	P_D	0.16	
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	°C

Thermal resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient ^C	$R_{\theta JA}$	250	300	°C/W

Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
BSS84KDwj	F2	84K	3000	30000	120000	7" reel

■ Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
		$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}, T_J=150^\circ\text{C}$	-	-	-100	
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	±10	μA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.5	-2	V
Static Drain-Source On-Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-0.32\text{A}$	-	2.2	3	Ω
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-0.2\text{A}$	-	2.5	3.5	
Diode Forward Voltage	V_{SD}	$I_{\text{S}}=-0.32\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.9	-1.3	V
Gate resistance	R_{G}	f=1MHz, Open drain	-	700	-	Ω
Maximum Body-Diode Continuous Current	I_{S}		-	-	-0.32	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	35	-	pF
Output Capacitance	C_{oss}		-	6	-	
Reverse Transfer Capacitance	C_{rss}		-	3	-	
Switching Parameters						
Total Gate Charge	Q_{g}	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-1\text{A}$	-	1.7	-	nC
Gate-Source Charge	Q_{gs}		-	0.6	-	
Gate-Drain Charge	Q_{gd}		-	0.2	-	
Reverse Recovery Charge	Q_{rr}	$I_{\text{F}}=-1\text{A}, \text{di}/\text{dt}=100\text{A}/\text{us}$	-	10	-	nC
Reverse Recovery Time	t_{rr}		-	18	-	ns
Turn-on Delay Time	$t_{\text{D(on)}}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DD}}=-30\text{V}, I_{\text{D}}=-1\text{A}$ $R_{\text{GEN}}=2.3\Omega$	-	6	-	ns
Turn-on Rise Time	t_{r}		-	21	-	
Turn-off Delay Time	$t_{\text{D(off)}}$		-	31	-	
Turn-off fall Time	t_{f}		-	32	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B. P_d is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.C. The value of R_{GJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in the still air environment with $T_A=25^\circ\text{C}$.The maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.

■ Typical Electrical and Thermal Characteristics Diagrams

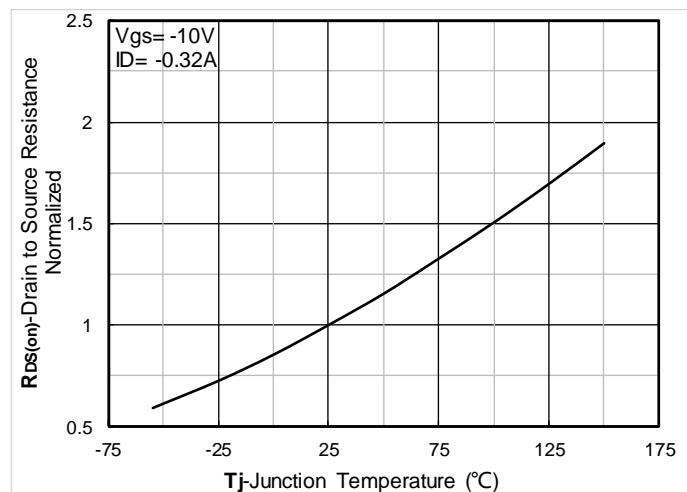
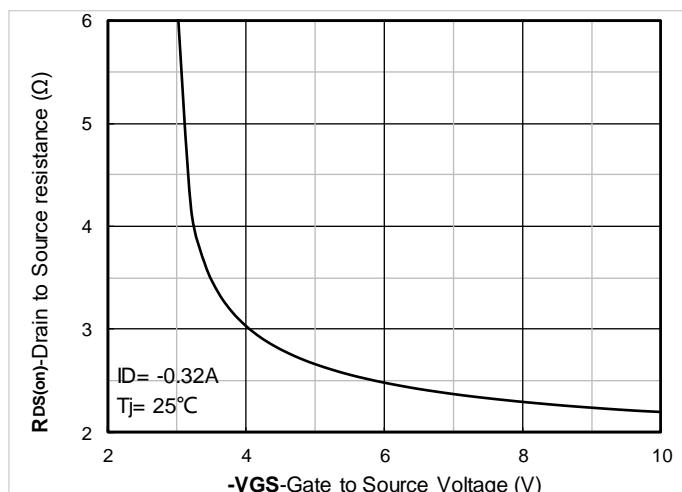
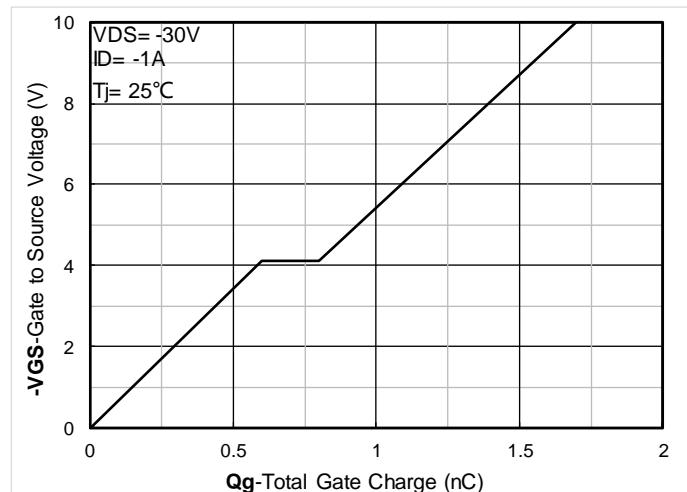
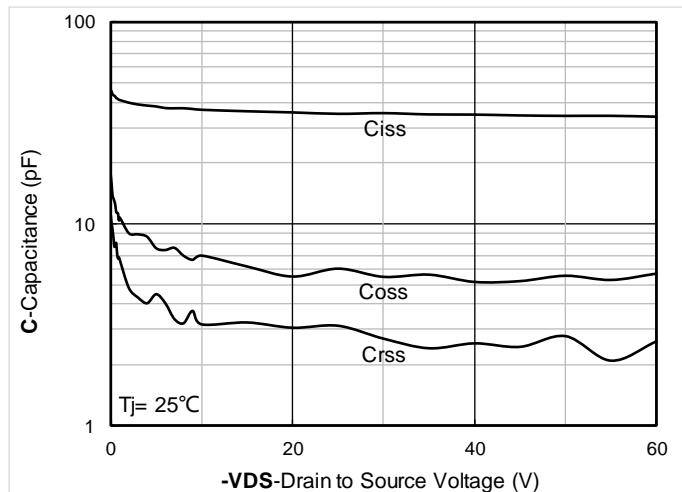
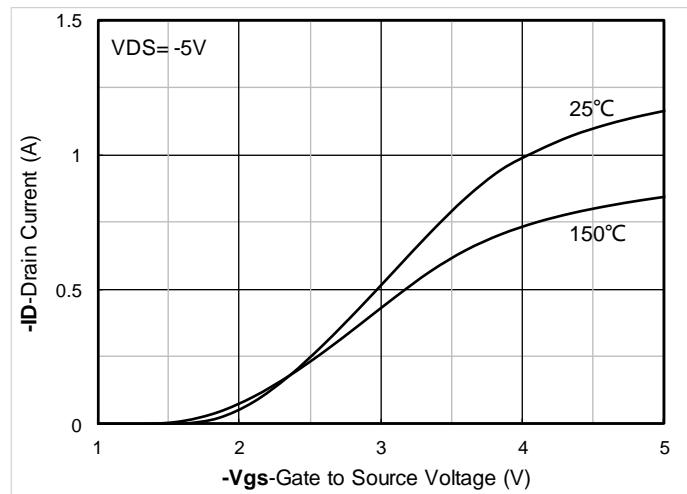
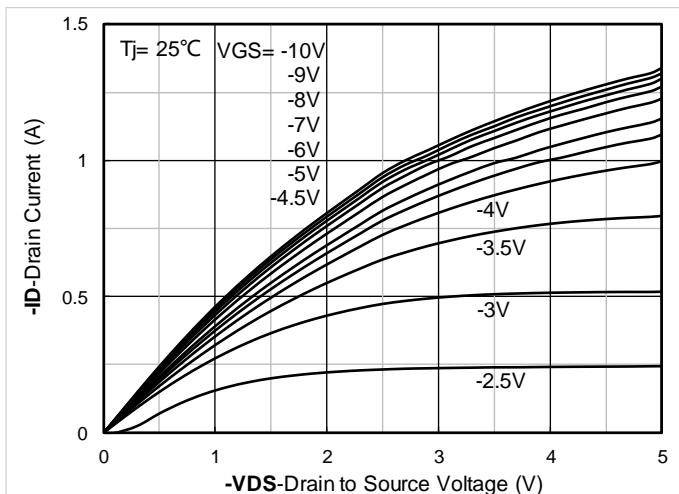


Figure 5. On-Resistance vs Gate to Source Voltage

Figure 6. Normalized On-Resistance

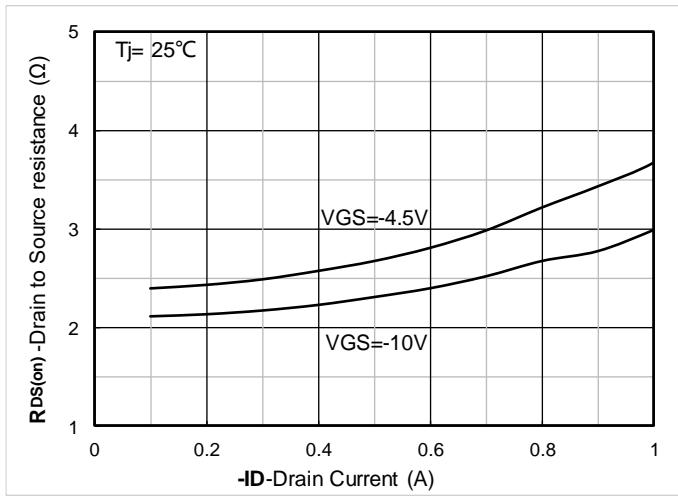


Figure 7. RDS(on) VS Drain Current

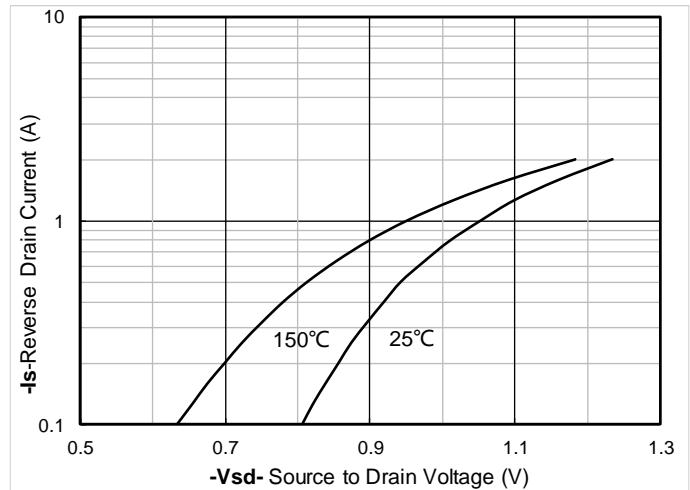


Figure 8. Forward characteristics of reverse diode

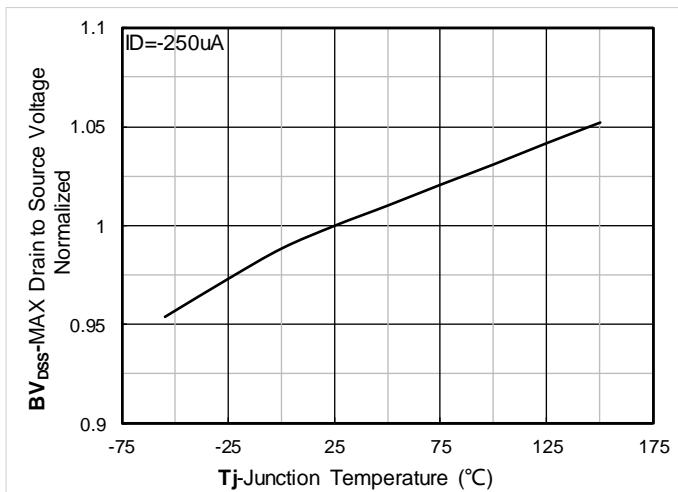


Figure 9. Normalized breakdown voltage

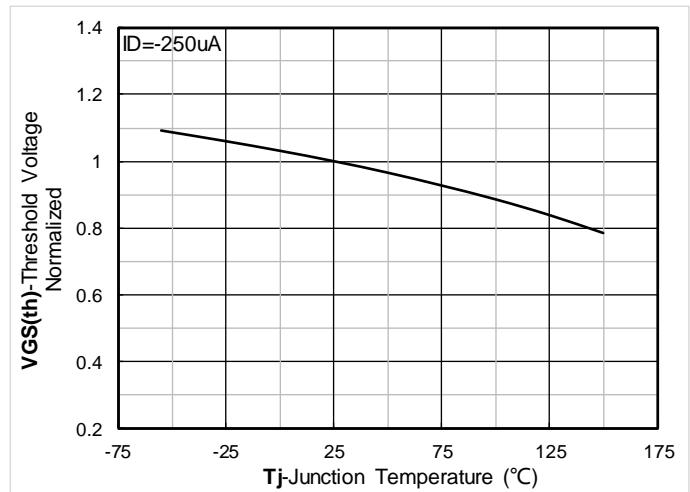


Figure 10. Normalized Threshold voltage

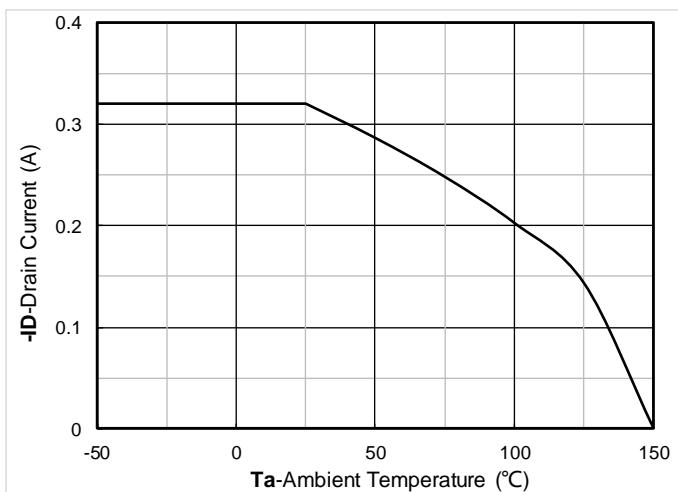


Figure 11. Current dissipation

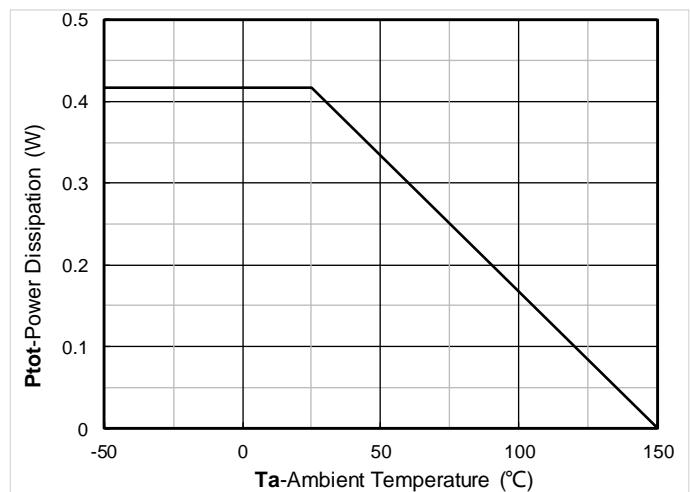


Figure 12. Power dissipation

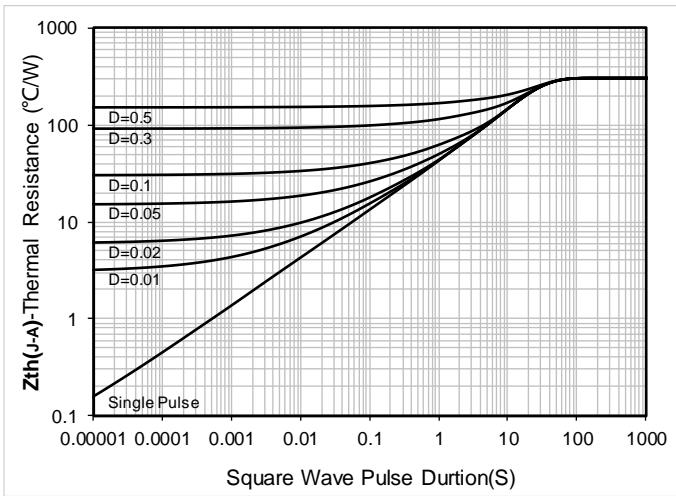


Figure 13. Maximum Transient Thermal Impedance

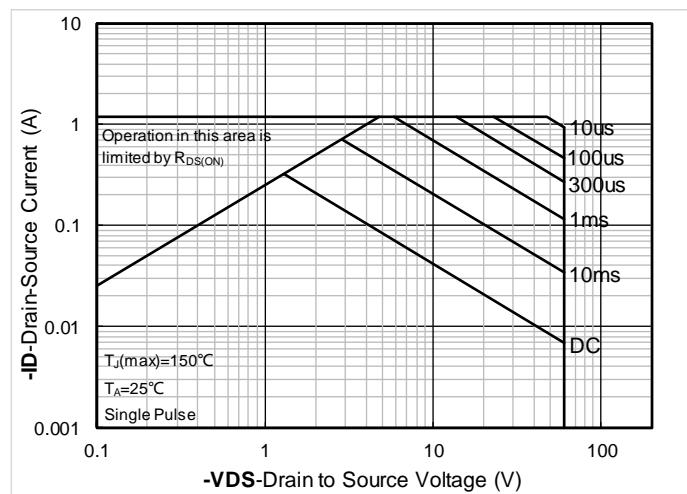
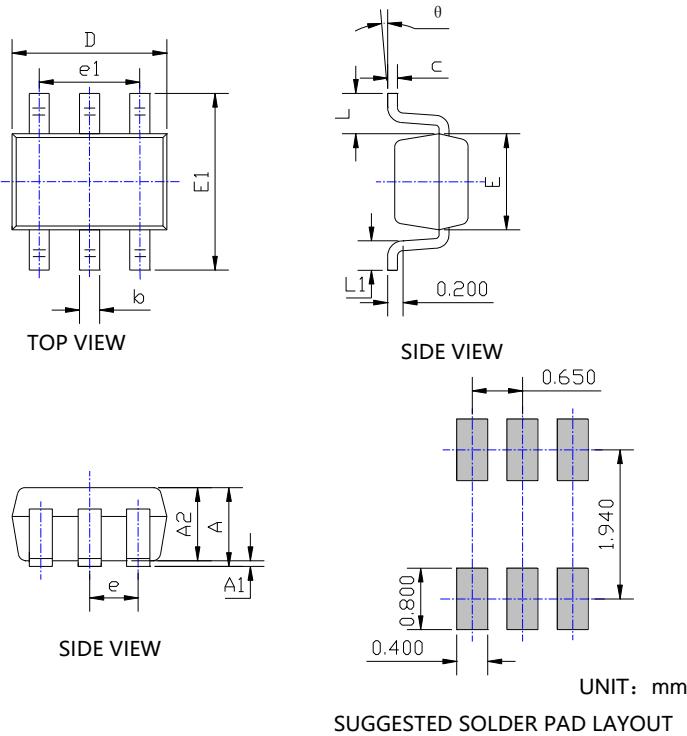


Figure 14. Safe Operation Area

■ SOT-363 Package information



SYMBOL	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.035	0.043	0.900	1.100
A1	0.000	0.004	0.000	0.100
A2	0.035	0.039	0.900	1.000
b	0.006	0.014	0.150	0.350
c	0.004	0.010	0.100	0.250
D	0.071	0.087	1.800	2.200
E	0.045	0.053	1.150	1.350
E1	0.085	0.096	2.150	2.450
e	0.026 TYP		0.650 TYP	
e1	0.047	0.055	1.200	1.400
L	0.021 REF		0.525 REF	
L1	0.010	0.018	0.260	0.460
θ	0°	8°	0°	8°

NOTE:

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3. THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

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