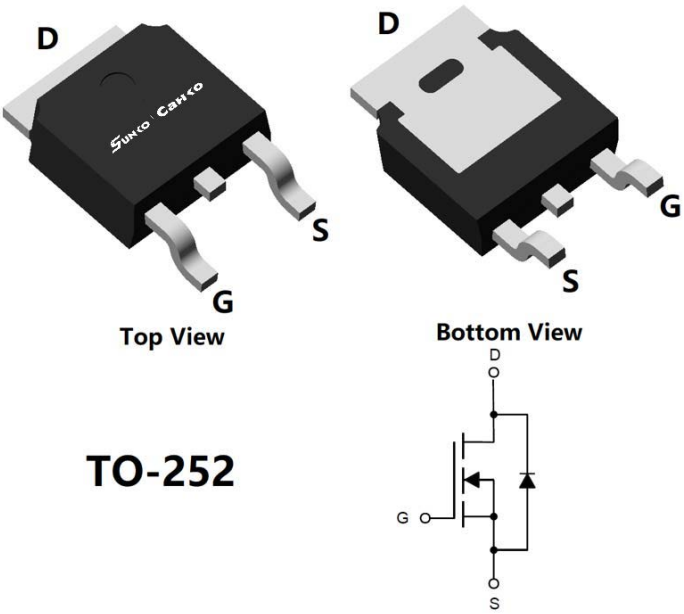
# N-Channel Enhancement Mode Field Effect Transistor




## Product Summary

- $V_{DS}$ 900V
- $I_D$ 5A
- $R_{DS(ON)}$( at $V_{GS}$=10V) ＜1.5Ω
- 100% EAS Tested
- 100% ▽$V_{DS}$ Tested

## General Description

- Super Junction High Voltage MOSFET technology
- Low power loss by high speed switching and low on-resistance
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

## Applications

- Switching applications
- PFC power supply stages
- PD Adapter

## ■ Absolute Maximum Ratings ($T_A$=25℃ unless otherwise noted)

| Parameter | | Symbol | Limit | Unit |
|---|---|---|---|---|
| Drain-source Voltage | | $V_{DS}$ | 900 | V |
| Gate-source Voltage | | $V_{GS}$ | ±30 | V |
| Drain Current | $T_A$=25℃ | $I_D$ | 0.8 | A |
| | $T_A$=100℃ | | 0.5 | |
| | $T_C$=25℃ | | 5 | |
| | $T_C$ =100℃ | | 3 | |
| Pulsed Drain Current [A] | | $I_{DM}$ | 10 | A |
| Avalanche energy [B] | | EAS | 0.6 | mJ |
| Total Power Dissipation [C] | $T_A$=25℃ | $P_D$ | 3.2 | W |
| | $T_A$=100℃ | | 1.3 | |
| | $T_C$=25℃ | | 125 | |
| | $T_C$ =100℃ | | 50 | |
| Junction and Storage Temperature Range | | $T_J$ ,$T_{STG}$ | -55～+150 | ℃ |

## ■Thermal resistance

| Parameter | | Symbol | Typ | Max | Units |
|---|---|---|---|---|---|
| Thermal Resistance Junction-to-Ambient [D] | Steady-State | $R_{\theta JA}$ | 30 | 38 | ℃/W |
| Thermal Resistance Junction-to-Case | Steady-State | $R_{\theta JC}$ | 0.8 | 1 | |

## ■ Ordering Information (Example)

| PREFERED P/N | PACKING CODE | Marking | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|---|---|---|---|---|---|---|
| SCD05C90HJ | F1/F2 | SCD05C90HJ | 2500 | / | 25000 | 13“ reel |

## ■ Electrical Characteristics ($T_J$=25℃ unless otherwise noted)

| Parameter | Symbol | Conditions | Min | Typ | Max | Units |
|---|---|---|---|---|---|---|
| **Static Parameter** | | | | | | |
| Drain-Source Breakdown Voltage | $BV_{DSS}$ | $V_{GS}$= 0V, $I_D$=250μA | 900 | - | - | V |
| Zero Gate Voltage Drain Current | $I_{DSS}$ | $V_{DS}$=900V, $V_{GS}$=0V | - | - | 1 | μA |
| | | $V_{DS}$=900V, $V_{GS}$=0V, Tj=150℃ | - | - | 100 | |
| Gate-Body Leakage Current | $I_{GSS}$ | $V_{GS}$= ±30V, $V_{DS}$=0V | - | - | ±100 | nA |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}$= $V_{GS}$, $I_D$=250μA | 2 | 3 | 4 | V |
| Static Drain-Source On-Resistance | $R_{DS(ON)}$ | $V_{GS}$=10V, $I_D$=2.5A | - | 1.1 | 1.5 | Ω |
| Diode Forward Voltage | $V_{SD}$ | $I_S$=5A, $V_{GS}$=0V | - | - | 1.2 | V |
| Gate resistance | $R_G$ | f=1MHz | - | 2.5 | - | Ω |
| Maximum Body-Diode Continuous Current | $I_S$ | | - | - | 5 | A |
| **Dynamic Parameters** | | | | | | |
| Input Capacitance | $C_{iss}$ | $V_{DS}$=450V, $V_{GS}$=0V, f=0.5MHz | - | 450 | - | pF |
| Output Capacitance | $C_{oss}$ | | - | 12 | - | |
| Reverse Transfer Capacitance | $C_{rss}$ | | - | 1.8 | - | |
| **Switching Parameters** | | | | | | |
| Total Gate Charge | $Q_g$ | $V_{GS}$=10V, $V_{DS}$=720V, $I_D$=5A | - | 13.6 | - | nC |
| Gate-Source Charge | $Q_{gs}$ | | - | 3.4 | - | |
| Gate-Drain Charge | $Q_{gd}$ | | - | 5.8 | - | |
| Reverse Recovery Charge | $Q_{rr}$ | $I_F$=5A, di/dt=100A/us | - | 2.5 | - | nC |
| Reverse Recovery Time | $t_{rr}$ | | - | 486 | - | ns |
| Turn-on Delay Time | $t_{D(on)}$ | $V_{GS}$=10V, $V_{DD}$=450V, $I_D$=5A $R_{GEN}$=25Ω | - | 14 | - | ns |
| Turn-on Rise Time | $t_r$ | | - | 23 | - | |
| Turn-off Delay Time | $t_{D(off)}$ | | - | 44 | - | |
| Turn-off fall Time | $t_f$ | | - | 21 | - | |

A. Repetitive rating; pulse width limited by max. junction temperature.
B. $T_J$=25℃, $V_{DD}$=50V, $V_G$=10V, $R_G$=25Ω, L=1mH, IAS=1.1A.
C. $P_d$ is based on max. junction temperature, using junction-case thermal resistance.
D. The value of $R_{\theta JA}$ is measured with the device mounted on 1 $in^2$ FR-4 board with 2oz. Copper, in the still air environment with $T_A$ =25℃. The maximum allowed junction temperature of 150℃. The value in any given application depends on the user's specific board design.

## ■Typical Electrical and Thermal Characteristics Diagrams

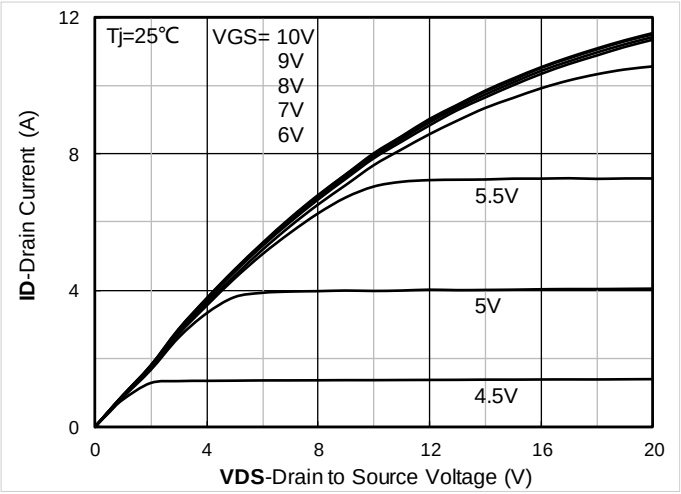



Figure 1. Output Characteristics

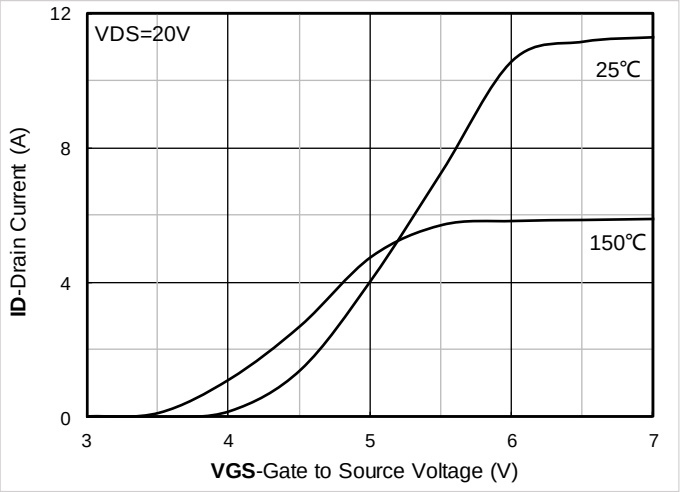



Figure 2. Transfer Characteristics

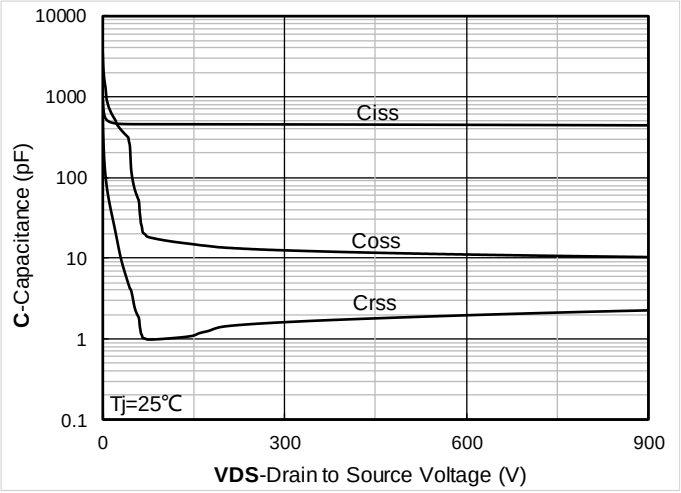



Figure 3. Capacitance Characteristics

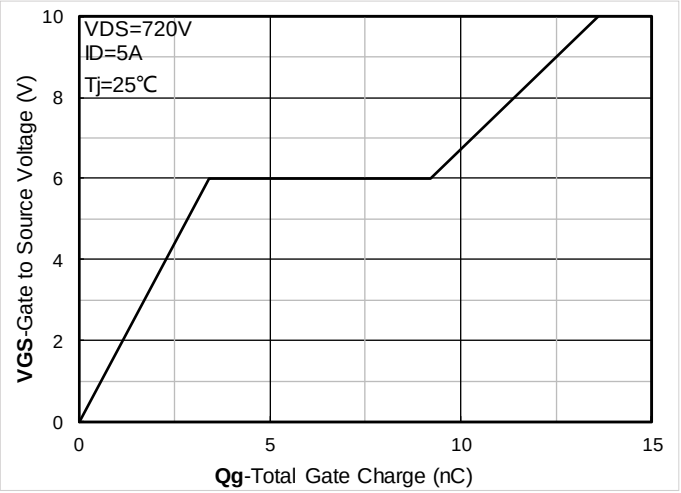



Figure 4. Gate Charge

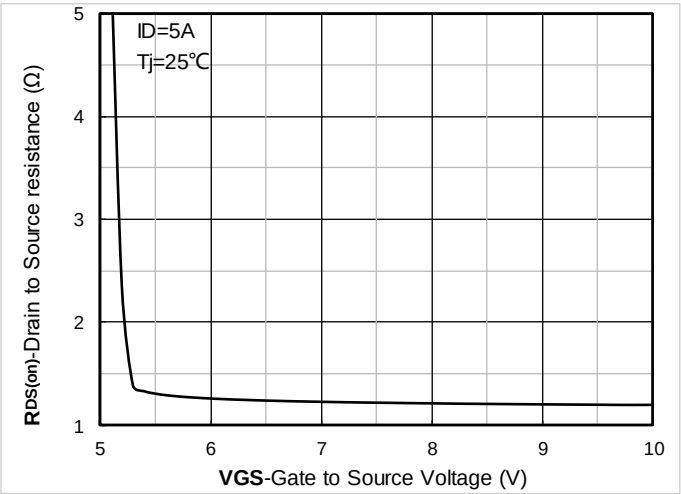



Figure 5. On-Resistance vs Gate to Source Voltage

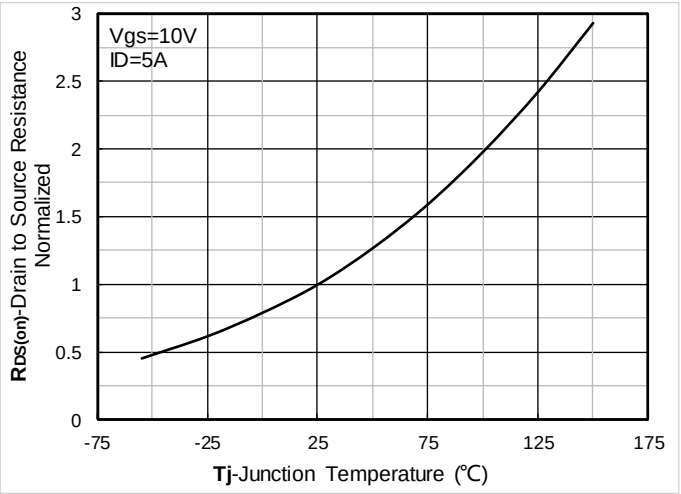



Figure 6. Normalized On-Resistance

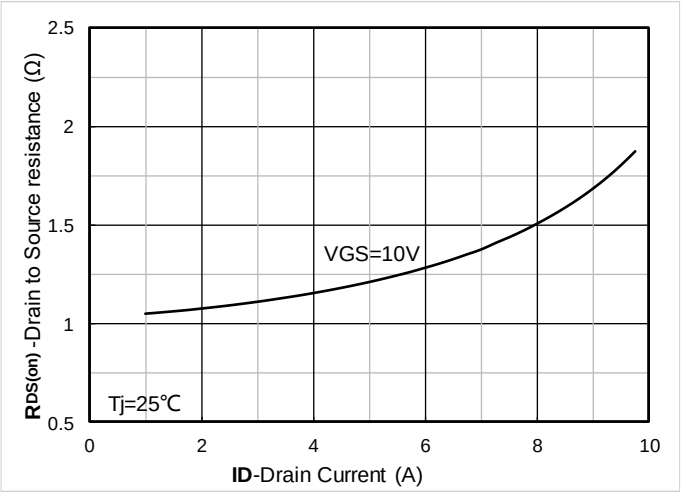



Figure 7. RDS(on) VS Drain Current

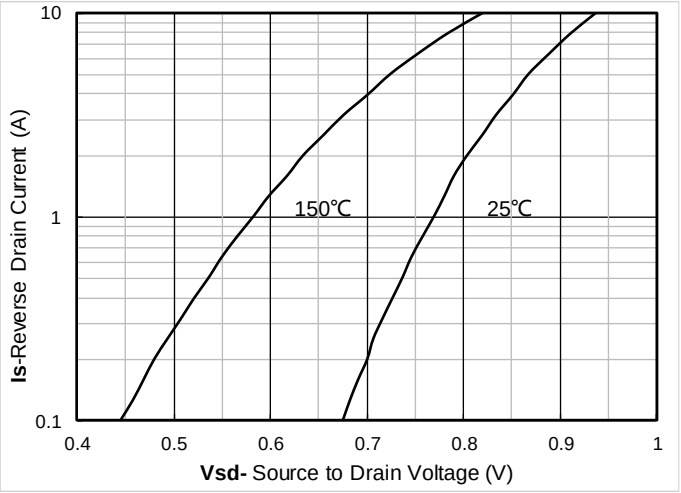



Figure 8. Forward characteristics of reverse diode

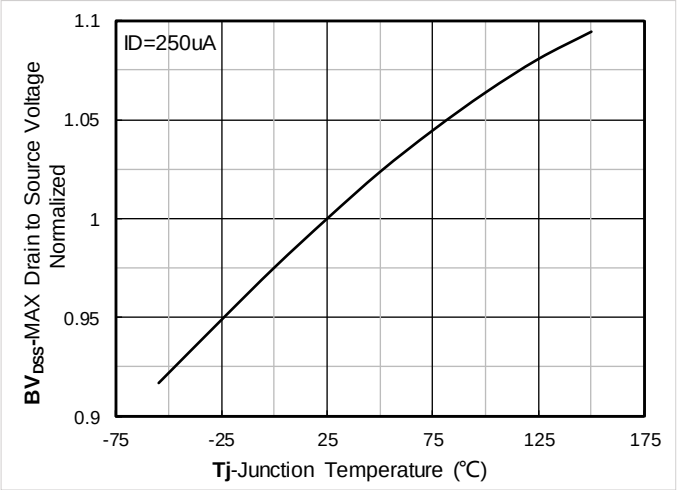



Figure 9. Normalized breakdown voltage

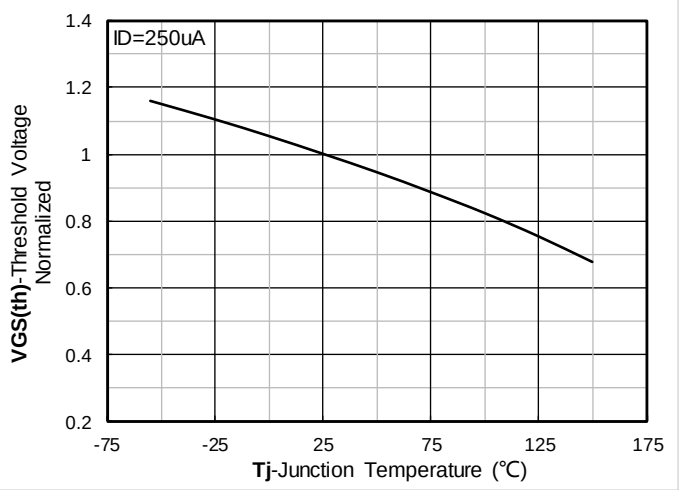



Figure 10. Normalized Threshold voltage

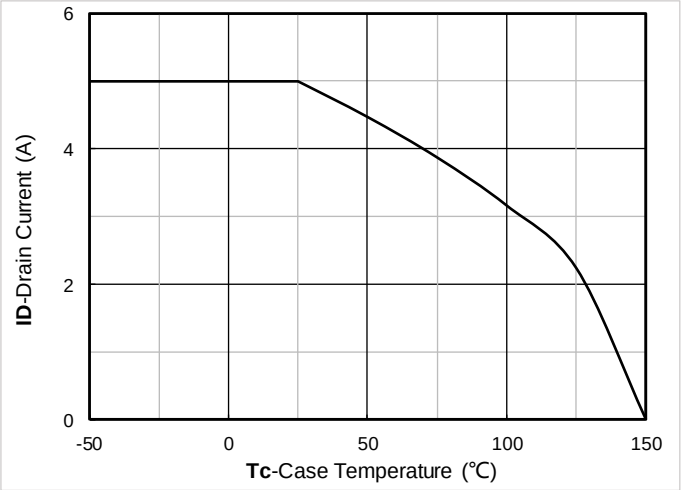



Figure 11. Current dissipation

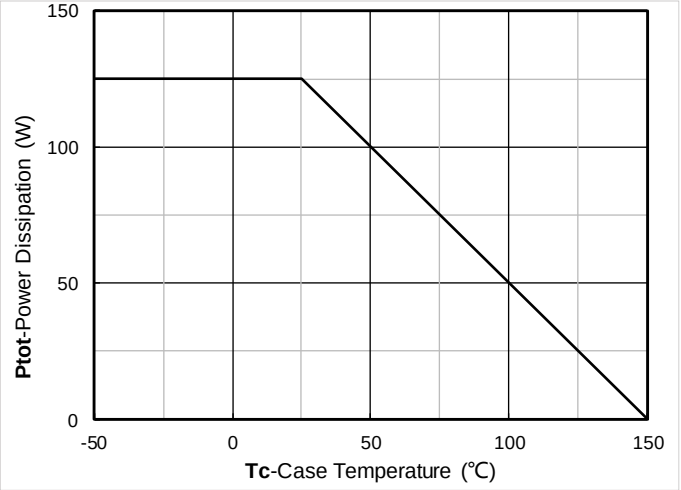



Figure 12. Power dissipation

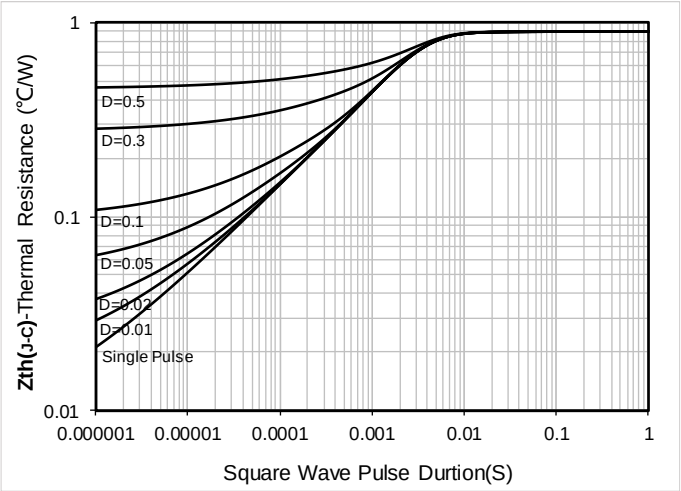



Figure 13. Maximum Transient Thermal Impedance

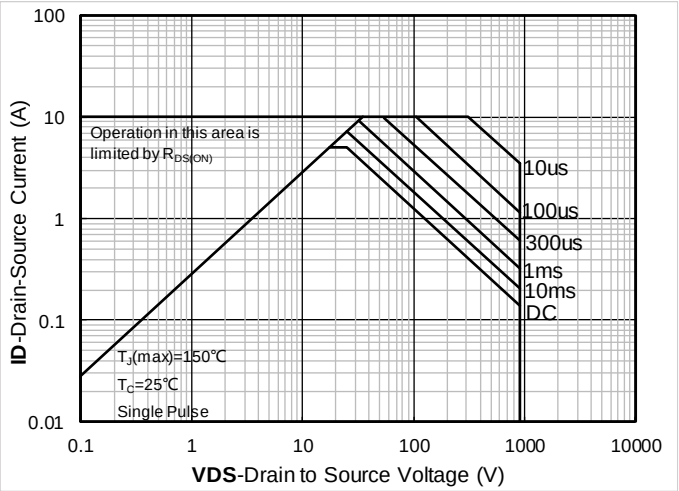



Figure 14. Safe Operation Area

## ■ Test Circuits & Waveforms

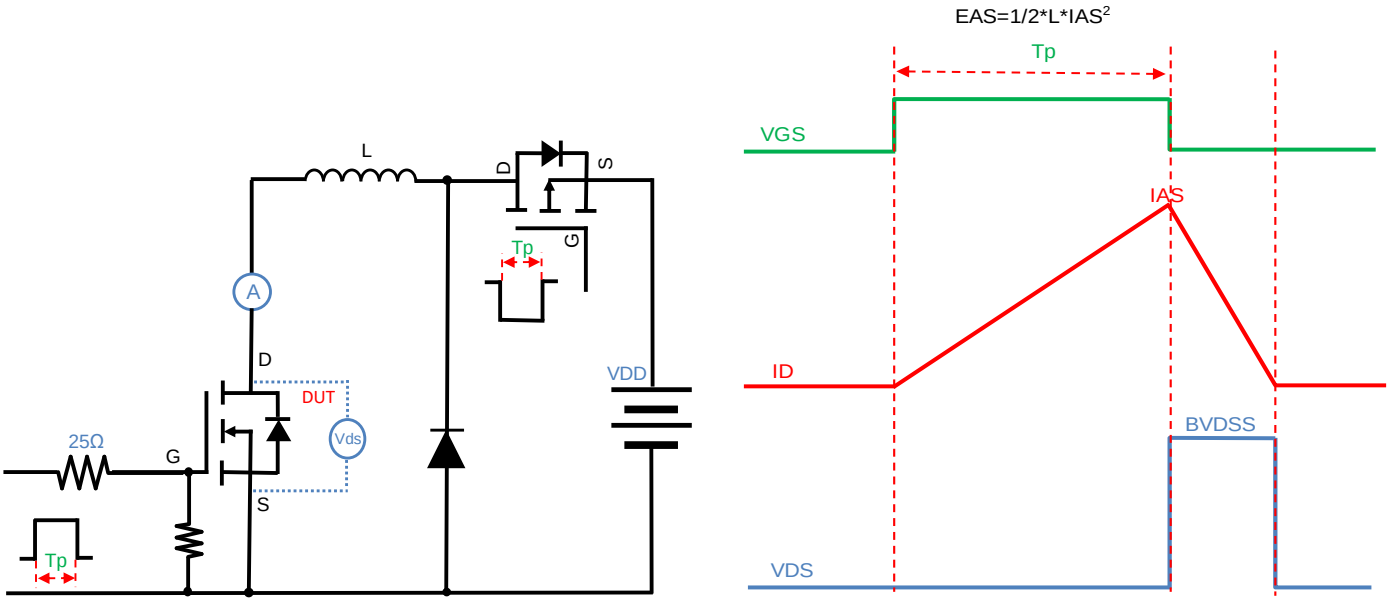



Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

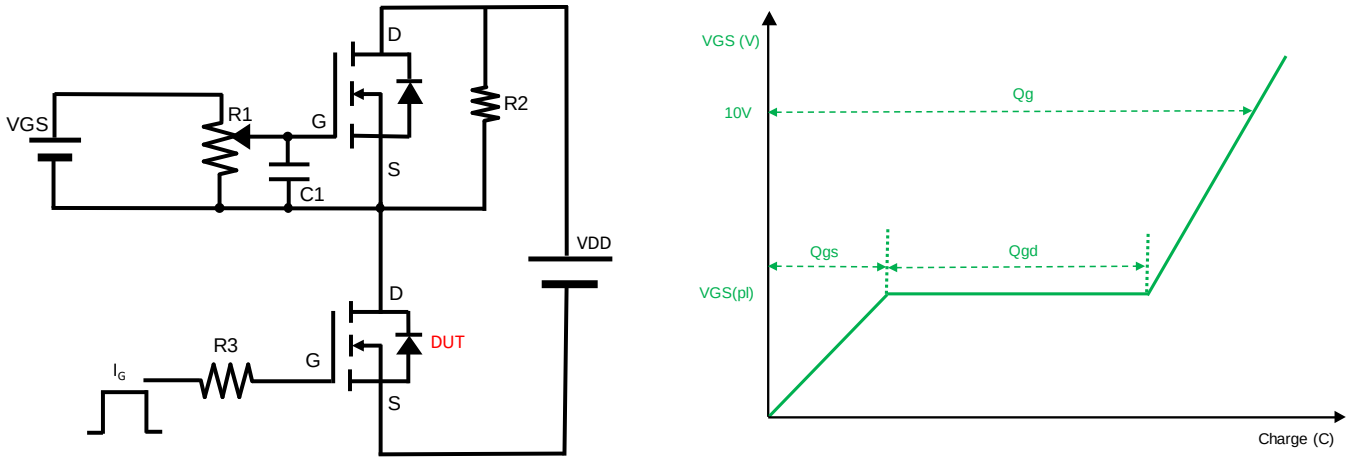



Figure B. Gate Charge Test Circuit & Waveform

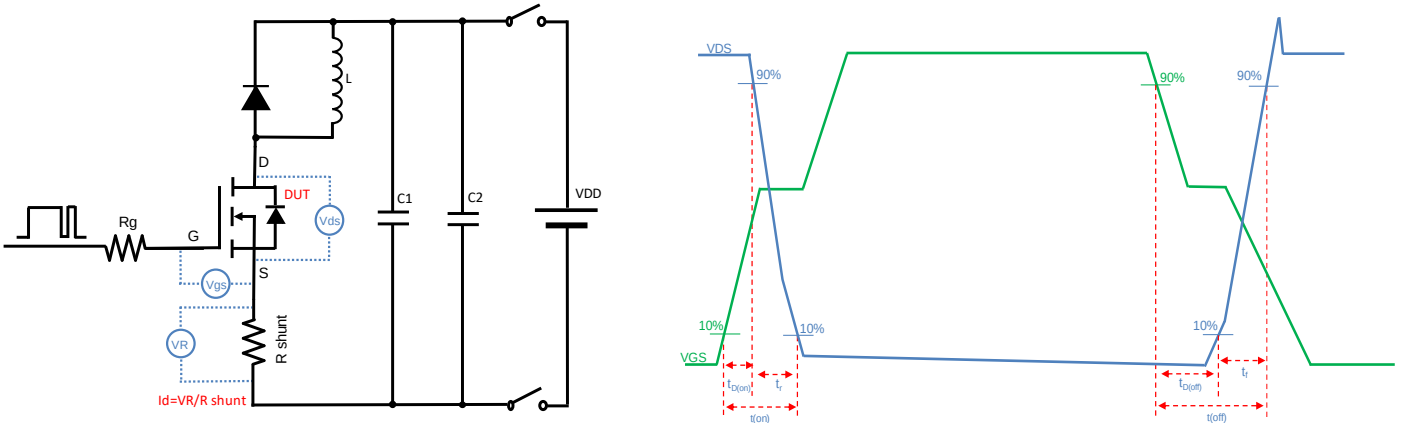



Figure C. Resistive Switching Test Circuit & Waveform

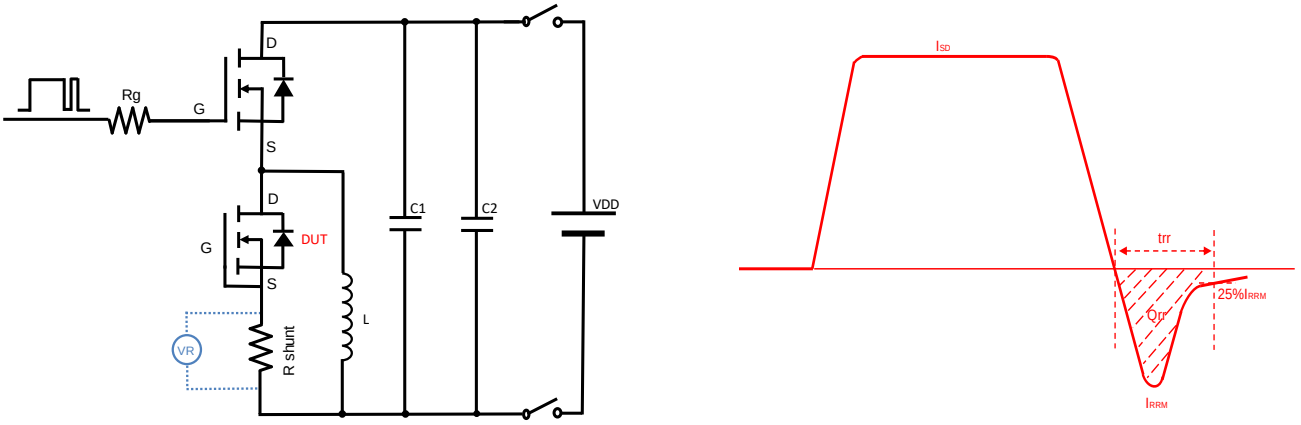
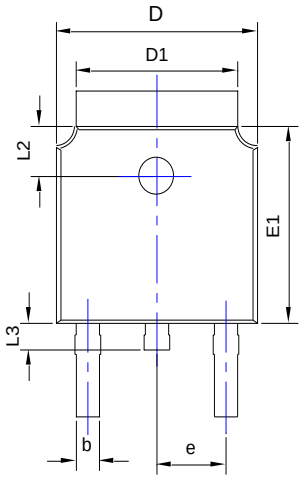
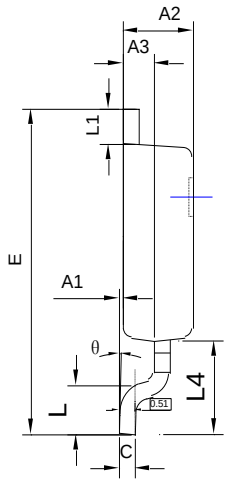



Figure D. Diode Recovery Test Circuit & Waveform

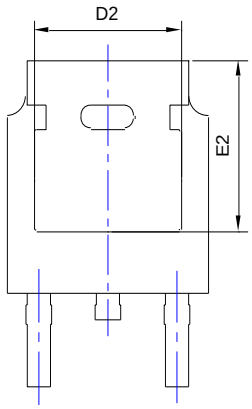
## ■ TO-252-B Package information



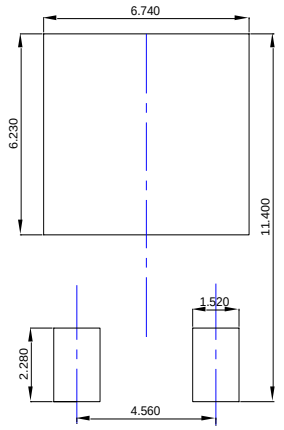

TOP VIEW




SIDE VIEW




BOTTOM VIEW




SUGGESTED SOLDER PAD LAYOUT

UNIT：mm

| DIMENSIONS | | | | | | |
|---|---|---|---|---|---|---|
| SYMBOL | INCHES | | | Millimeter | | |
| | MIN. | NOM. | MAX. | MIN. | NOM. | MAX. |
| A1 | 0.000 | --- | 0.008 | 0.000 | --- | 0.200 |
| A2 | 0.087 | 0.091 | 0.094 | 2.200 | 2.300 | 2.400 |
| A3 | 0.035 | 0.039 | 0.043 | 0.900 | 1.000 | 1.100 |
| b | 0.026 | 0.030 | 0.034 | 0.660 | 0.760 | 0.860 |
| c | 0.018 | 0.020 | 0.023 | 0.460 | 0.520 | 0.580 |
| D | 0.256 | 0.260 | 0.264 | 6.500 | 6.600 | 6.700 |
| D1 | 0.203 | 0.209 | 0.215 | 5.150 | 5.300 | 5.450 |
| D2 | 0.181 | 0.189 | 0.195 | 4.600 | 4.800 | 4.950 |
| E | 0.390 | 0.398 | 0.406 | 9.900 | 10.100 | 10.300 |
| E1 | 0.236 | 0.240 | 0.244 | 6.000 | 6.100 | 6.200 |
| E2 | 0.203 | 0.209 | 0.215 | 5.150 | 5.300 | 5.450 |
| e | 0.090BSC | | | 2.286BSC | | |
| L | 0.049 | 0.059 | 0.069 | 1.250 | 1.500 | 1.750 |
| L1 | 0.035 | --- | 0.050 | 0.900 | --- | 1.270 |
| L2 | 0.055 | --- | 0.075 | 1.400 | --- | 1.900 |
| L3 | 0.024 | 0.031 | 0.039 | 0.600 | 0.800 | 1.000 |
| L4 | 0.114REF | | | 2.900REF | | |
| θ | 0° | --- | 10° | 0° | --- | 10° |

NOTE:
1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

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