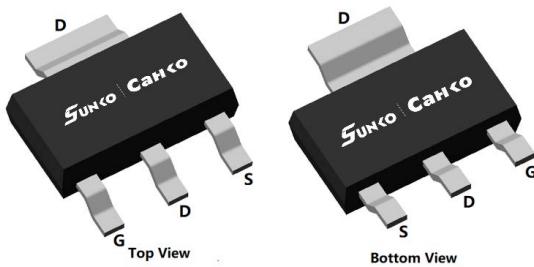
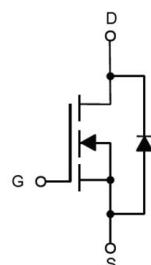


## P-Channel Enhancement Mode Field Effect Transistor



SOT-223



### Product Summary

• $V_{DS}$	-60 V
• $I_D$	-5 A
• $R_{DS(ON)}$ (at $V_{GS}=-10V$ )	<55 mΩ
• $R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	<70 mΩ

### General Description

- Split gate trench MOSFET technology
- Extremely low switching loss
- Excellent stability and uniformity
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

### Applications

- Power management
- Load switch

#### ■ Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		$V_{DS}$	-60	V
Gate-source Voltage		$V_{GS}$	$\pm 20$	V
Drain Current	$T_A=25^\circ\text{C}$	$I_D$	-5	A
	$T_A=100^\circ\text{C}$		-3	
Pulsed Drain Current <sup>A</sup>		$I_{DM}$	-25	A
Total Power Dissipation <sup>B</sup>	$T_A=25^\circ\text{C}$	$P_D$	1	W
	$T_A=100^\circ\text{C}$		0.4	
Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~+150	°C

#### ■ Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient <sup>C</sup>	Steady-State	$R_{\theta JA}$	100	120	°C/W

#### ■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
SCM05GP06A	F2	05GP06	2500	5000	25000	13" reel

■ Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-60	-	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
		$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}, T_J=150^\circ\text{C}$	-	-	-100	
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.5	-2	-3	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-5\text{A}$	-	40	55	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4\text{A}$	-	50	70	
Diode Forward Voltage	$V_{\text{SD}}$	$I_{\text{S}}=-5\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.9	-1.2	V
Gate resistance	$R_{\text{G}}$	f=1MHz, Open drain	-	12	-	$\Omega$
Maximum Body-Diode Continuous Current	$I_{\text{S}}$		-	-	-5	A
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	1050	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	380	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	20	-	
<b>Switching Parameters</b>						
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-5\text{A}$	-	18.7	-	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		-	4.7	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	3	-	
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_{\text{F}}=-5\text{A}, \text{di}/\text{dt}=100\text{A}/\text{us}$	-	8	-	$\text{nC}$
Reverse Recovery Time	$t_{\text{rr}}$		-	20	-	ns
Turn-on Delay Time	$t_{\text{D}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DD}}=-30\text{V}, I_{\text{D}}=-5\text{A}$ $\text{RGEN}=2.2\Omega$	-	7.5	-	ns
Turn-on Rise Time	$t_{\text{r}}$		-	40	-	
Turn-off Delay Time	$t_{\text{D}(\text{off})}$		-	43	-	
Turn-off fall Time	$t_{\text{f}}$		-	55	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B.  $P_d$  is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.C. The value of  $R_{\theta JA}$  is measured with the device mounted on the minimum recommend pad size, in the still air environment with  $TA = 25^\circ\text{C}$ . The maximum allowed junction temperature of  $150^\circ\text{C}$ . The value in any given application depends on the user's specific board design.

## ■ Typical Electrical and Thermal Characteristics Diagrams

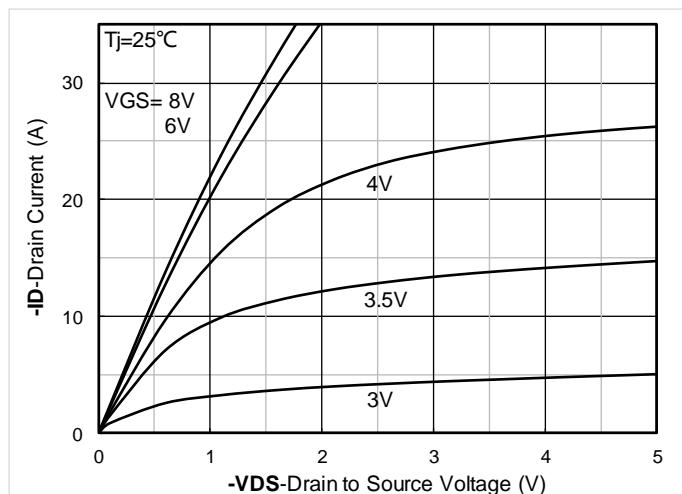


Figure 1. Output Characteristics

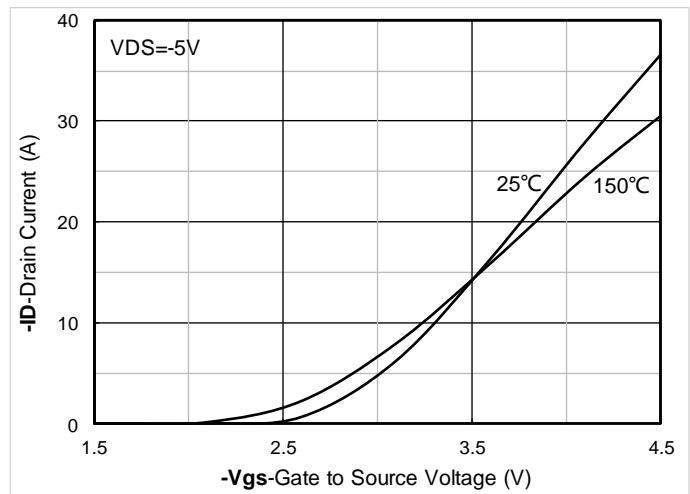


Figure 2. Transfer Characteristics

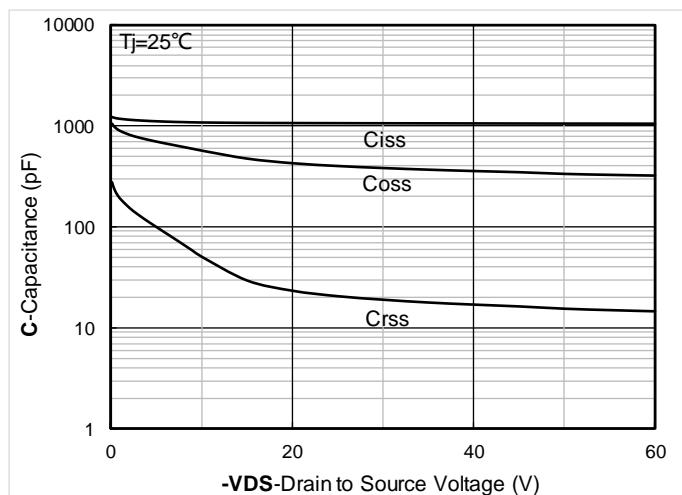


Figure 3. Capacitance Characteristics

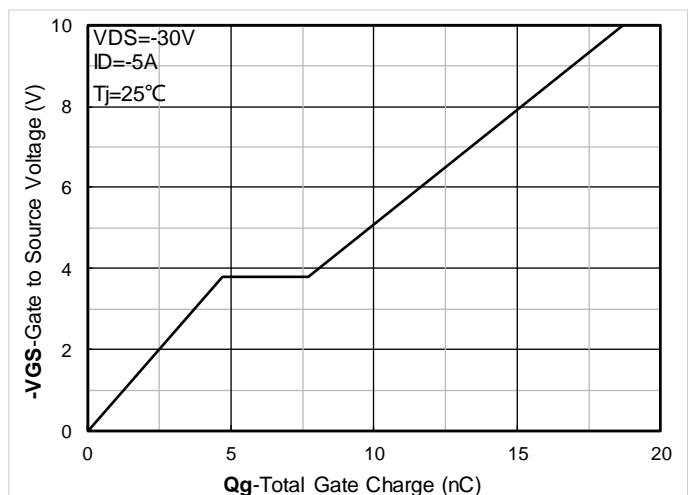


Figure 4. Gate Charge

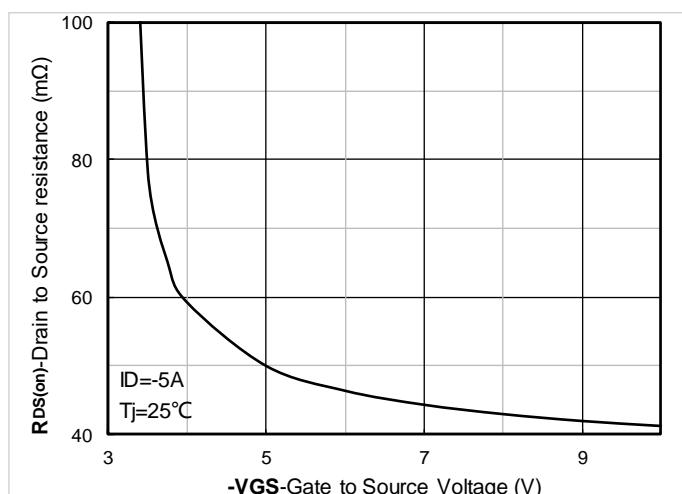


Figure 5. On-Resistance vs Gate to Source Voltage

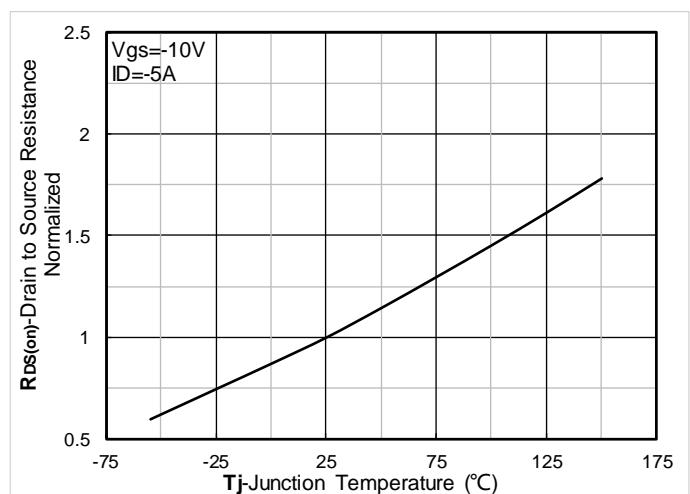


Figure 6. Normalized On-Resistance

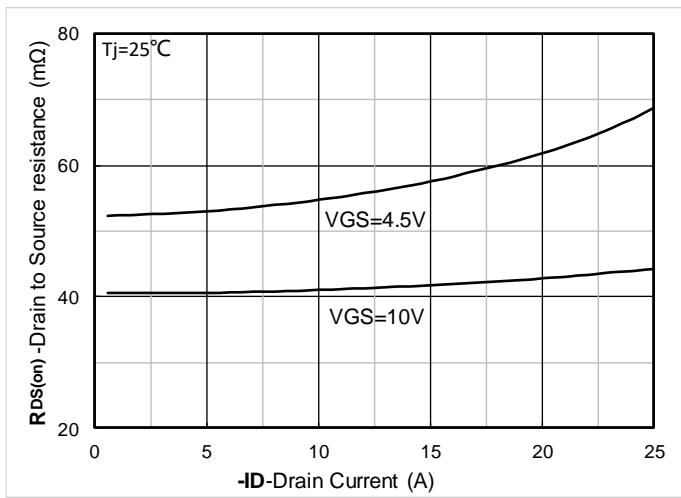


Figure 7. RDS(on) VS Drain Current

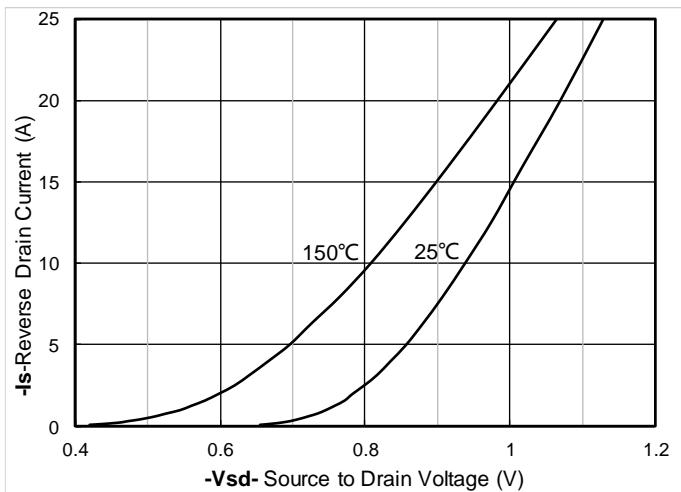


Figure 8. Forward characteristics of reverse diode

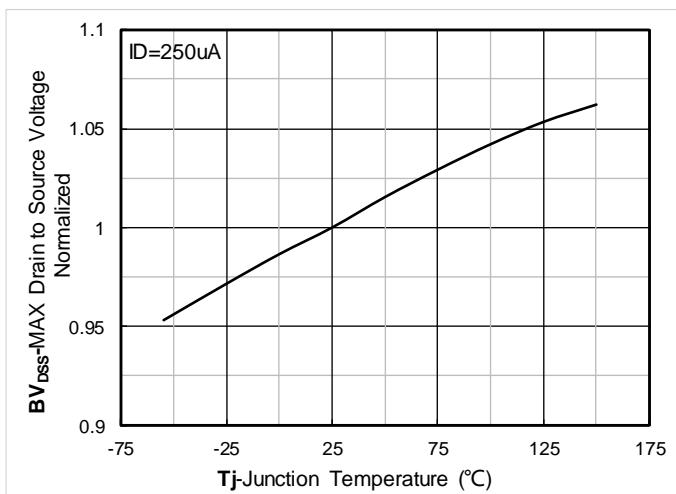


Figure 9. Normalized breakdown voltage

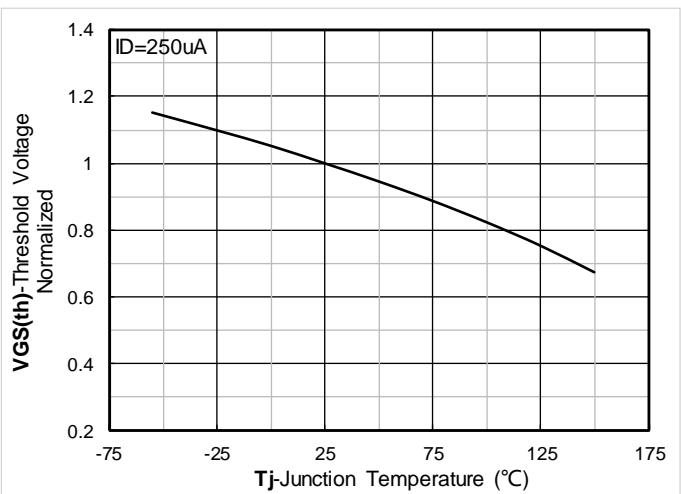


Figure 10. Normalized Threshold voltage

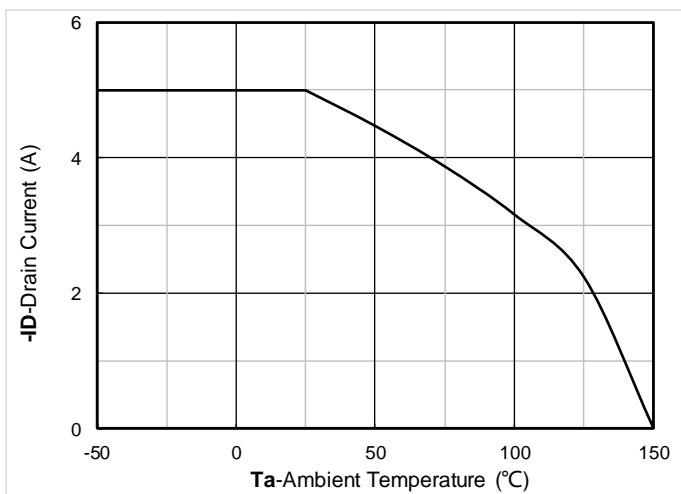


Figure 11. Current dissipation

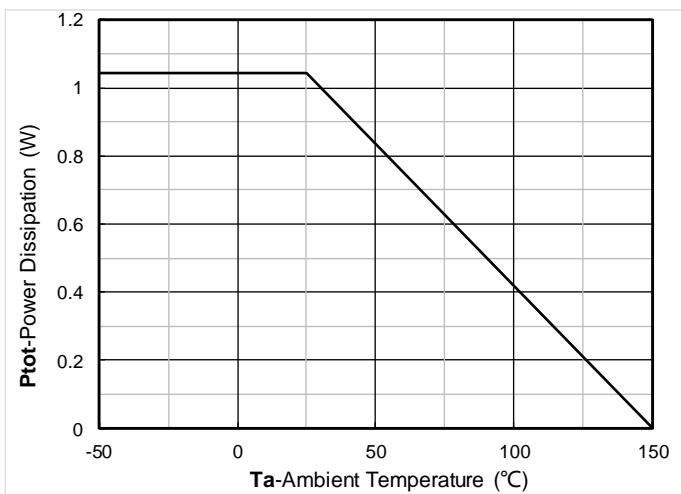


Figure 12. Power dissipation

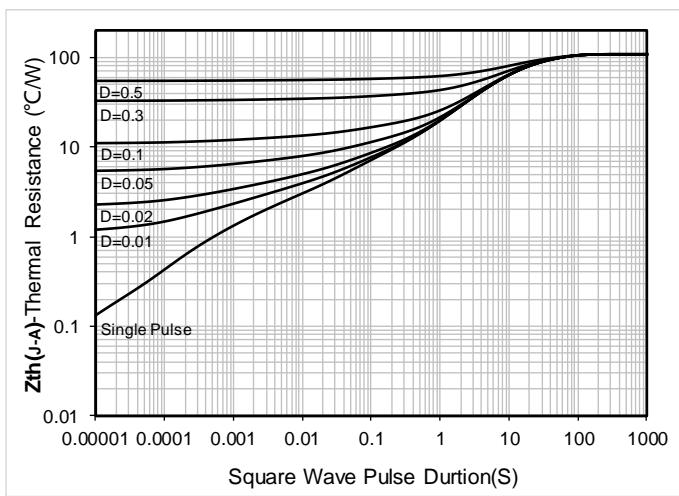


Figure 13. Maximum Transient Thermal Impedance

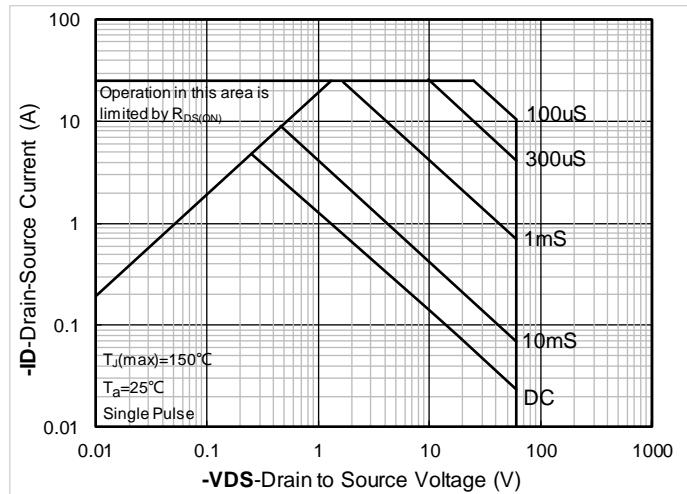
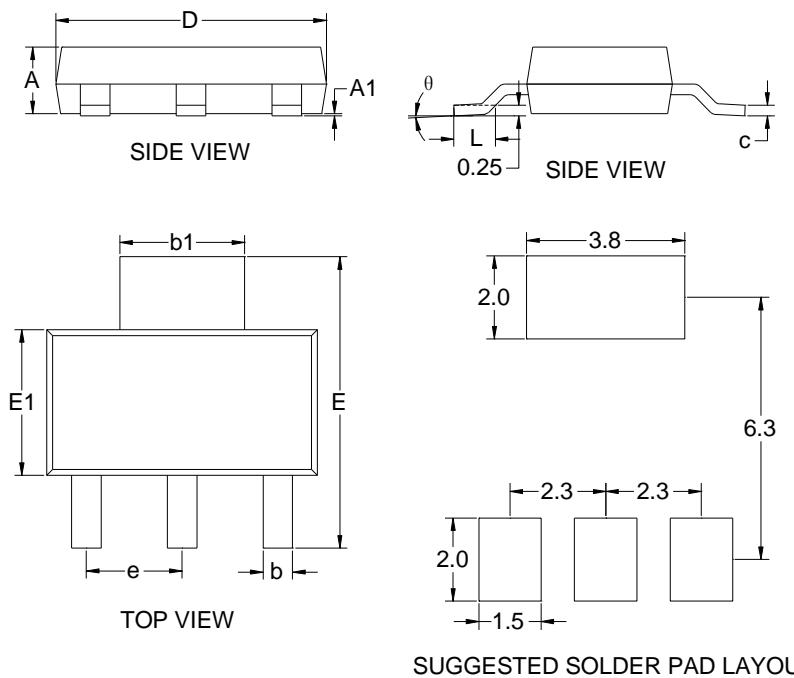


Figure 14. Safe Operation Area

## ■ SOT-223 Package Information



DIM	DIMENSIONS			
	INCHES		MM	
	MIN	MAX	MIN	MAX
A	0.0591	0.0670	1.5000	1.7000
A1	0.0008	0.0039	0.0200	0.1000
b	0.0259	0.0330	0.6600	0.8400
b1	0.1140	0.1220	2.9000	3.1000
c	0.0090	0.0138	0.2300	0.3500
D	0.2480	0.2640	6.3000	6.7000
E	0.2637	0.2874	6.7000	7.3000
E1	0.1290	0.1460	3.3000	3.7000
e	0.0866	0.0945	2.2000	2.4000
L	0.0295	0.0492	0.7500	1.2500
θ	0°	10°	0°	10°

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