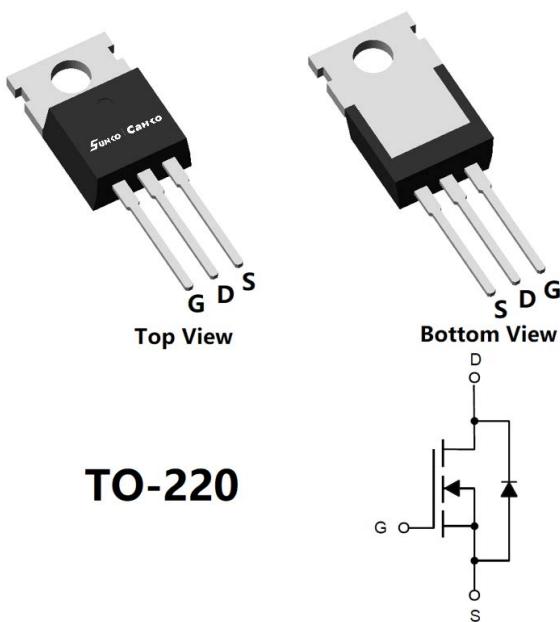


P-Channel Enhancement Mode Field Effect Transistor



Product Summary

• V_{DS}	-100V
• I_D	-23A
• $R_{DS(ON)}$ (at $V_{GS}=-10V$)	<85mΩ
• $R_{DS(ON)}$ (at $V_{GS}=-4.5V$)	<100mΩ
• 100% EAS Tested	
• 100% ∇V_{DS} Tested	

General Description

- Trench Power MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Load switch
- Motor drive control
- DC-DC convertor

Absolute Maximum Ratings ($T_J=25^\circ C$ unless otherwise noted)

Parameter			Symbol	Limit	Unit
Drain-source Voltage			V_{DS}	-100	V
Gate-source Voltage			V_{GS}	± 20	V
Continuous Drain Current (Note 1,2)	Steady-State	$T_A=25^\circ C, V_{GS} = -10V$	I_D	-4.5	A
		$T_A=100^\circ C, V_{GS} = -10V$		-2.8	
Continuous Drain Current (Note 1,3)	Steady-State	$T_C=25^\circ C, V_{GS} = -10V$		-23	
		$T_C = 100^\circ C, V_{GS} = -10V$		-14.5	
Pulsed Drain Current	$T_C=25^\circ C, t_p=100\mu s$		I_{DM}	-65	A
Avalanche energy	$V_G=-10V, R_G=25\Omega, L=0.5mH, IAS=-14.3A$		EAS	51.1	mJ
Total Power Dissipation (Note 1,2)	Steady-State	$T_A=25^\circ C$	P_D	3.5	W
		$T_A=100^\circ C$		1.4	
Total Power Dissipation (Note 1,3)	Steady-State	$T_C=25^\circ C$		125	
		$T_C = 100^\circ C$		50	
Junction and Storage Temperature Range			T_J, T_{STG}	-55~+150	°C

Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	Steady-State	$R_{\theta JA}$	-	35	°C/W
Thermal Resistance Junction-to-Case	Steady-State	$R_{\theta JC}$	-	1	

Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
SCP085P10A	B1	SCP085P10A	50	/	5000	Tube

■ Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-100\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
		$V_{\text{DS}}=-100\text{V}, V_{\text{GS}}=0\text{V}, T_J=150^\circ\text{C}$	-	-	-100	
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	±100	nA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.5	-2	-2.5	V
Static Drain-Source On-Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-10\text{A}$	-	65	85	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-5\text{A}$	-	72	100	
Diode Forward Voltage	V_{SD}	$I_{\text{S}}=-10\text{A}, V_{\text{GS}}=0\text{V}$	-	-	-1.2	V
Gate resistance	R_{G}	$f=1\text{MHz}$	-	10	-	Ω
Maximum Body-Diode Continuous Current	I_{S}		-	-	-23	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-50\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	2065	-	pF
Output Capacitance	C_{oss}		-	90	-	
Reverse Transfer Capacitance	C_{rss}		-	72	-	
Switching Parameters						
Total Gate Charge	Q_{g}	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-50\text{V}, I_{\text{D}}=-10\text{A}$	-	44.4	-	nC
Gate-Source Charge	Q_{gs}		-	4.7	-	
Gate-Drain Charge	Q_{gd}		-	5.5	-	
Reverse Recovery Charge	Q_{rr}	$I_{\text{F}}=-10\text{A}, \text{di/dt}=100\text{A/us}$	-	45	-	nC
Reverse Recovery Time	t_{rr}		-	30	-	ns
Turn-on Delay Time	$t_{\text{D(on)}}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DD}}=-50\text{V}, I_{\text{D}}=-10\text{A}$ $R_{\text{GEN}}=3\Omega$	-	9	-	ns
Turn-on Rise Time	t_{r}		-	42	-	
Turn-off Delay Time	$t_{\text{D(off)}}$		-	91	-	
Turn-off fall Time	t_{f}		-	31	-	

Note:

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- The value of R_{thJA} is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A = 25^\circ\text{C}$. The maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.
- Thermal resistance from junction to soldering point (on the exposed drain pad).

■ Typical Electrical and Thermal Characteristics Diagrams

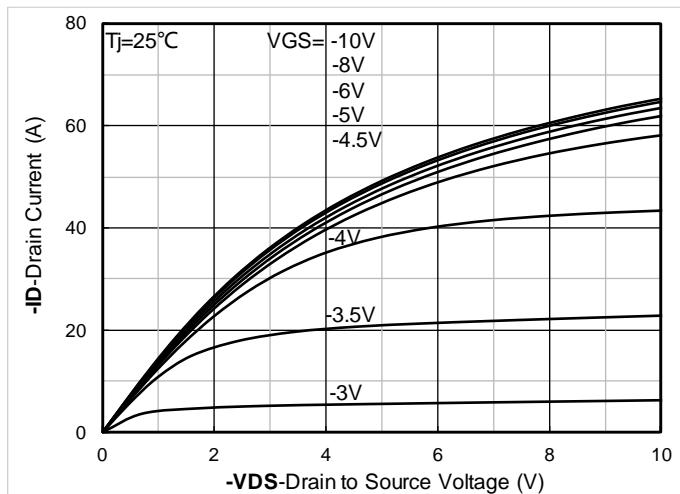


Figure 1. Output Characteristics

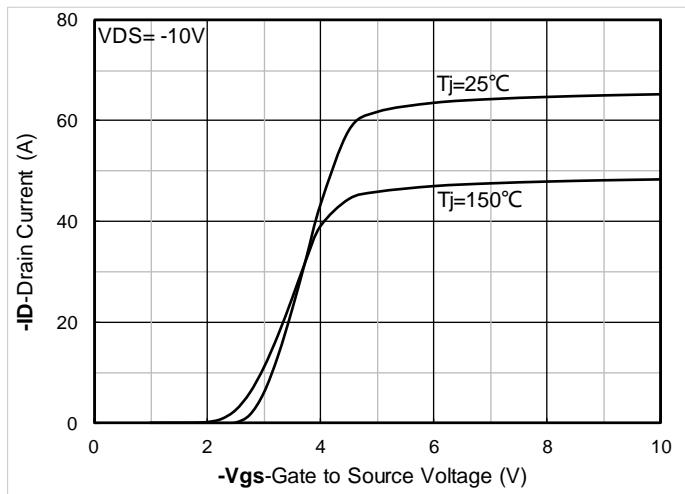


Figure 2. Transfer Characteristics

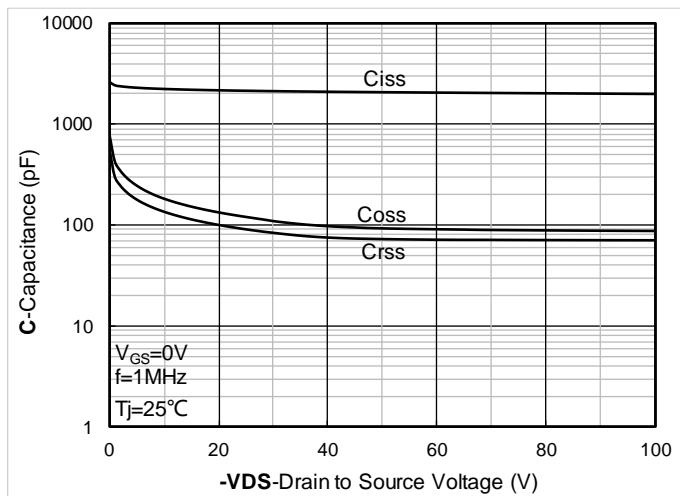


Figure 3. Capacitance Characteristics

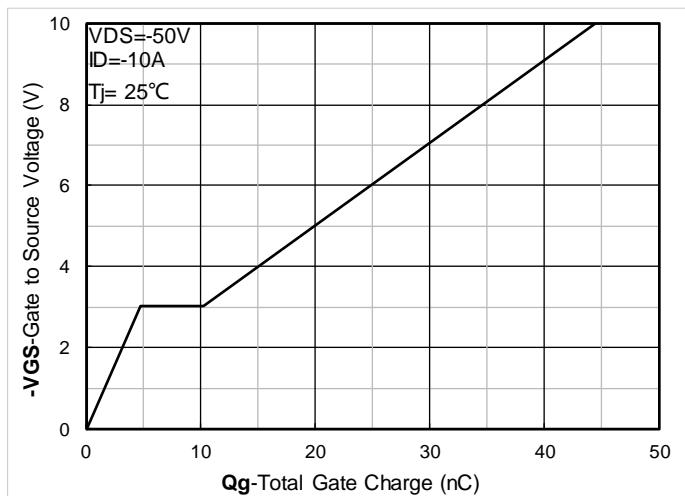


Figure 4. Gate Charge

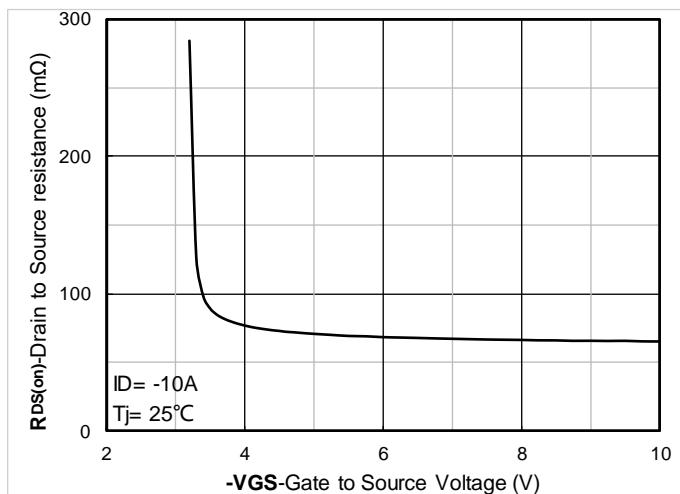


Figure 5. On-Resistance vs Gate to Source Voltage

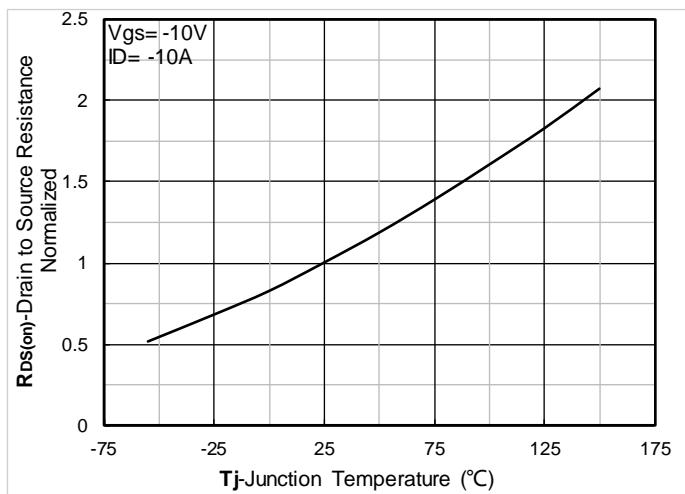


Figure 6. Normalized On-Resistance

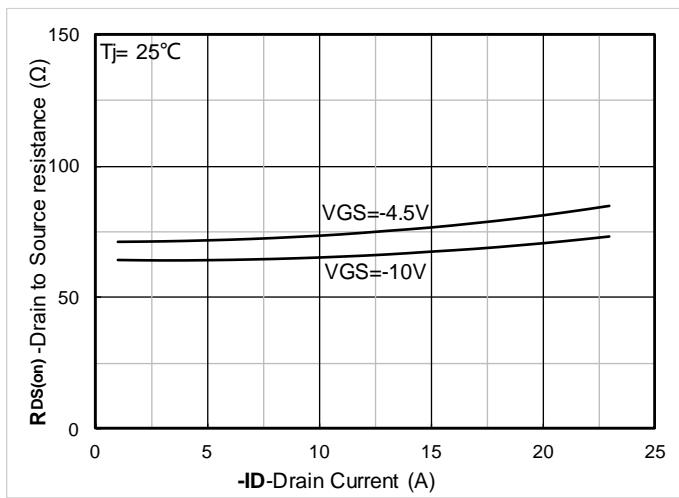


Figure 7. RDS(on) VS Drain Current

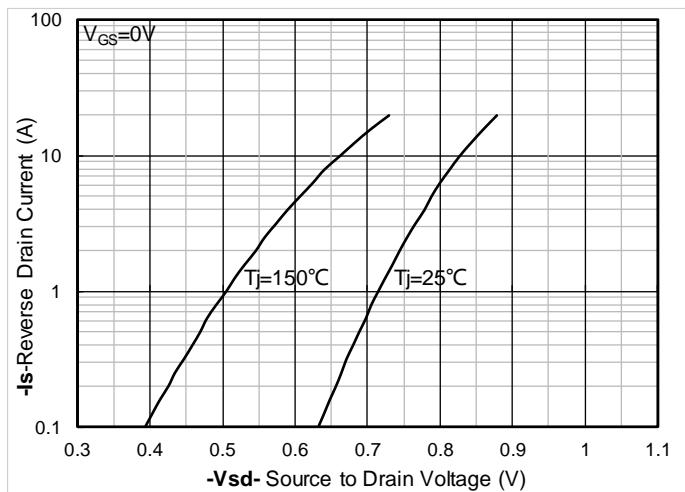


Figure 8. Forward characteristics of reverse diode

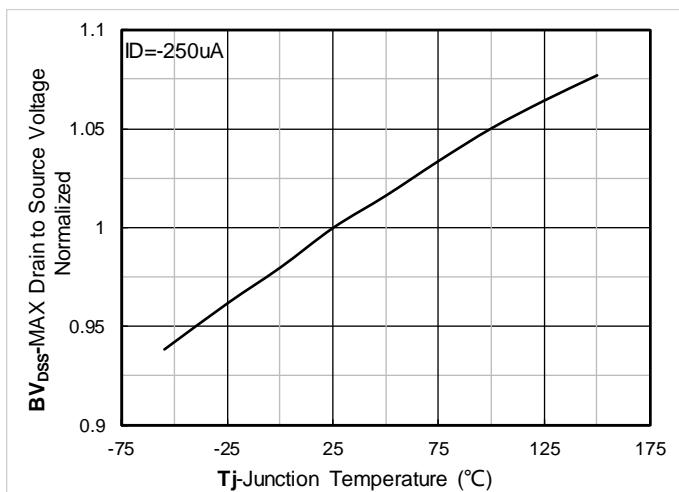


Figure 9. Normalized breakdown voltage

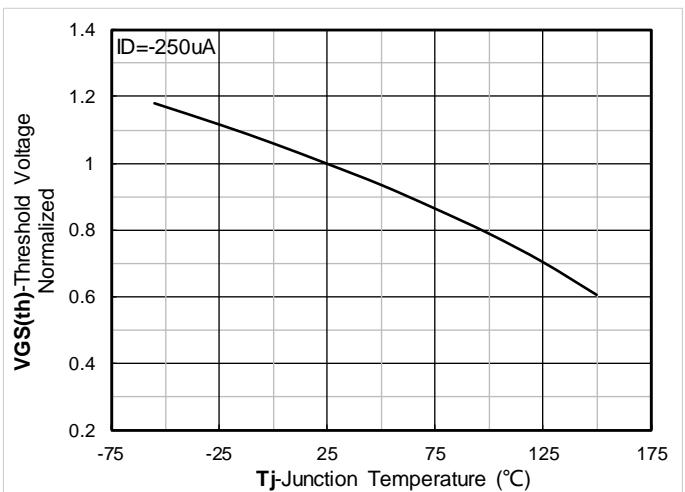


Figure 10. Normalized Threshold voltage

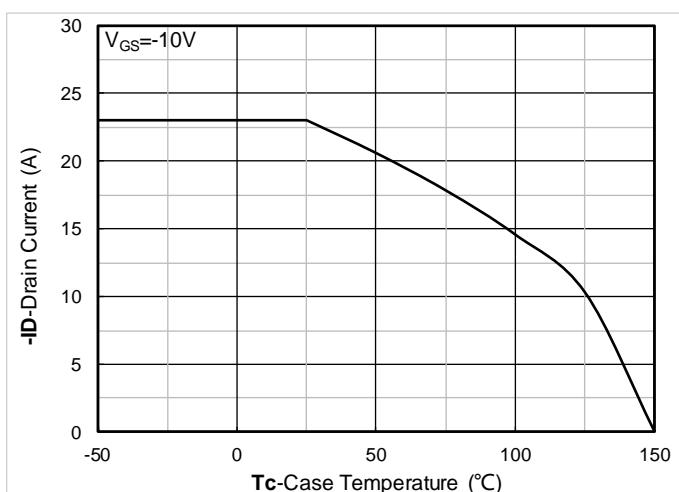


Figure 11. Current dissipation

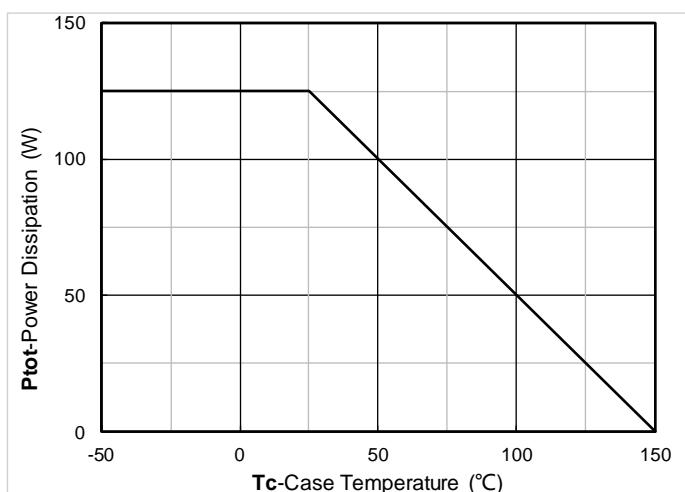


Figure 12. Power dissipation

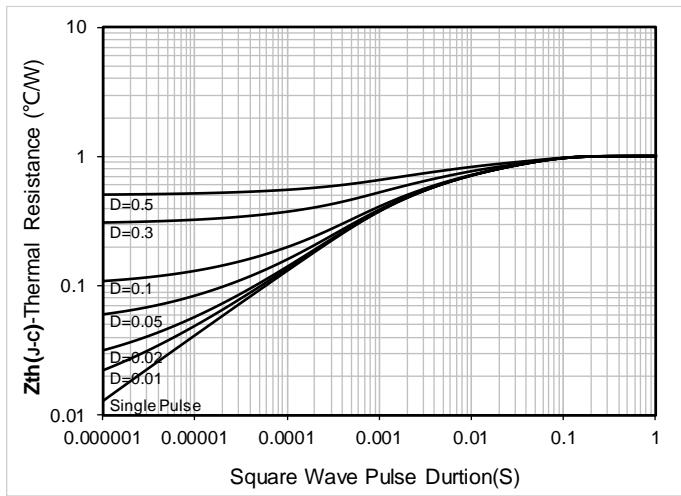


Figure 13. Maximum Transient Thermal Impedance

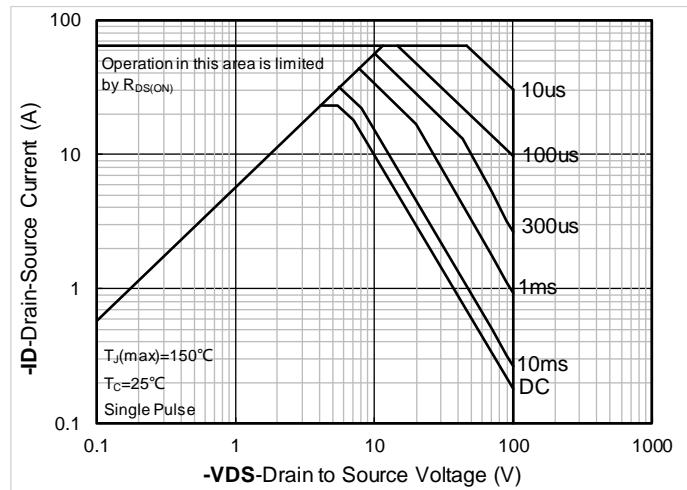
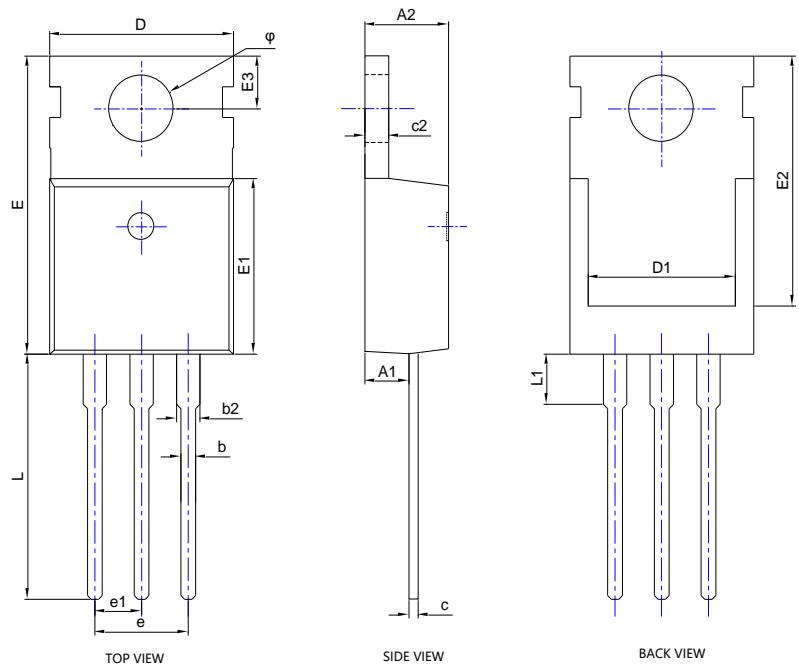


Figure 14. Safe Operation Area

■ TO-220AB-E Package information



SYMBOL	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A1	0.093	0.114	2.350	2.900
A2	0.176	0.184	4.470	4.670
b	0.028	0.036	0.710	0.910
b2	0.048	0.054	1.220	1.360
c	0.019	0.024	0.470	0.600
c2	0.047	0.055	1.200	1.400
D	0.382	0.408	9.700	10.370
D1	0.276	0.350	7.000	8.890
E	0.579	0.622	14.700	15.800
E1	0.350	0.373	8.900	9.470
E2	0.463	0.535	11.750	13.600
E3	0.108BSC		2.740BSC	
e	0.200BSC		5.080BSC	
e1	0.100BSC		2.540BSC	
L	0.508	0.583	12.900	14.800
L1	0.100	0.151	2.540	3.840
φ	0.142	0.154	3.600	3.900

NOTE:
1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.

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