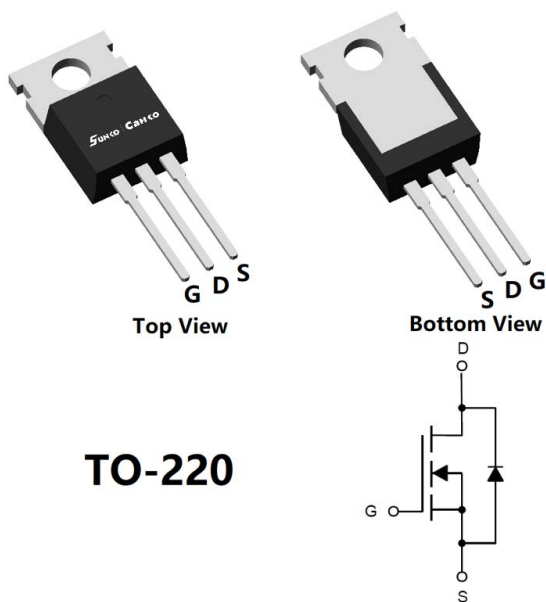


N-Channel Enhancement Mode Field Effect Transistor



Product Summary

- V_{DS} 100V
- I_D 70A
- $R_{DS(ON)}$ (at $V_{GS}=10V$) $<8.6\text{ mohm}$
- $R_{DS(ON)}$ (at $V_{GS}=4.5V$) $<11\text{ mohm}$
- 100% EAS Tested
- 100% ∇V_{DS} Tested

General Description

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

■ Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	100	V
Gate-source Voltage		V_{GS}	± 20	V
Drain Current	$T_C=25^\circ\text{C}$	I_D	70	A
	$T_C=100^\circ\text{C}$		44.2	
Pulsed Drain Current ^A		I_{DM}	280	A
Avalanche energy ^B		EAS	200	mJ
Total Power Dissipation ^C	$T_C=25^\circ\text{C}$	P_D	125	W
	$T_C=100^\circ\text{C}$		50	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 ~ +150	$^\circ\text{C}$

■ Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient ^D	$t \leq 10S$	$R_{\theta JA}$	12	15	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Ambient ^D	Steady-State		50	60	
Thermal Resistance Junction-to-Case	Steady-State	$R_{\theta JC}$	0.8	1.0	

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
SCP70G10A	B1	SCP70G10A	50	/	5000	Tube

■ Electrical Characteristics (T_j=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100,V _{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} =0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	1	1.8	3	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D =20A		7.2	8.6	mΩ
		V _{GS} = 4.5V, I _D =20A		8.8	11	mΩ
Diode Forward Voltage	V _{SD}	I _S =20A,V _{GS} =0V			1.3	V
Maximum Body-Diode Continuous Current	I _S				70	A
Gate resistance	R _G	f= 1 MHz, Open drain		0.68		Ω
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =50V,V _{GS} =0V,f=1MHZ		2270		pF
Output Capacitance	C _{oss}			797		
Reverse Transfer Capacitance	C _{rss}			36		
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =10V,V _{DS} =50V,I _D =25A		32		nC
Gate-Source Charge	Q _{gs}			11.1		
Gate-Drain Charge	Q _{gd}			4.78		
Reverse Recovery Chrage	Q _{rr}	I _F =20A, di/dt=100A/us		84		ns
Reverse Recovery Time	t _{rr}			51.5		
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DD} =50V,I _D =25A R _{GEN} =2.2Ω		51		
Turn-on Rise Time	t _r			14.4		
Turn-off Delay Time	t _{D(off)}			69.2		
Turn-off fall Time	t _f			20.6		

A. Repetitive rating; pulse width limited by max. junction temperature.

B. V_{DD}=50V, R_G=25Ω, L=2mH, I_{AS}=14.2A

C. P_d is based on max. junction temperature, using junction-case thermal resistance.

D. The value of R_{θJA} is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The Power dissipation P_{DSM} is based on R_{θJA} ≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

■ Typical Performance Characteristics

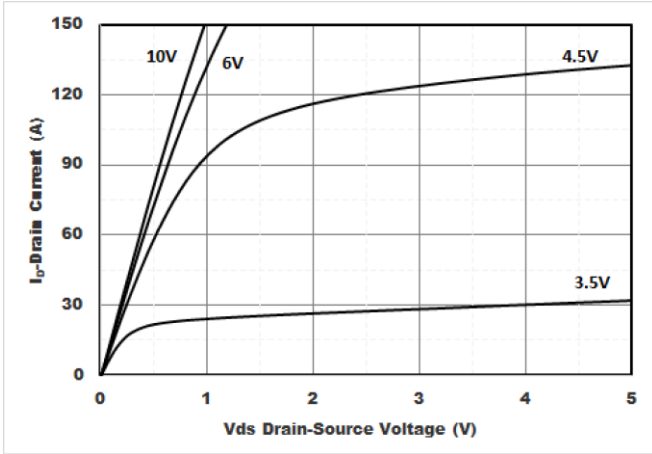


Figure1. Output Characteristics

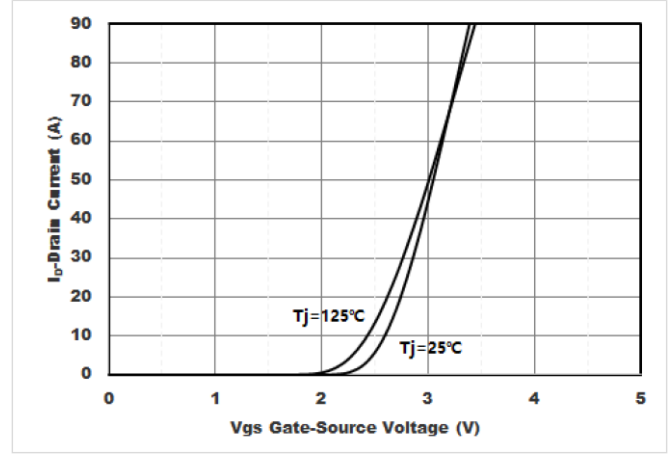


Figure2. Transfer Characteristics

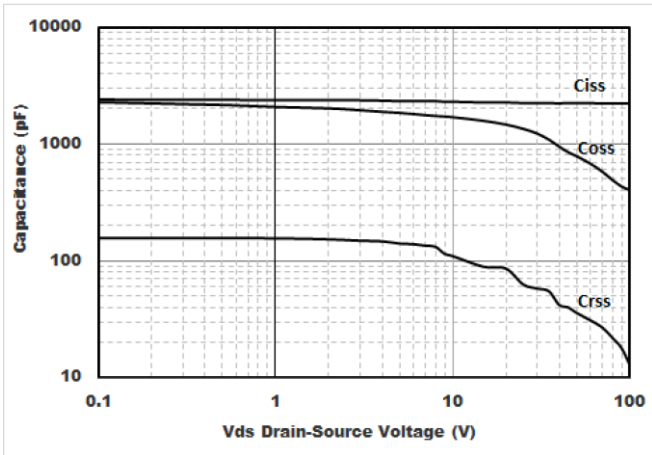


Figure3. Capacitance Characteristics

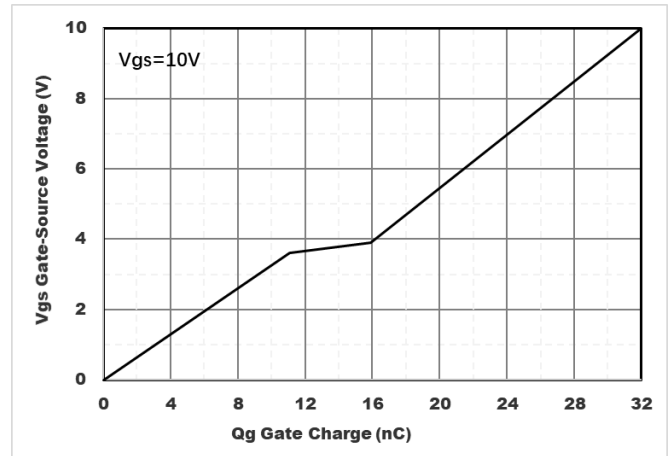


Figure4. Gate Charge

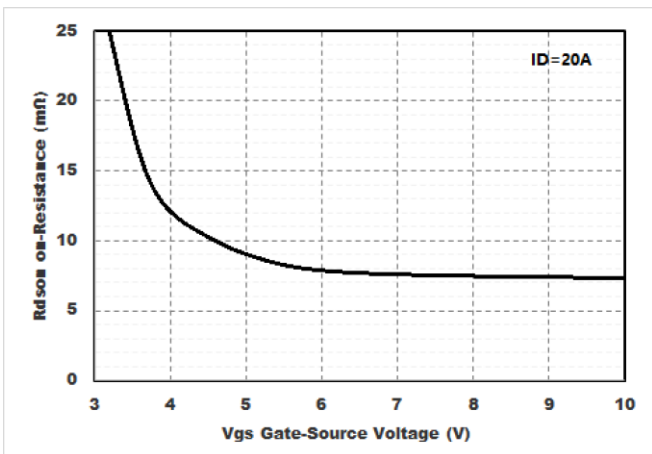


Figure5. On-Resistance vs. Drain Current and Gate Voltage

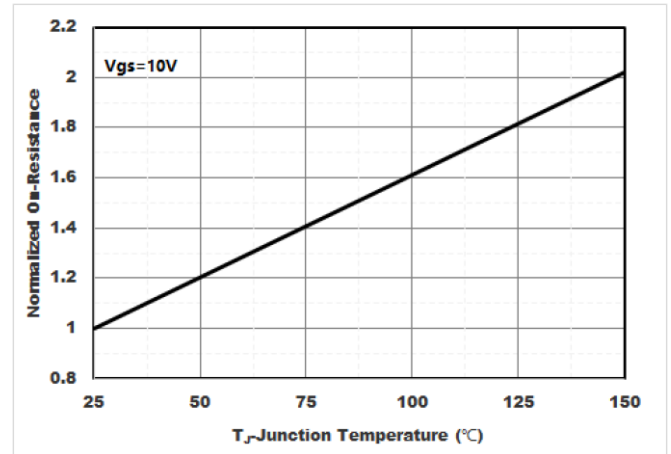


Figure6. Normalized On-Resistance

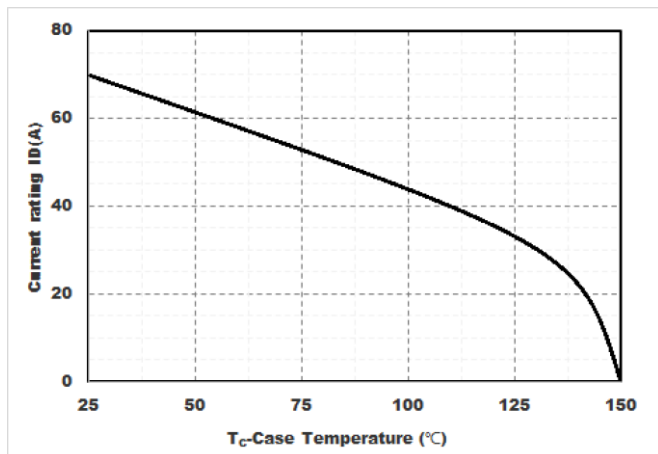


Figure7. Drain current

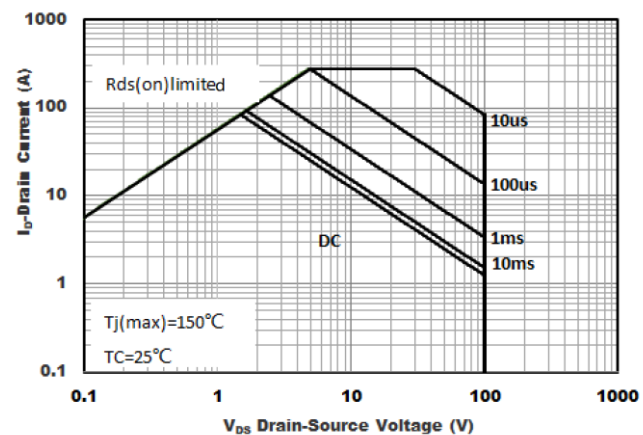


Figure8. Safe Operation Area

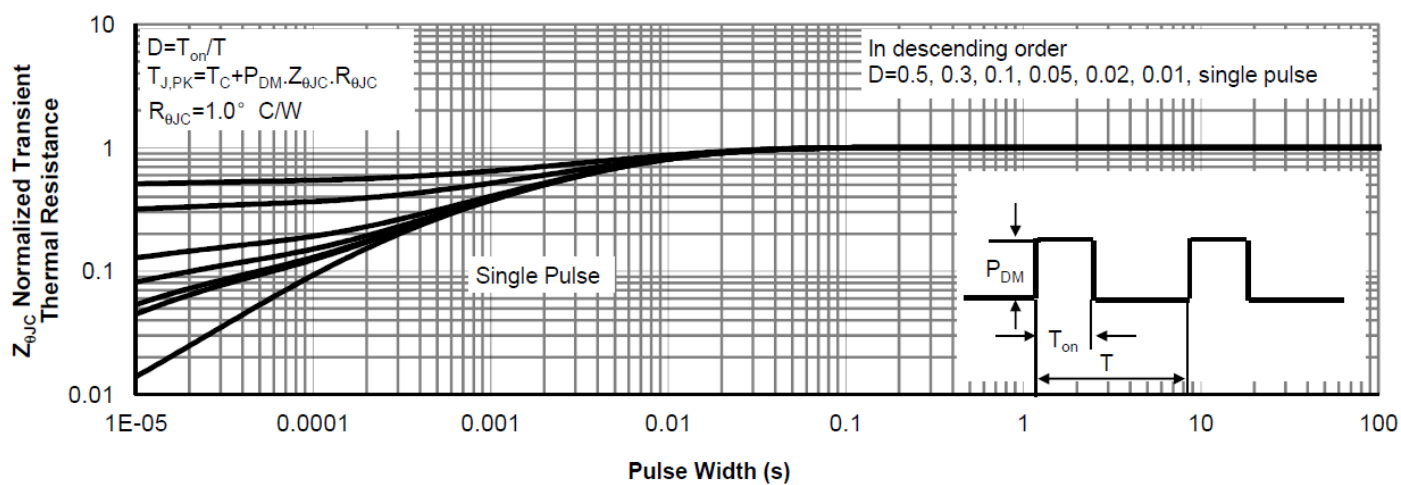
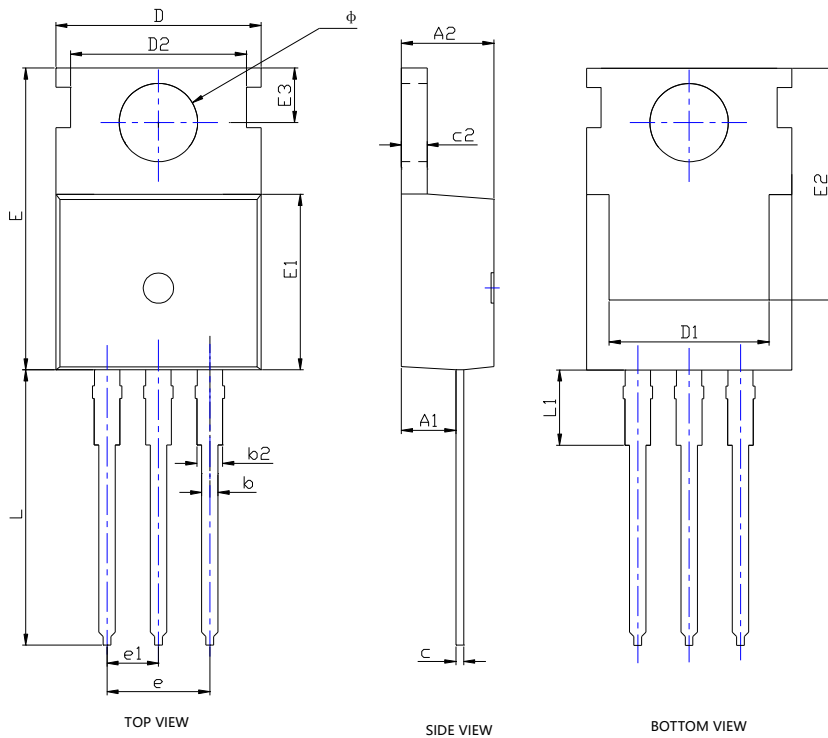


Figure9. Normalized Maximum Transient thermal impedance

■ TO-220AB-C Package information



SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A1	0.091	0.098	2.300	2.500
A2	0.173	0.181	4.400	4.600
b	0.028	0.035	0.700	0.900
b2	0.049	0.056	1.250	1.420
c	0.018	0.022	0.450	0.550
c2	0.049	0.053	1.250	1.350
D	0.382	0.402	9.700	10.200
D1	0.295	0.331	7.500	8.400
D2	0.335	0.350	8.500	8.900
E	0.602	0.634	15.300	16.100
E1	0.358	0.366	9.100	9.300
E2	0.497	0.525	12.630	13.330
E3	0.108BSC		2.750BSC	
e	0.200BSC		5.080BSC	
e1	0.100BSC		2.540BSC	
L	0.512	0.531	13.000	13.500
L1	---	0.138	---	3.500
ø	0.140	0.148	3.550	3.750

NOTE:
 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
 2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.

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