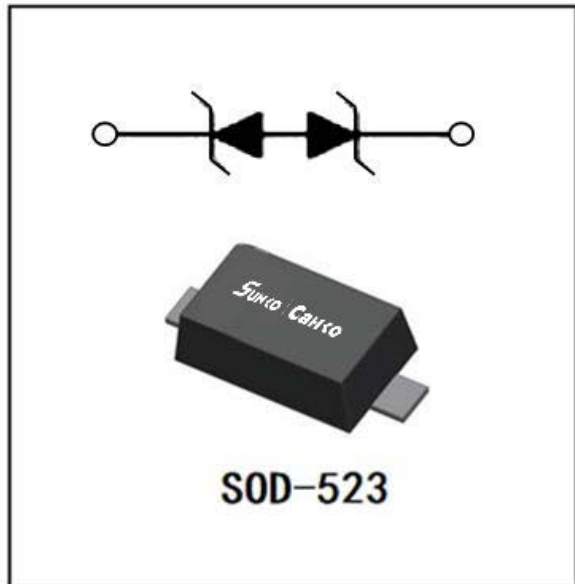


1-Line, Bi-directional, Transient Voltage Suppressor



Features

- Stand-off voltage: $\pm 5V$ Max
- Transient protection for each line according to IEC61000-4-2(ESD): $\pm 30kV$ (contact)
- IEC61000-4-5(surge): 2A (8/20 μs)
- Ultra-low capacitance: $C_J = 3pF$ typ
- Low leakage current
- Solid-state silicon technology

Applications

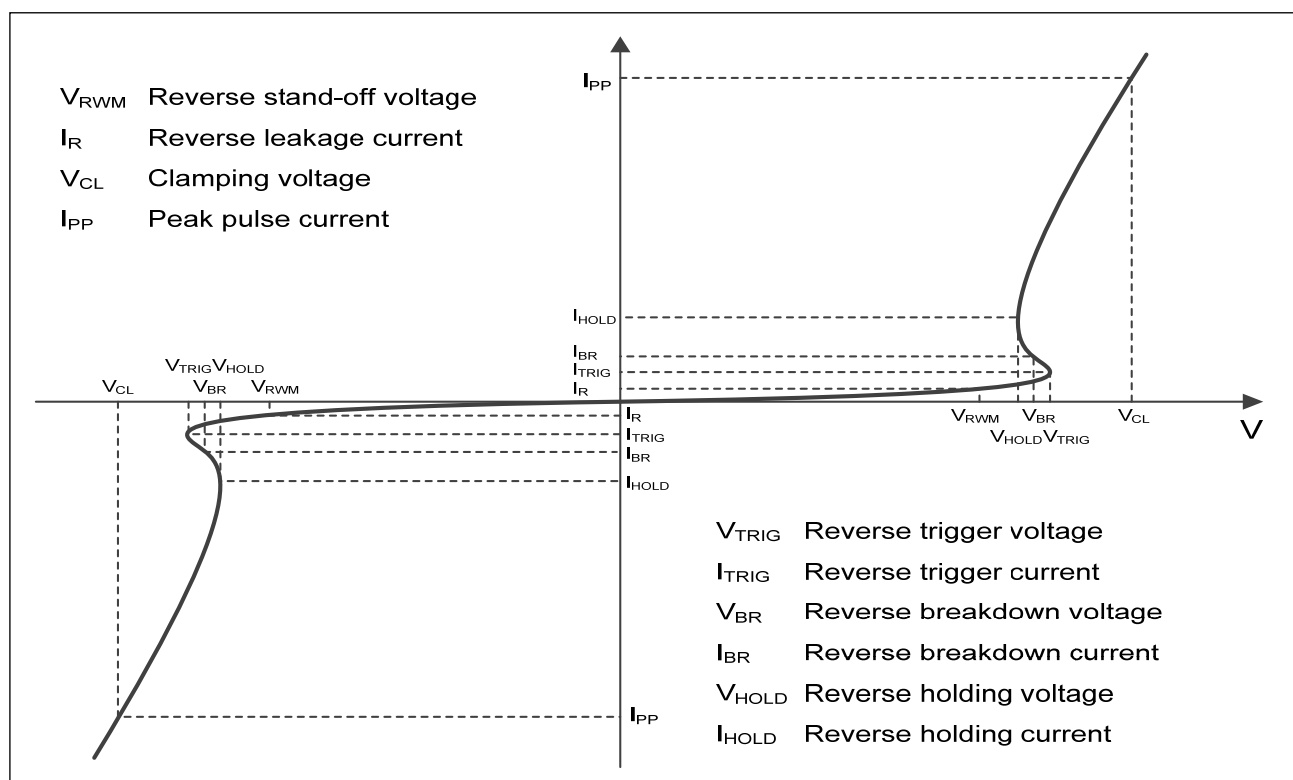
- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- 10/100/1000 Ethernet
- Local Area Network (LAN) equipment
- Communication systems
- Portable electronics
- SIM card protection
- High-speed data lines

Mechanical Data

- **Package:** SOD-523
- Case Material: "Green" Molding Compound.
- Marking Information: See Below



■ Definitions of electrical characteristics



■Maximum Ratings

PARAMETER	SYMBOL	Rating	UNIT
Peak pulse power ($t_p = 8/20\mu s$)	P_{pk}	28	W
Peak pulse current ($t_p = 8/20\mu s$)	I_{PP}	2	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 20	KV
ESD according to IEC61000-4-2 contact discharge		± 20	KV
Junction temperature	T_J	125	°C
Operating temperature	T_{OP}	-40~85	°C
Storage temperature	T_{STG}	-55~150	°C

■Electrical Characteristics ($T_a=25^\circ C$ Unless otherwise specified)

PARAMETER	Symbol	UNIT	Conditions	Min	Typ	Max
Reverse maximum working voltage	V_{RWM}	V				5
Reverse leakage current	I_R	nA	$V_{RWM} = 5V$			100
Reverse breakdown voltage	V_{BR}	V	$I_{BR} = 1mA$	5.5		
Clamping voltage ³⁾	V_{CL}	V	$I_{PP} = 1A, t_p = 8/20\mu s$			10
		V	$I_{PP} = 2A, t_p = 8/20\mu s$			14
Junction capacitance	C_J	pF	$V_R = 0V, f = 1MHz$		3	5

(1). TLP parameter: $Z_0 = 50\Omega$, $t_p = 100ns$, $t_r = 2ns$, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.

(2). Contact discharge mode, according to IEC61000-4-2.

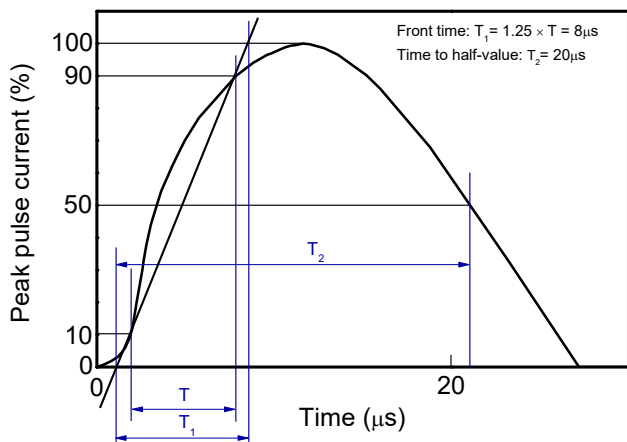
(3). Non-repetitive current pulse, according to IEC61000-4-5.

■Ordering Information (Example)

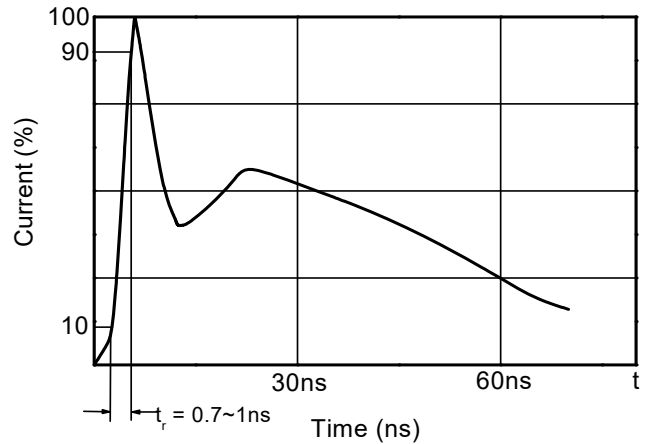
PREFERED P/N	PACKING CODE	UNIT WEIGHT(mg)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
ESDLC5V0D5B	F2	Approximate 0.9	3 000	30 000	120 000	7 Tape & reel

■ Characteristics (Typical)

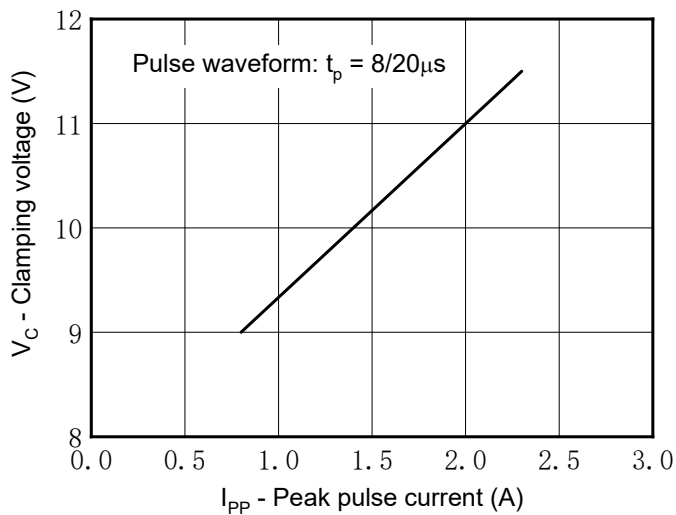
8/20 μ s waveform per IEC61000-4-5



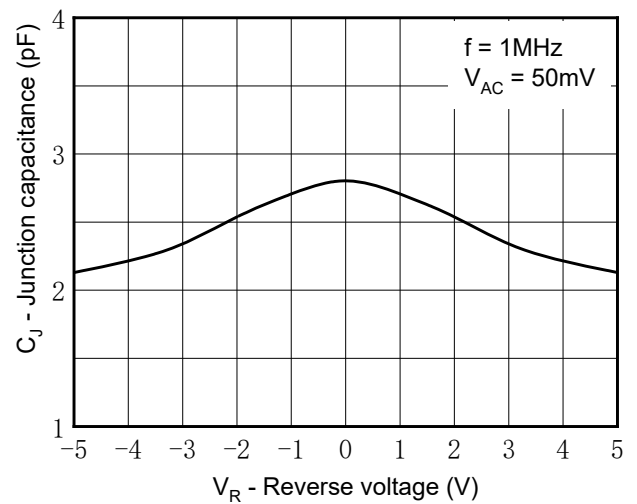
Contact discharge current waveform per IEC61000-4-2



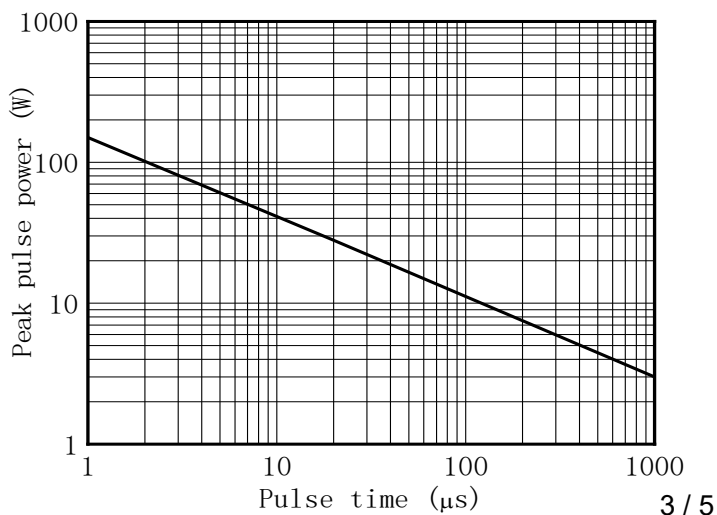
Clamping voltage vs. Peak pulse current



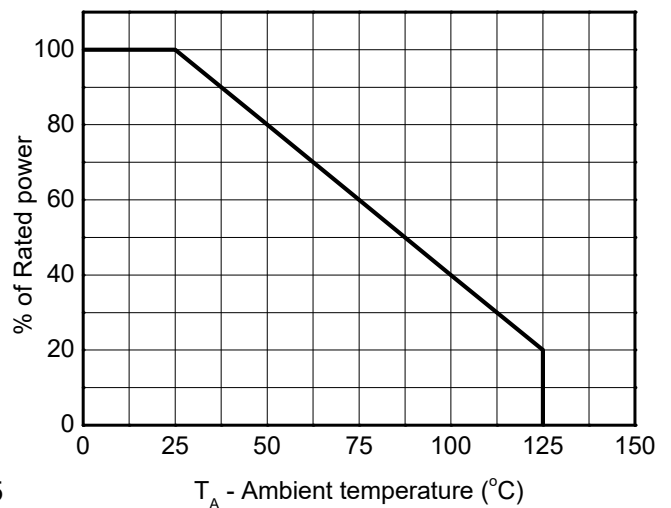
Capacitance vs. Reverse voltage



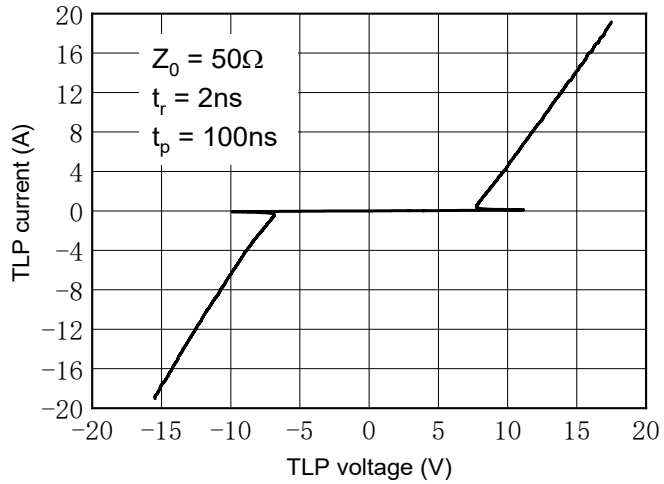
Non-repetitive peak pulse power vs. Pulse time



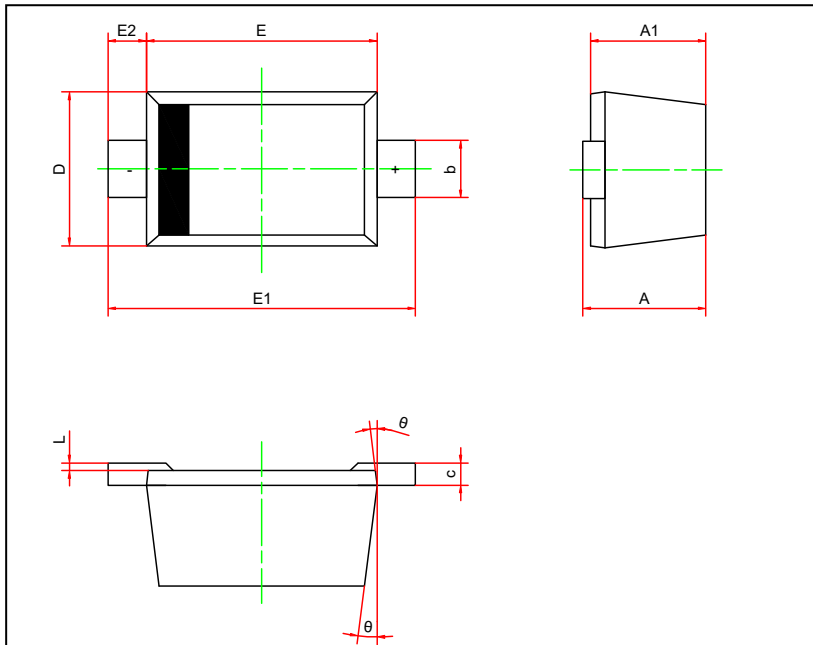
Power derating vs. Ambient temperature



TLP Measurement

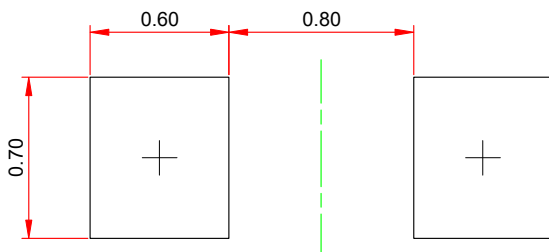


■ Outline Dimensions



Symbol	Min.(mm)	Max.(mm)
A	0.510	0.770
A1	0.500	0.700
b	0.250	0.350
c	0.080	0.150
D	0.750	0.850
E	1.100	1.300
E1	1.500	1.700
E2	0.200 Ref	
L	0.010	0.070
θ	7° Ref	

■ Recommend land pattern (Unit:mm)



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met

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