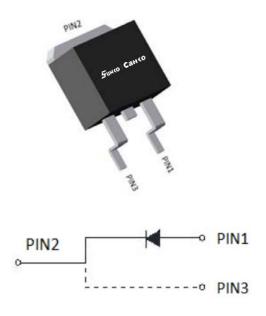


# **Silicon Carbide Schottky Diode**

$V_{RRM}$	650 V
I <sub>F (135°C)</sub>	50 A
$Q_C$	135.3nC



#### **Features**

- Positive temperature coefficient
- Temperature-independent switching
- Maximum working temperature at 175 °C
- Unipolar devices and zero reverse recovery current
- Zero reverse recovery current
- Essentially no switching losses
- Reduction of heat sink requirements
- High-frequency operation
- Reduction of EMI

### **Typical Applications**

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

#### **Mechanical Data**

• Package: TO-263

Molding compound meets UL 94 V-0 flammability

rating, -, halogen-free
• Terminals: Tin plated leads

• Polarity: As marked

■Maximum Ratings (T<sub>c</sub>=25°C Unless otherwise specified)

PARAMTETER	SYMBOL	UNIT	VALUE
Device marking code			D106550BQG3
Reverse voltage (repetitive peak) @ T <sub>j</sub> =25°C	$V_{RRM}$	V	650
Reverse voltage (Surge Peak) @ T <sub>j</sub> =25°C	$V_{RSM}$	V	650
Reverse voltage (DC) @ T <sub>j</sub> =25°C	$V_{DC}$	V	650
Continuous forward current @ T <sub>c</sub> =25°C	I <sub>F</sub>	A	108
Continuous forward current @ T <sub>c</sub> =135°C			50
Non-repetitive peak forward surge current @ T <sub>c</sub> =25°C, tp=10ms, Half Sine Wave	I <sub>FSM</sub>	А	380
Power Dissipation@ T <sub>c</sub> =25°C	D.	W	375
Power Dissipation@ T <sub>c</sub> =110°C	P <sub>TOT</sub>	VV	162.5
i²t Value@ Tc=25°C ,tp=10ms	∫ i²dt	A <sup>2</sup> S	722
Operating junction and Storage temperature range	$T_{j}$ , $T_{stg}$	°C	-55 to +175



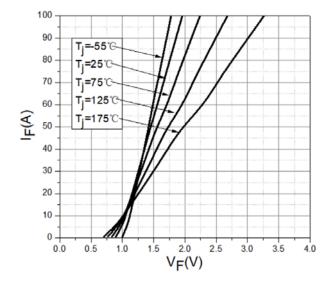
#### **■**Electrical Characteristics

PARAMTETER	SYMBOL	UNIT	TEST CONDITIONS	Тур.	Max.
Torrupted voltage drap	V <sub>F</sub>	V	I <sub>F</sub> =50A, T <sub>j</sub> =25°C	1.45	1.6
Forward voltage drop	VF		I <sub>F</sub> =50A, T <sub>j</sub> =175°C	1.9	-
Poverse leakage current		μA	V <sub>R</sub> =650V, T <sub>j</sub> =25°C	3	25
Reverse leakage current I <sub>R</sub>	I <sub>R</sub>	μΑ	V <sub>R</sub> =650V, T <sub>j</sub> =175°C	20	-
Total capacitive charge	Qc	nC	$V_R$ =400V, $T_j$ =25°C, $QC$ = $\int_0^{VR}C(V)dV$	135.3	-
	tance C	pF	V <sub>R</sub> =0V, f=1MHZ	2453	-
Total capacitance			V <sub>R</sub> =200V, f=1MHZ	247	-
			V <sub>R</sub> =400V, f=1MHZ	243	-
Capacitance Stored Energy	Ec	μJ	V <sub>R</sub> =400V	16.5	-

### **■Thermal Characteristics** (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	$R_{eJ-C}$	°C W	0.4

## ■Typical Characteristics



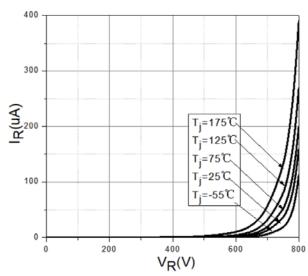


Figure 1. Forward Characteristics

Figure 2. Reverse Characteristic



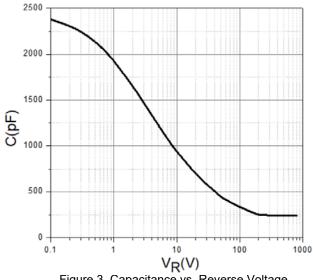


Figure 3. Capacitance vs. Reverse Voltage

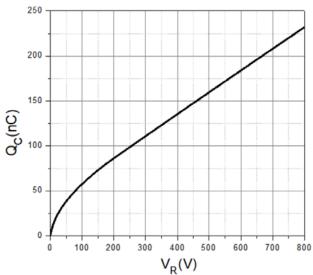


Figure 4. Total Capacitance Charge vs. Reverse Voltage

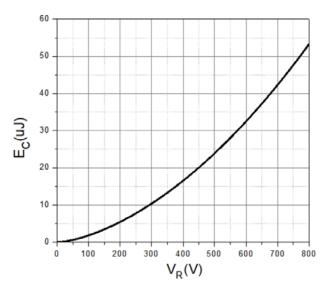


Figure 5. Capacitance Stored Energy

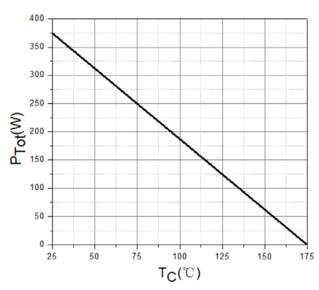


Figure 6. Power Derating

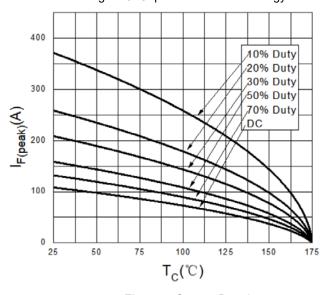


Figure 7. Current Derating

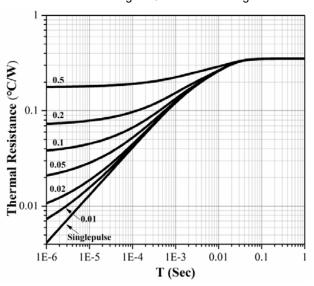
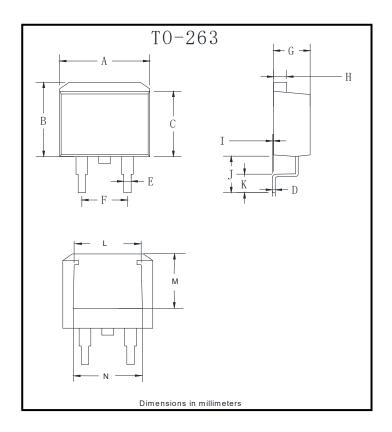


Figure 8. Transient Thermal Impedance

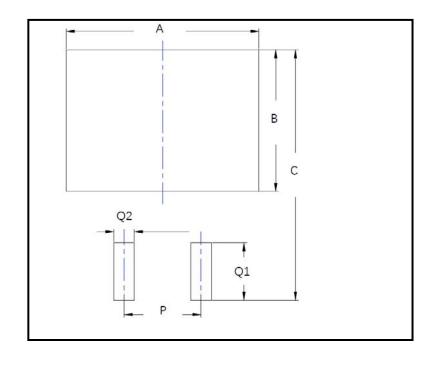


### **■**Outline Dimensions



TO-263		
Dim	Min	Max
Α	9.5	11.5
В	9.7	10.5
С	8.4	9.0
D	0.28	0.64
E	0.68	0.94
F	4.55	5.6
G	4.04	5.10
Н	1.14	1.4
I	0	0.2
J	4.9	6.05
K	1.79	2.79
L	7.3	7.9
М	6.2	6.8
N	7.6	8.2

## ■ 推荐焊盘尺寸图 Suggested Pad Layout



Dim	Millimeters
Α	12.7
В	9.4
С	16.6
Р	5.08
Q1	3.8
Q2	1.35



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