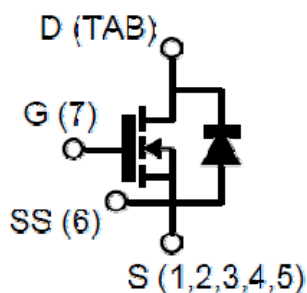
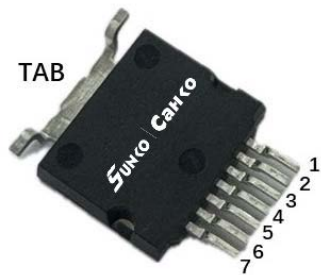


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	650V
I_D (25°C)	92A
$R_{DS(on)}$	25mΩ



Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free,

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** T2PAK
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings (T_c=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D206525T2GH		
Drain source voltage @ T _j =25°C	$V_{DS,max}$	V	650	$V_{GS}=0\text{ V}$, $I_D=100\mu\text{A}$	
Gate source voltage @ T _j =25°C	$V_{GS,max}$	V	-10/+22	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	Note1
Gate source voltage @ T _j =25°C	$V_{GS,op}$	V	-4/+18	Recommended operational values	
Continuous drain current @ T _c =25°C	I_D	A	92	$V_{GS}=18\text{V}$, T _c =25°C	Fig.14
Continuous drain current @ T _c =110°C			60	$V_{GS}=18\text{V}$, T _c =110°C	
Pulse Drain Current	$I_{D,pulse}$	A	219	Limited by t _{pw}	Fig.15
Avalanche energy, Single Pulse	E_{AS}	J	1	$V_{DD}=75\text{V}$, L=30mH	
Power Dissipation	P_{TOT}	W	294	T _c =25°C , T _j = 175°C	Fig.13
Operating junction and Storage temperature range	T _j , T _{stg}	°C	-55 to +175		
Soldering temperature	T _L	°C	260	1.6mm (0.063") from case for 10s	

■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	1.5	2.5	4.0	$V_{DS}=V_{GS}$, $I_D=50mA$	Fig.4, 11
Drain source breakdown voltage	$V_{(BR)DSS}$	V	650			$V_{GS}=0$, $I_D=100\mu A$	
Zero gate voltage drain current	I_{DSS}	μA		<1	100	$V_{DS}=650V$, $V_{GS}=0V$	
				10	500	$V_{DS}=650V$, $V_{GS}=0V$, $T_j=175^\circ C$	
Gate source leakage current	I_{GSS}	nA			250	$V_{GS}=18V$, $V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS(on)}$	m Ω		25	30	$V_{GS}=18V$, $I_D=50A$	Fig.3, 5, 6
				35		$V_{GS}=18V$, $I_D=50A$, $T_j=175^\circ C$	
Transconductance	g_f	S		18.2		$V_{DS}=10V$, $I_D=50A$	

■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C_{iss}	pF		4838		$V_{DS}=400V$, $V_{GS}=0V$, $T_j=25^\circ C$, $f=1MHz$, $V_{AC}=25mV$	Fig.10
Output capacitance	C_{oss}			358			
Reverse capacitance	C_{rss}			47			
Coss stored energy	E_{oss}	μJ		34			Fig.12
Gate source charge	Q_{gs}	nC		80		$V_{DS}=400V$, $V_{GS}=-4/18V$, $I_D=50A$	Fig.16
Gate drain charge	Q_{gd}			75			
Gate charge	Q_g			275			
Short-Circuit Withstand Time	t_{sc}	μs		1.6		$V_{GS}=-4/18V$, $V_{DS}=400V$, $I_{sc}=800A$ $R_G=30\Omega$	
Internal Gate Resistance	$R_{G(int)}$	Ω		1.0	5.0	$f=1MHz$, $V_{AC}=25mV$	

■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	ns		12.3		$V_{DD}=400V$, $V_{GS}=-4/+18V$, $I_D=50A$, $L=50\mu H$ $R_{g(ext)}=2.7\Omega$	Fig.17, 18
Rise time	t_r			31.3			
Turn off delay time	$t_{d(off)}$			40.7			
Fall time	t_f			15.8			
Turn on switching energy	E_{on}	μJ		360			
Turn off switching energy	E_{off}			188			

■Body diode characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V_{SD}	V		4.2		$V_{GS}=0V, I_{SD}=25A$	Fig.8
Continuous diode forward current	I_S	A		52		$V_{GS}=0V, T_c=25^\circ C$	
Reverse recovery time	t_{rr}	nS		48		$V_{DS}=400V, V_{GS}=0V, I_{SD}=25A, di/dt=300A/\mu S$	
Reverse recovery charge	Q_{rr}	nC		209			
Peak reverse recovery current	I_{rrm}	A		6.1			

Note 1: When using SiC Body Diode the maximum recommended $V_{GS} = -5V$

■Thermal Characteristics (T_a=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	$R_{\theta J-C}$	$^\circ C/W$	0.51

■Typical Characteristics

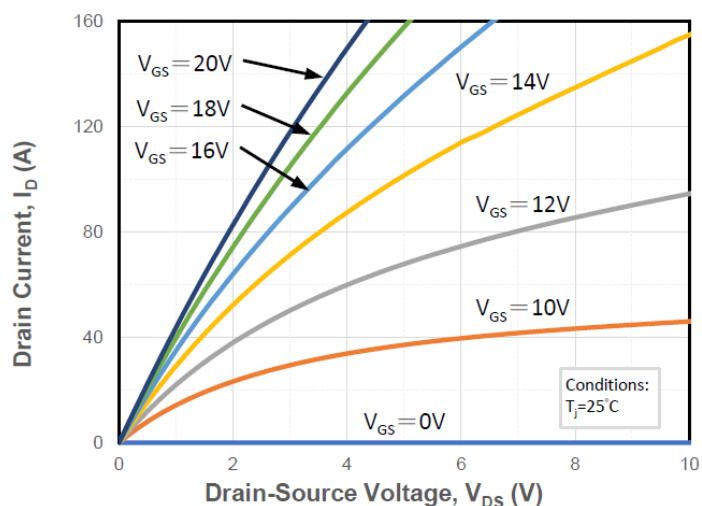


Figure 1. Output Characteristics Tj = 25°C

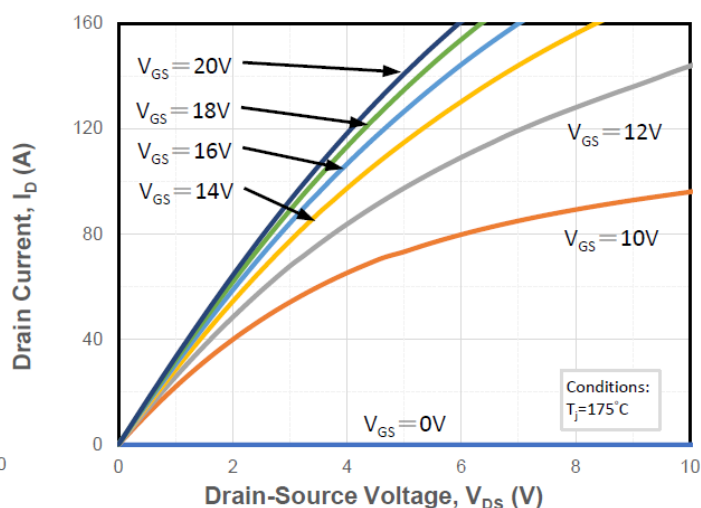


Figure2. Output Characteristics Tj = 175°C

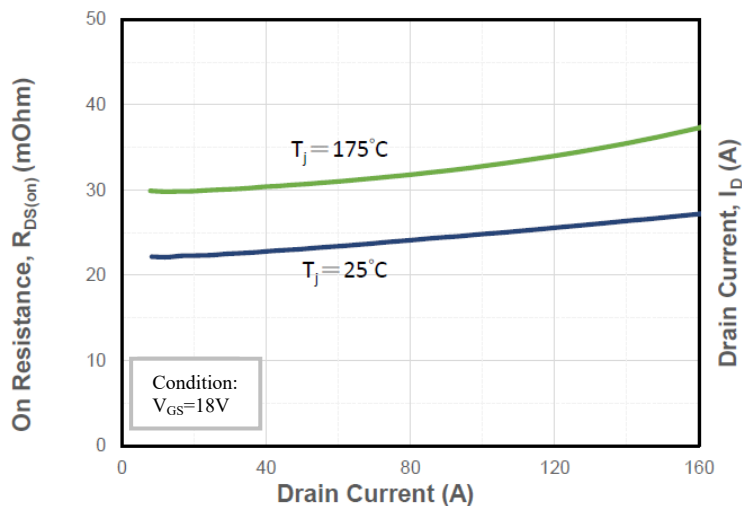


Figure 3. On-resistance vs. drain current

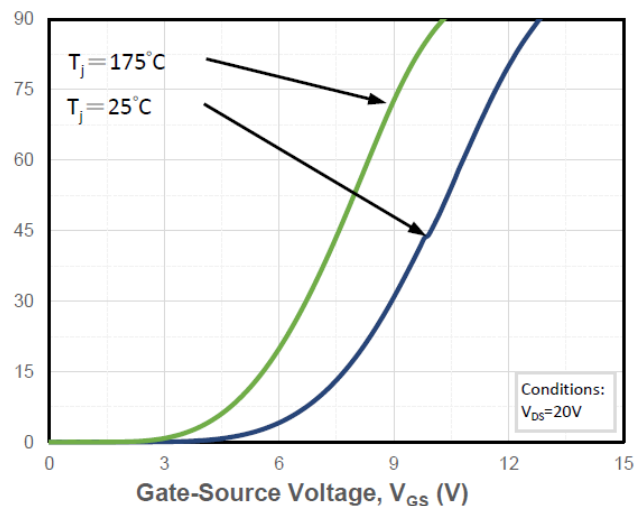


Figure 4. Transfer Characteristics for various T_J

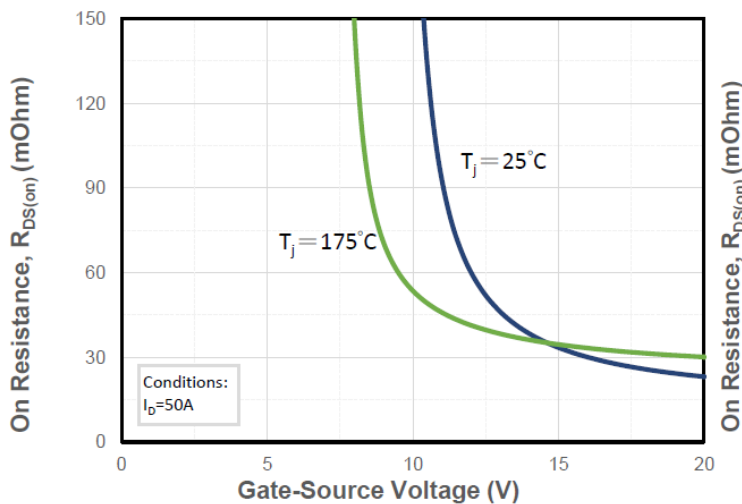


Figure 5. On-resistance vs. gate voltage for various T_J

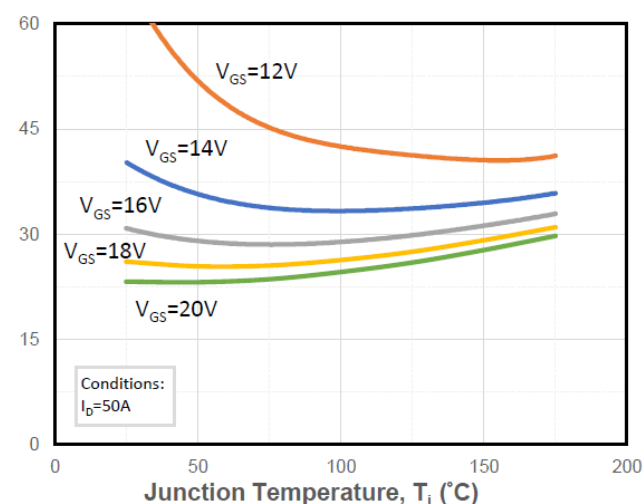


Figure 6. On-resistance vs. Temperature for various Gate voltage

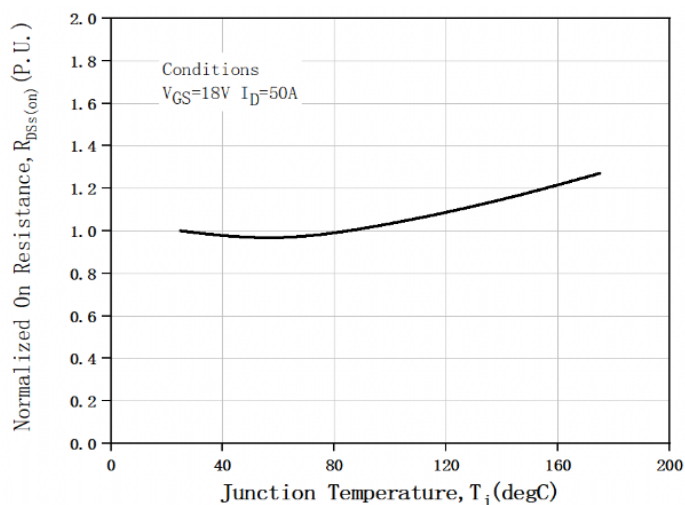


Figure 7. Normalized On-Resistance vs. Temperature

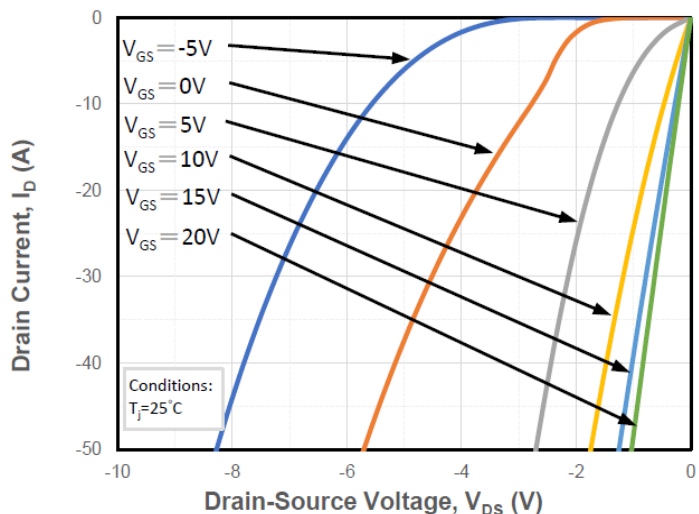


Figure 8. Reverse Output Characteristics at $T_J = 25°C$

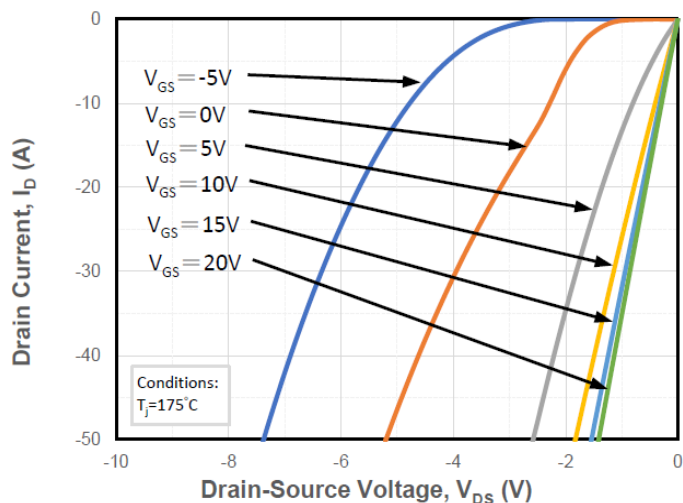


Figure 9. Reverse Output Characteristics at $T_j = 175^\circ\text{C}$

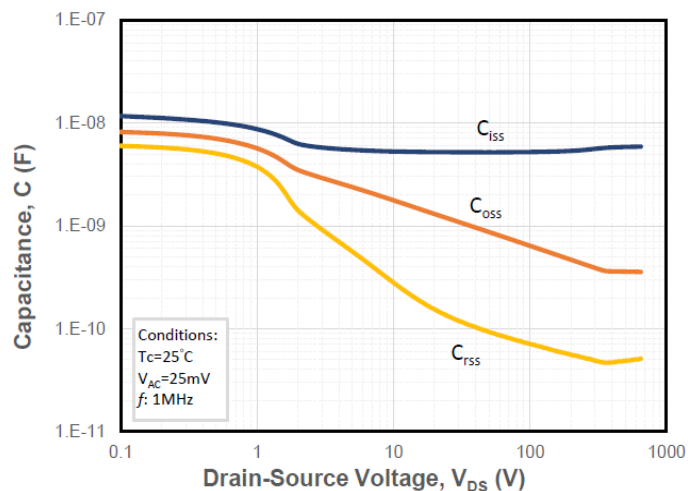


Figure 10. Capacitances vs. Drain to Source Voltage

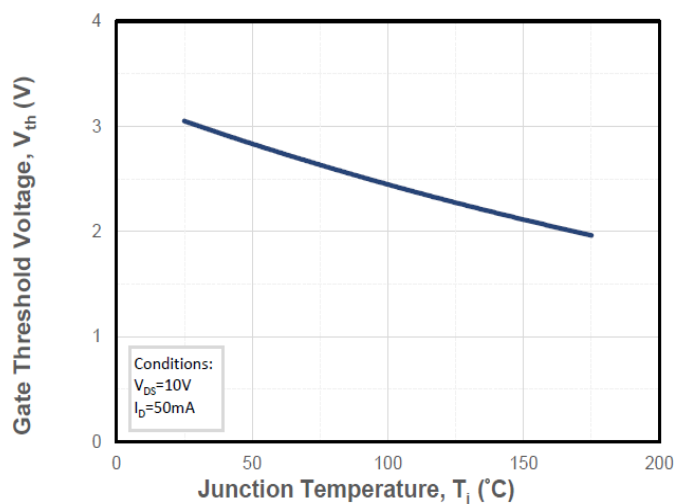


Figure 11. Threshold voltage vs. temperature

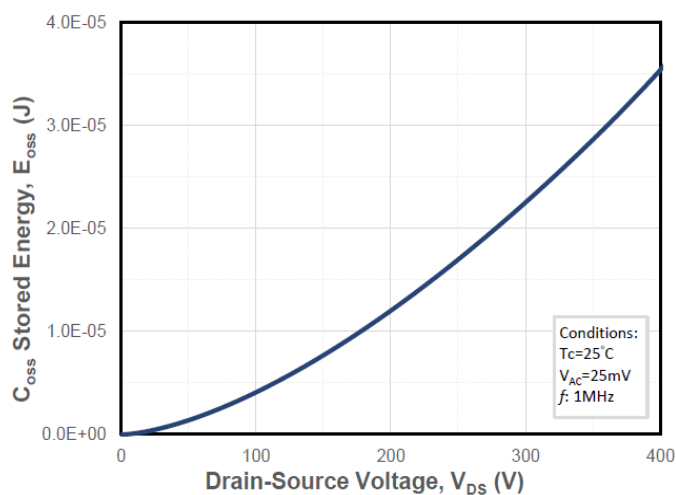


Figure 12. Output Capacitor Stored Energy

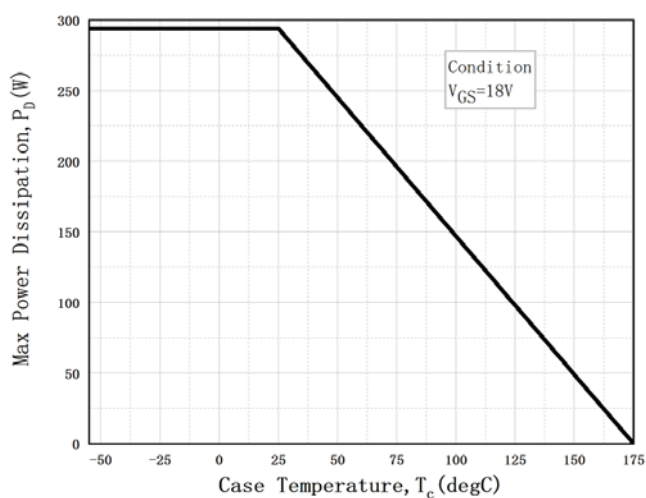


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

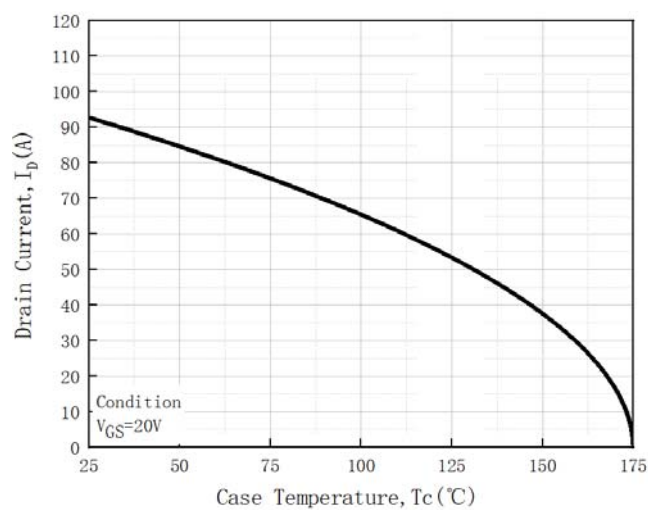


Figure 14. Drain Current Derating vs. Case Temperature

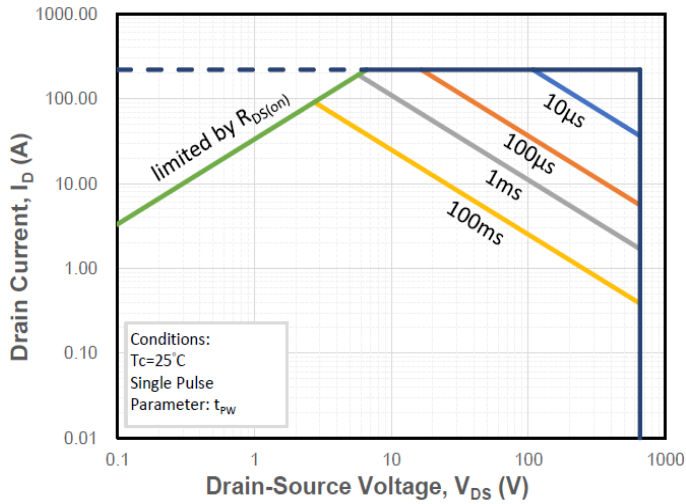


Figure 15. Safe Operating Area

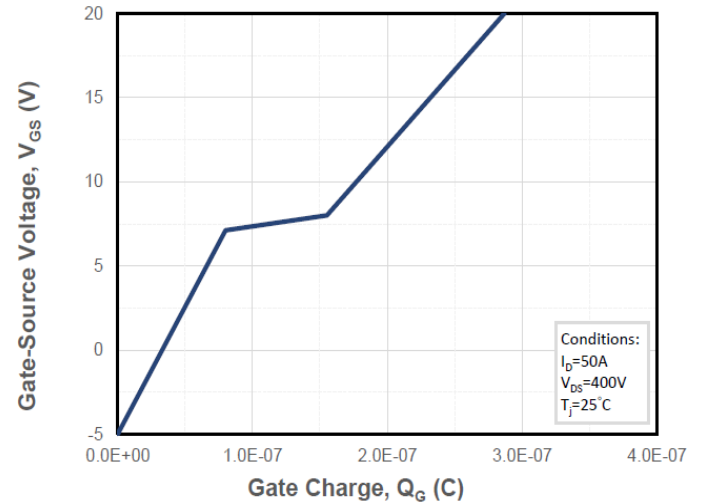


Figure 16. Gate Charge Characteristics

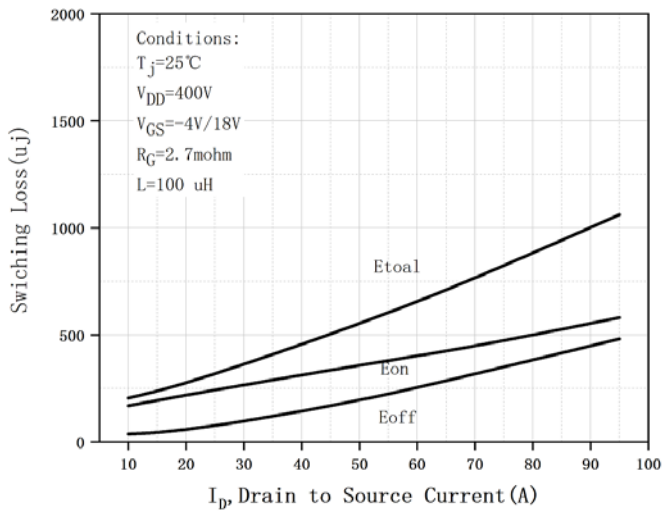


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

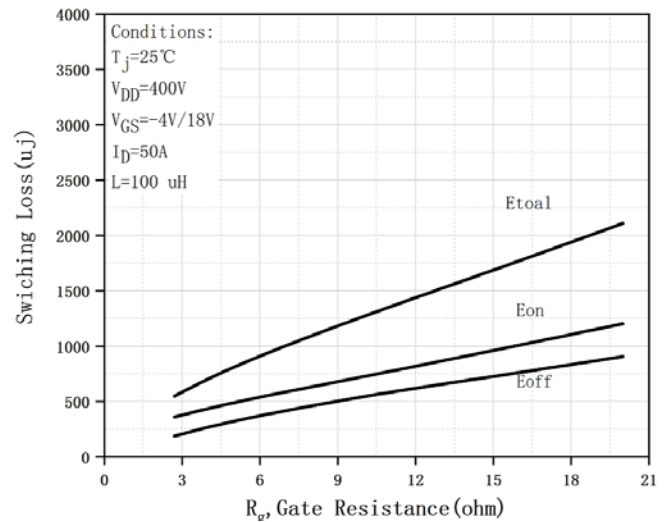


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor ($R_{G(ext.)}$)

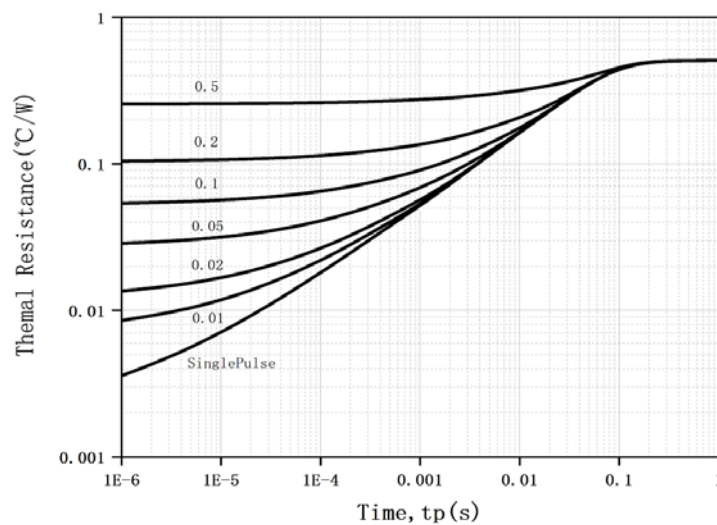


Figure 19. Transient Junction to Case Thermal Impedance

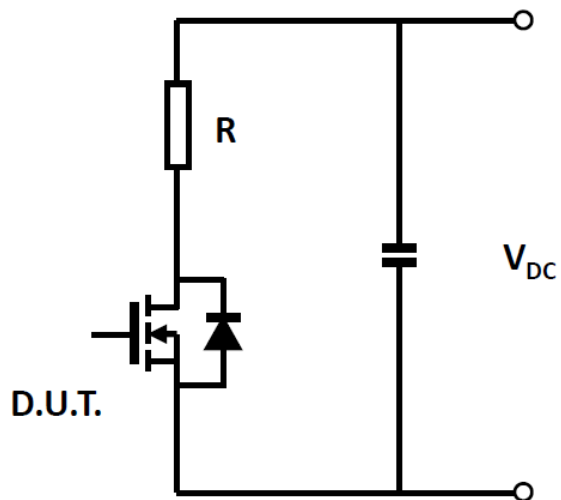


Figure 20. Schematic of Resistive Switching

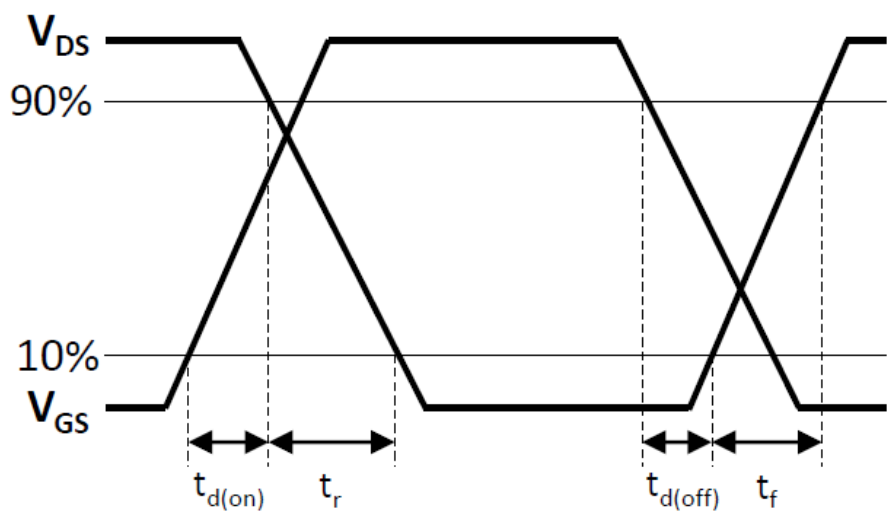
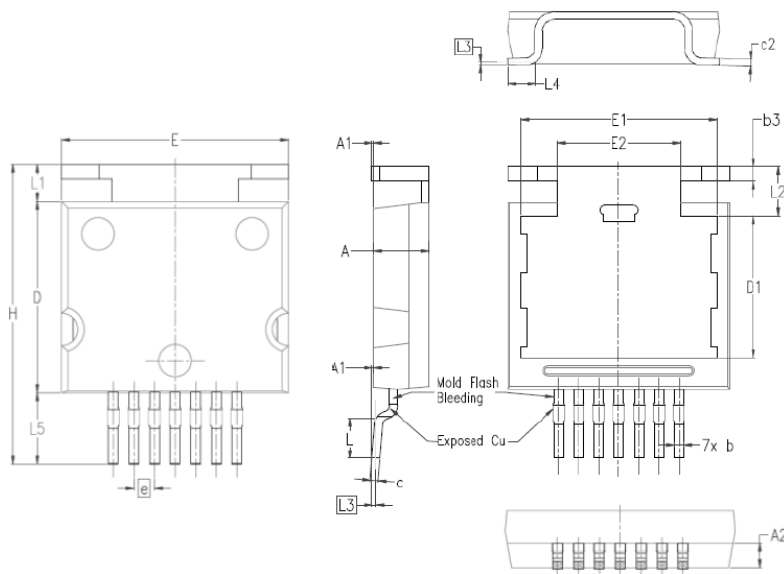


Figure 21. Switching Times Definition

■Outline Dimensions



Symbol	mm		
	Min.	Typ.	Max.
A	3.30	3.50	3.70
A1	---	0.10	0.25
A2	1.30	1.50	1.70
b	0.50	0.60	0.70
b3	0.80	0.90	1.00
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	9.00	9.20
E	13.60	14.00	14.40
E1	12.00	12.40	12.80
E2	7.60	7.80	8.00
e	1.27 BSC		
H	17.70	18.50	19.30
L	1.90	2.50	3.10
L1	2.30 REF		
L2	2.85	3.10	3.35
L3	0.25 BSC		
L4	1.25	1.85	2.45
L5	4.40 REF		

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