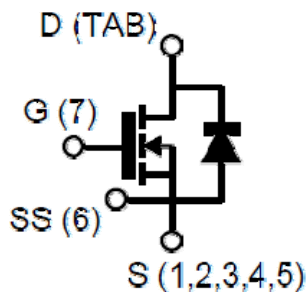
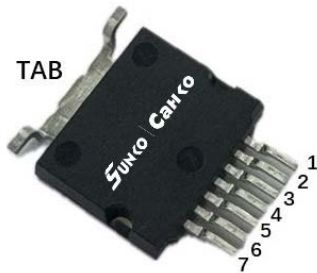


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	650V
I_D (25°C)	57A
$R_{DS(on)}$	60mΩ



Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free,

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** T2PAK
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D206560T2GH		
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	650	$V_{GS}=0\text{ V}$, $I_D=100\mu\text{A}$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-10/+25	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	Note1
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-4/+18	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	I_D	A	57	$V_{GS}=18\text{V}$, $T_c=25^\circ\text{C}$	Fig.14
Continuous drain current @ $T_c=110^\circ\text{C}$			38	$V_{GS}=18\text{V}$, $T_c=110^\circ\text{C}$	
Pulse Drain Current	$I_{D,pulse}$	A	128	Limited by t_{pw}	Fig.15
Avalanche energy, Single Pulse	E_{AS}	mJ	500	$V_{DD}=75\text{V}$, $L=30\text{mH}$	
Power Dissipation	P_{TOT}	W	242	$T_c=25^\circ\text{C}$, $T_j = 175^\circ\text{C}$	Fig.13
Operating junction and Storage temperature range	T_j, T_{stg}	$^\circ\text{C}$	-55 to +175		
Soldering temperature	T_L	$^\circ\text{C}$	260	1.6mm (0.063") from case for 10s	

■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	1.5	2.6	3.5	$V_{DS}=V_{GS}$, $I_D=20mA$	Fig.4, 11
Drain source breakdown voltage	$V_{(BR)DSS}$	V	650			$V_{GS}=0$, $I_D=100\mu A$	
Zero gate voltage drain current	I_{DSS}	μA		<1	50	$V_{DS}=650V$, $V_{GS}=0V$	
				10	500	$V_{DS}=650V$, $V_{GS}=0V$, $T_J=175^\circ C$	
Gate source leakage current	I_{GSS}	nA			250	$V_{GS}=18V$, $V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS\ ON}$	$m\Omega$		60	70	$V_{GS}=18V$, $I_D=20A$	Fig.3, 5, 6
				70		$V_{GS}=18V$, $I_D=20A$, $T_J=175^\circ C$	
Transconductance	g_f	S		13.2		$V_{DS}=15V$, $I_D=40A$	

■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C_{iss}	pF		1850		$V_{DS}=400V$, $V_{GS}=0V$, $T_J=25^\circ C$, $f=1MHz$, $V_{AC}=25mV$	Fig.10
Output capacitance	C_{oss}			208			
Reverse capacitance	C_{rss}			33			
Coss stored energy	E_{oss}	μJ		24			Fig.12
Gate source charge	Q_{gs}	nC		30		$V_{DS}=400V$, $V_{GS}=-4/18V$, $I_D=20A$	Fig.16
Gate drain charge	Q_{gd}			43			
Gate charge	Q_g			117			
Short-Circuit Withstand Time	t_{sc}	μs		6		$V_{GS}=-4/18V$, $V_{DS}=400V$, $I_{sc}=350A$ $R_G=30\Omega$	
Internal Gate Resistance	$R_{G(int)}$	Ω		2.0	5.0	$f=1MHz$, $V_{AC}=25mV$	

■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	ns		6.7		$V_{DD}=400V$, $V_{GS}=-4/+18V$, $I_D=20A$, $L=100\mu H$ $R_{g(ext)}=2.7\Omega$	Fig.17, 18
Rise time	t_r			17			
Turn off delay time	$t_{d(off)}$			19			
Fall time	t_f			9.5			
Turn on switching energy	E_{on}	μJ		102.5			
Turn off switching energy	E_{off}			18			

■Body diode characteristics (T_c=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V _{SD}	V		4.0		V _{GS} =0V, I _{SD} =10A	Fig.8
Continuous diode forward current	I _S	A		41		V _{GS} =0V, T _c =25°C	
Reverse recovery time	t _{rr}	nS		58		V _{DS} =400V, V _{GS} =0V, I _{SD} =10A, di/dt=300A/uS	
Reverse recovery charge	Q _{rr}	nC		122			
Peak reverse recovery current	I _{rrm}	A		3.75			

Note 1: When using SiC Body Diode the maximum recommended V_{GS} = -5V

■Thermal Characteristics (T_a=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	R _{θJ-C}	°C/W	0.62

■Typical Characteristics

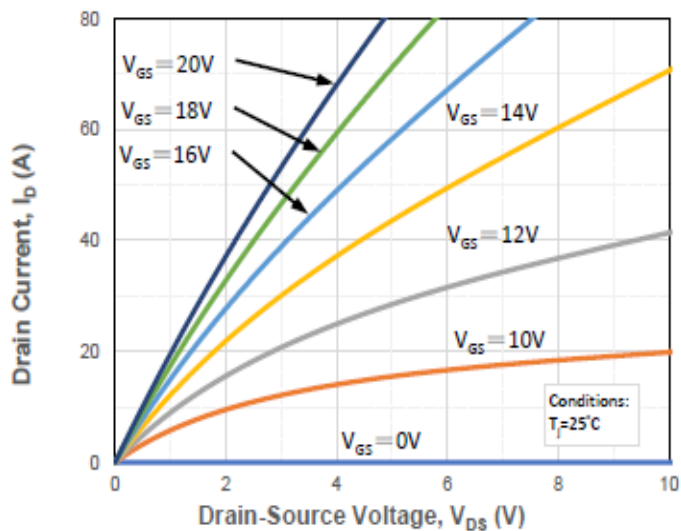


Figure 1. Output Characteristics T_j = 25°C

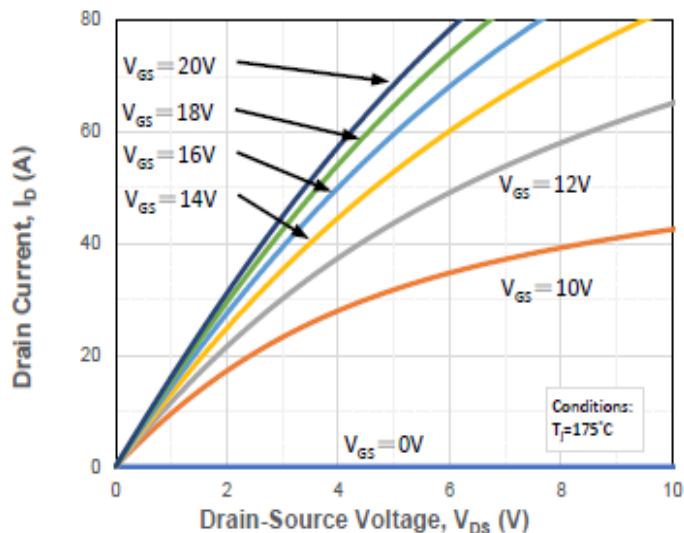


Figure2. Output Characteristics T_j = 175°C

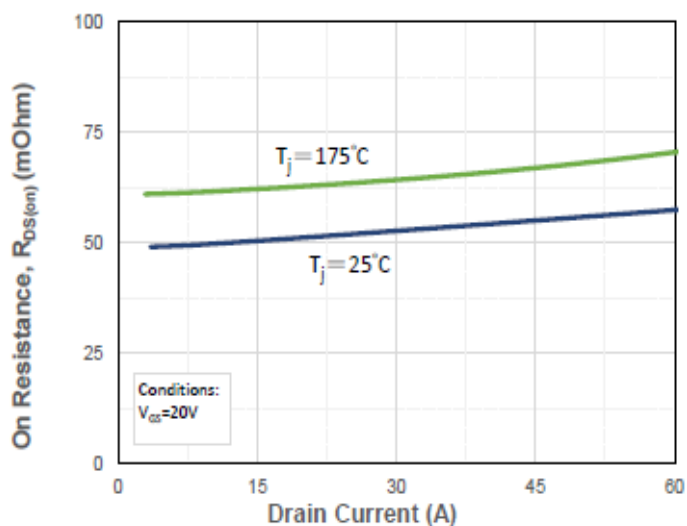


Figure 3. On-resistance vs. drain current

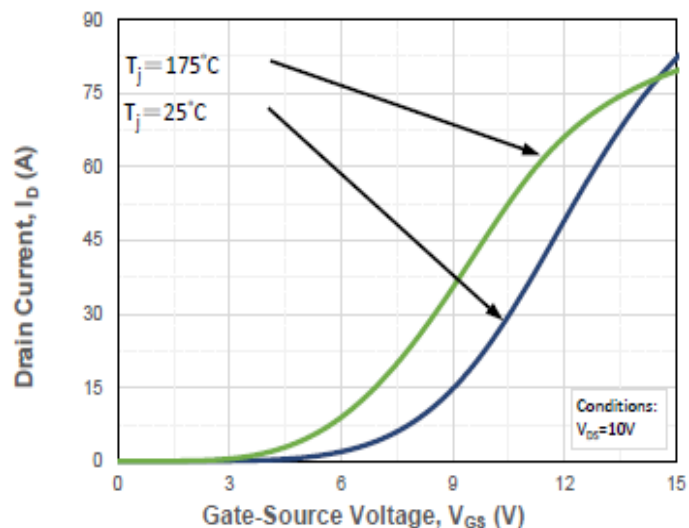


Figure 4. Transfer Characteristics for various T_j

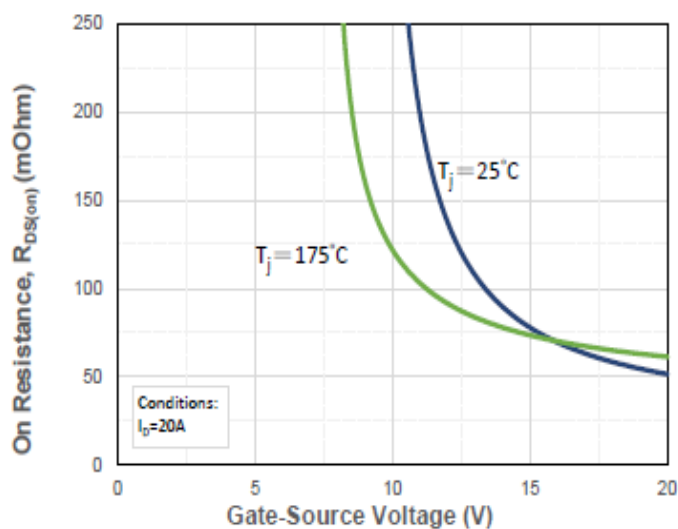


Figure 5. On-resistance vs. gate voltage for various T_j

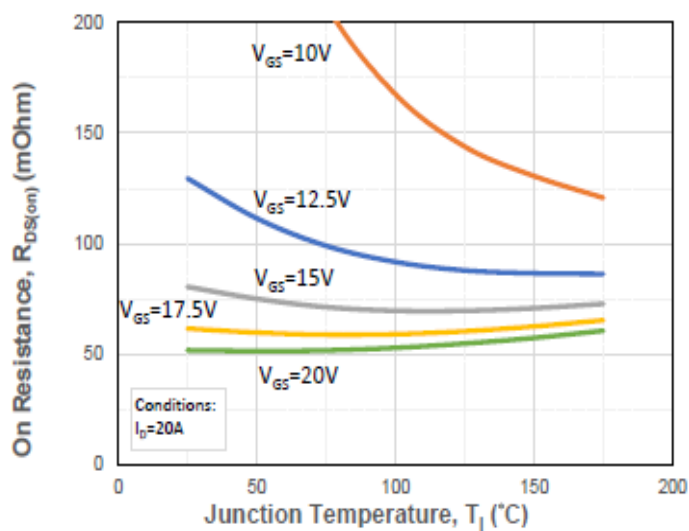


Figure 6. On-resistance vs. Temperature for various Gate voltage

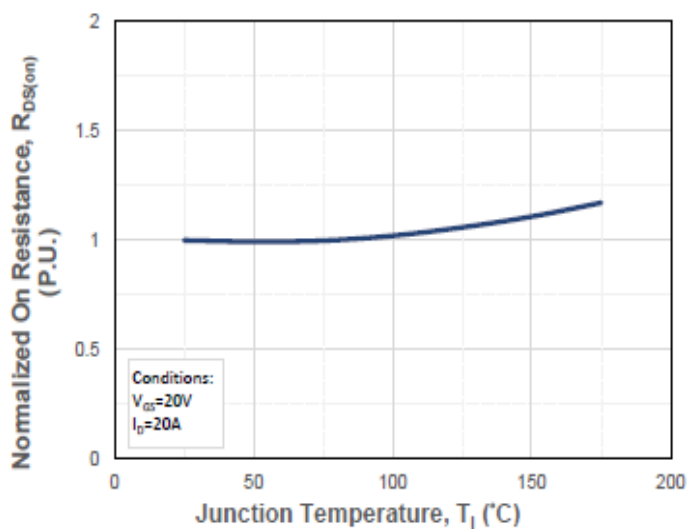


Figure 7. Normalized On-Resistance vs. Temperature

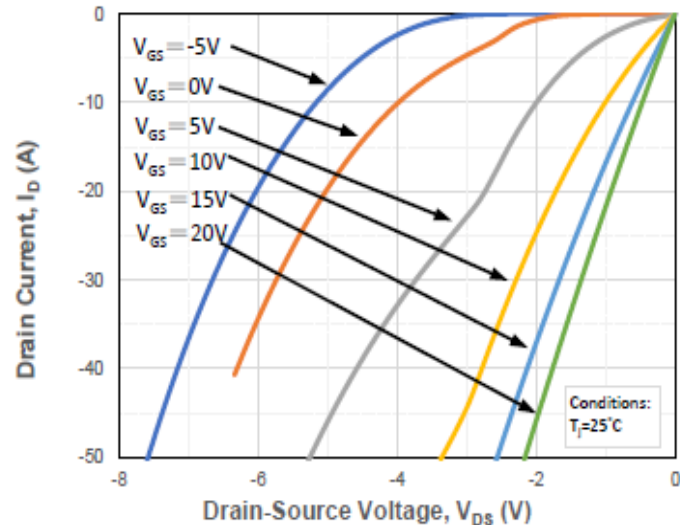


Figure 8. Reverse Output Characteristics at $T_j = 25^\circ\text{C}$

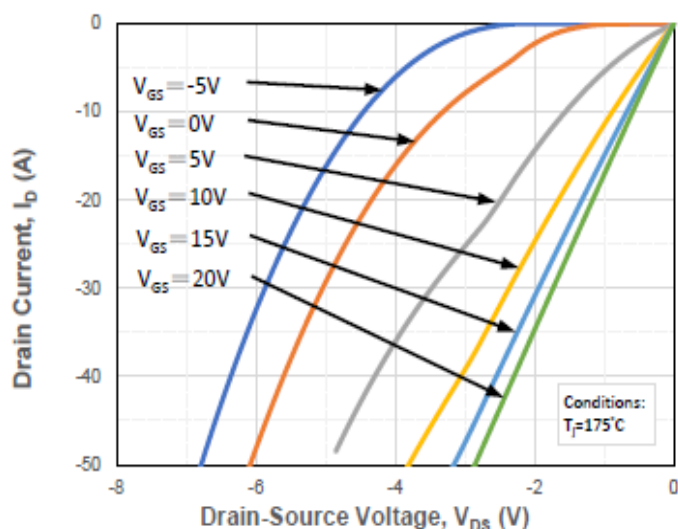


Figure 9. Reverse Output Characteristics at $T_j = 175^\circ\text{C}$

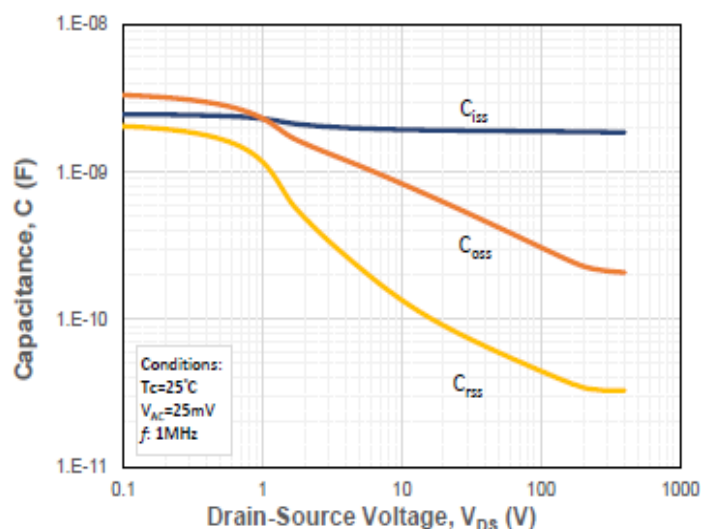


Figure 10. Capacitances vs. Drain to Source Voltage

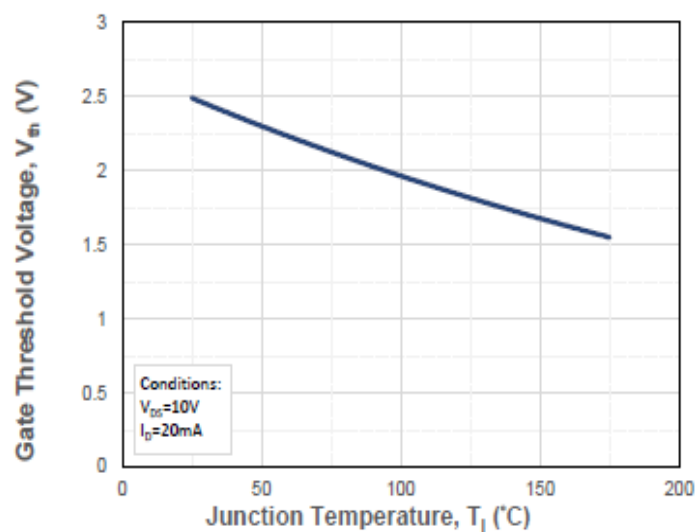


Figure 11. Threshold voltage vs. temperature

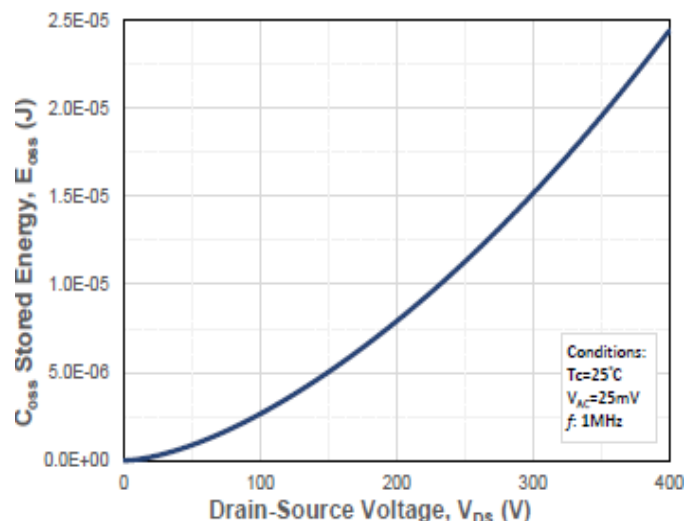


Figure 12. Output Capacitor Stored Energy

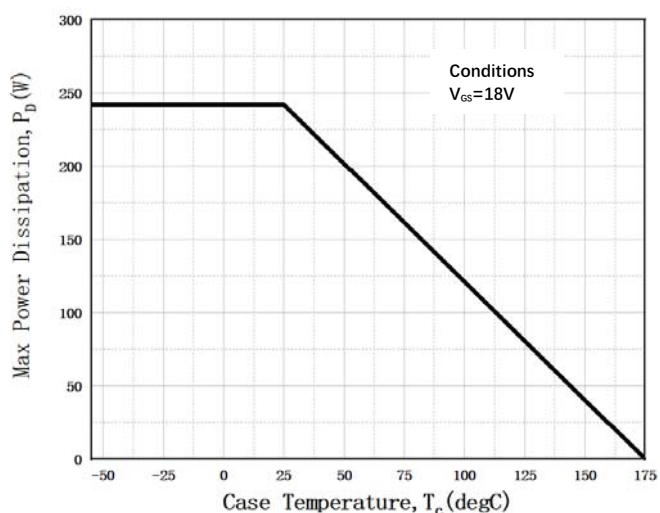


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

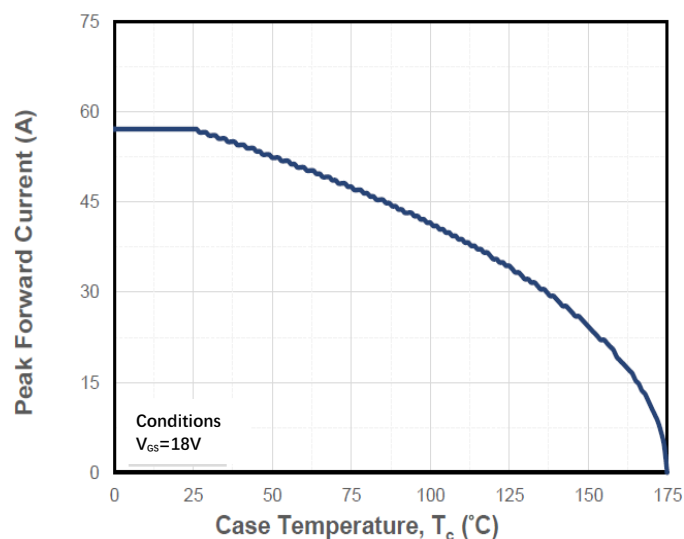


Figure 14. Drain Current Derating vs. Case Temperature

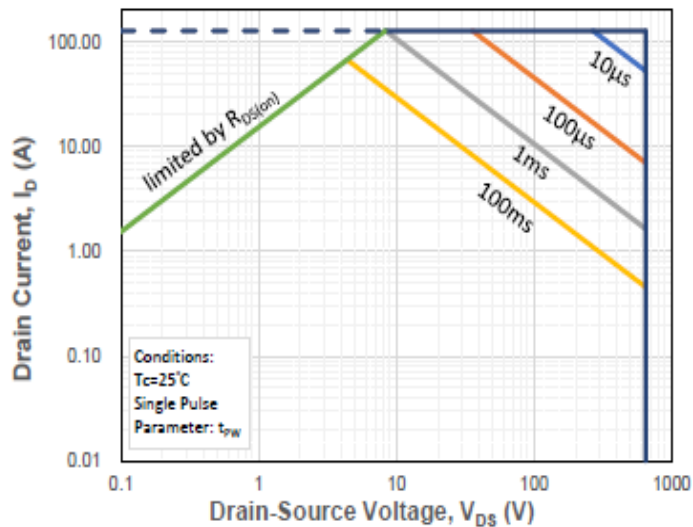


Figure 15. Safe Operating Area

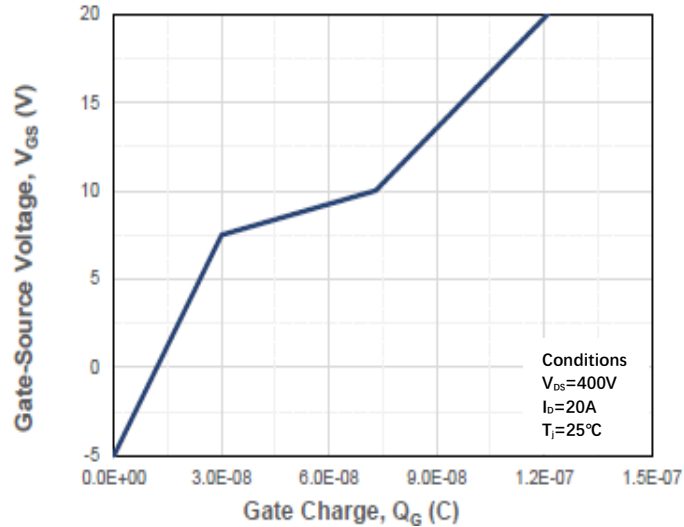


Figure 16. Gate Charge Characteristics

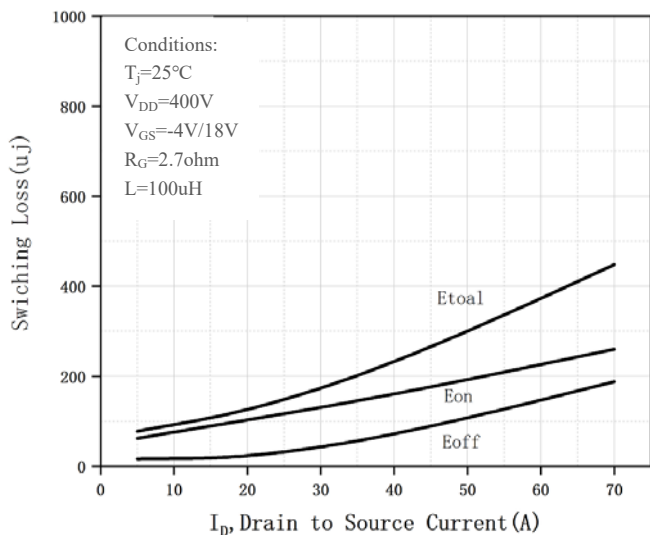


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

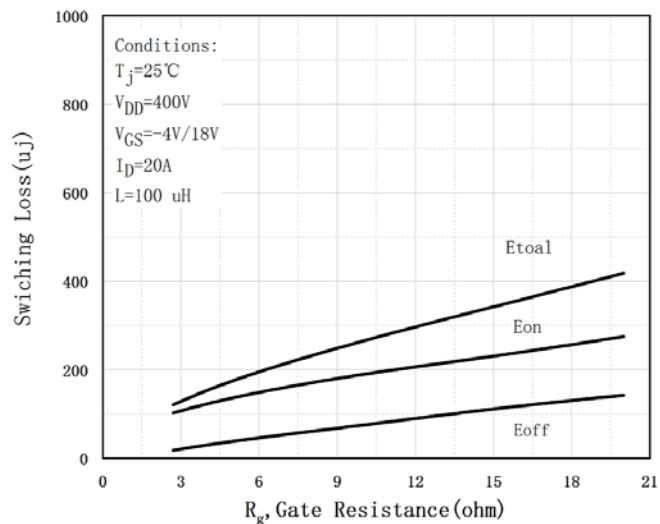


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor ($R_{G(ext.)}$)

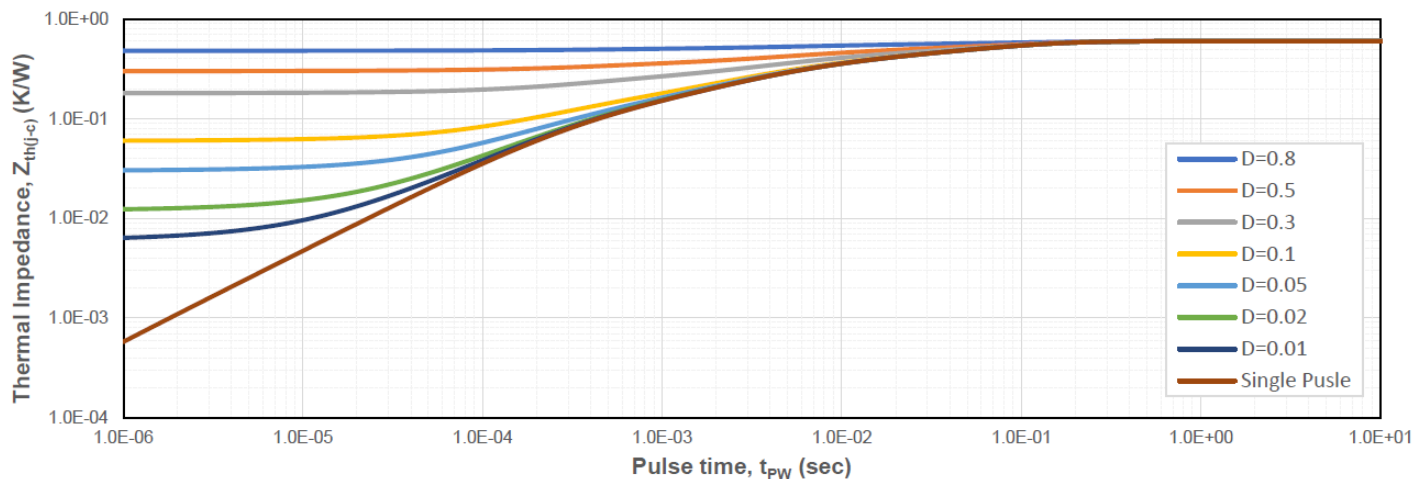


Figure 19. Transient Junction to Case Thermal Impedance

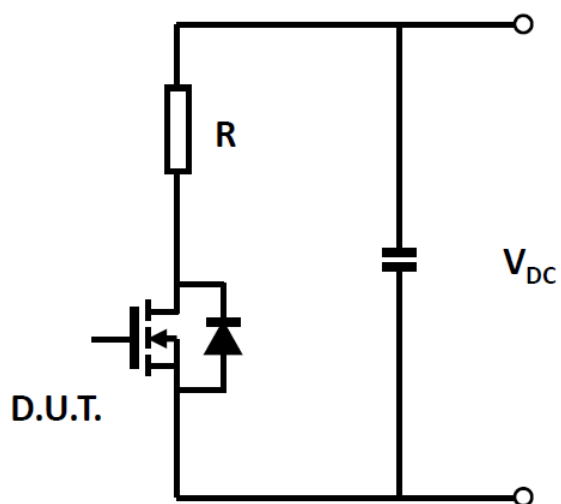


Figure 20. Schematic of Resistive Switching

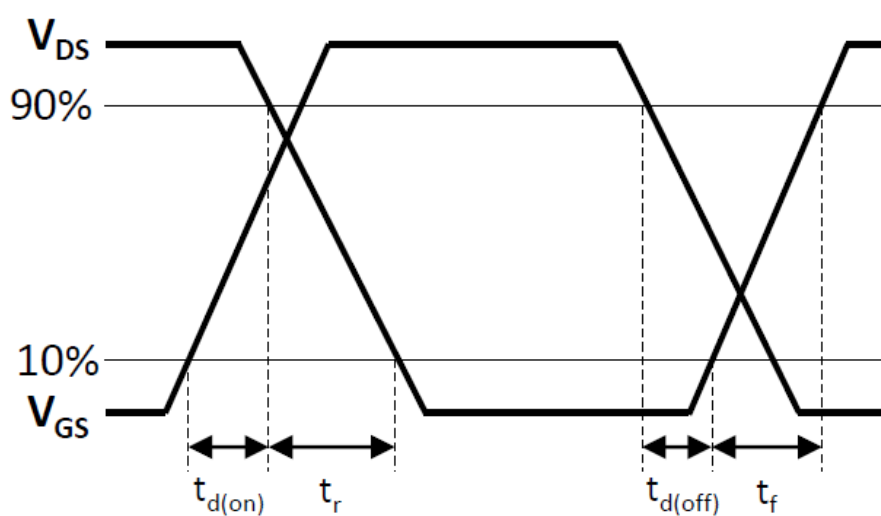
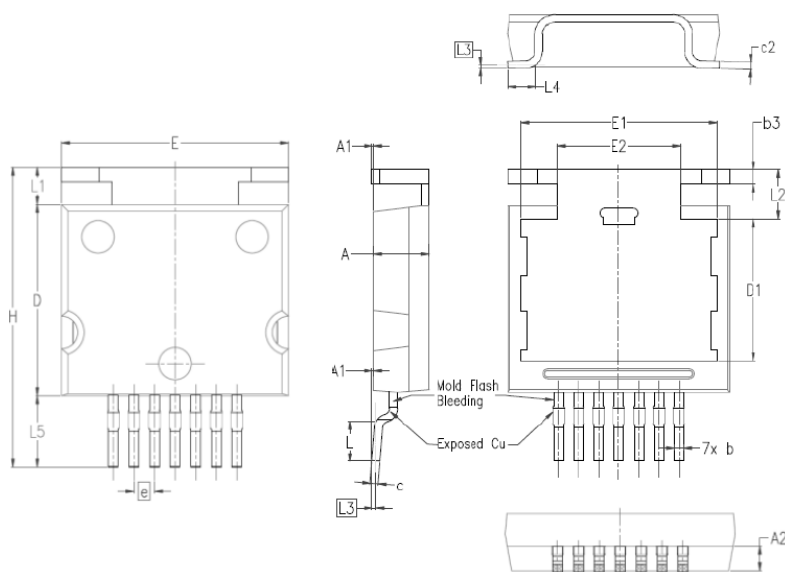


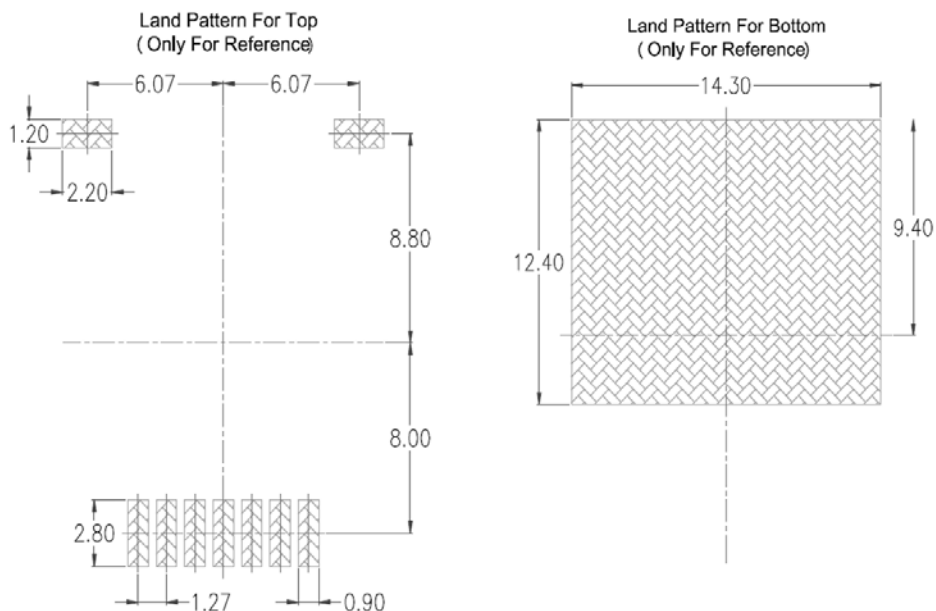
Figure 21. Switching Times Definition

■Outline Dimensions



Symbol	mm		
	Min.	Typ.	Max.
A	3.30	3.50	3.70
A1	---	0.10	0.25
A2	1.30	1.50	1.70
b	0.50	0.60	0.70
b3	0.80	0.90	1.00
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	9.00	9.20
E	13.60	14.00	14.40
E1	12.00	12.40	12.80
E2	7.60	7.80	8.00
e	1.27 BSC		
H	17.70	18.50	19.30
L	1.90	2.50	3.10
L1	2.30 REF		
L2	2.85	3.10	3.35
L3	0.25 BSC		
L4	1.25	1.85	2.45
L5	4.40 REF		

■Suggested Pad Layout



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