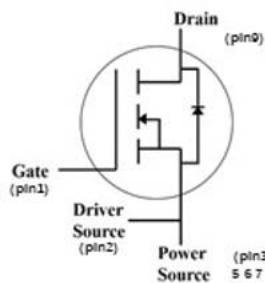
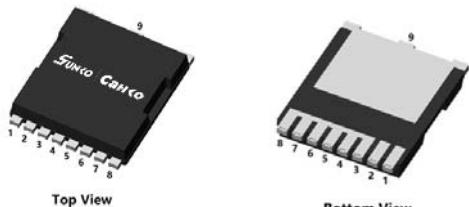


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	650V
I_D (25°C)	57A
$R_{DS(on)}$	60mΩ

**Features**

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free,

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TOLL
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code				D206560TLGH	
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	650	$V_{GS}=0\text{ V}$, $I_D=100\mu\text{A}$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-10/+25	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	Note1
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-4/+18	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	I_D	A	57	$V_{GS}=18\text{V}$, $T_c=25^\circ\text{C}$	Fig.14
Continuous drain current @ $T_c=110^\circ\text{C}$			38	$V_{GS}=18\text{V}$, $T_c=110^\circ\text{C}$	
Pulse Drain Current	$I_{D,pulse}$	A	127	Limited by t_{pw}	Fig.15
Avalanche energy, Single Pulse	E_{AS}	mJ	500	$V_{DD}=75\text{V}$, $L=30\text{mH}$	
Power Dissipation	P_{TOT}	W	234	$T_c=25^\circ\text{C}$, $T_j = 175^\circ\text{C}$	Fig.13
Operating junction and Storage temperature range	T_j , T_{stg}	°C	-55 to +175		
Soldering temperature	T_L	°C	260	1.6mm (0.063") from case for 10s	

■ Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	V _{GS(th)}	V	2.0	2.6	4.0	V _{DS} =V _{GS} , I _D = 20mA	Fig.4, 11
Drain source breakdown voltage	V _{(BR)DSS}	V	650			V _{GS} =0, I _D =100uA	
Zero gate voltage drain current	I _{DSS}	uA		<1	50	V _{DS} =650V, V _{GS} = 0V	
				10	500	V _{DS} =650V, V _{GS} = 0V, T _j = 175°C	
Gate source leakage current	I _{GSS}	nA			250	V _{GS} = 18V, V _{DS} =0V	
Current drain source on-state resistance	R _{DS ON}	mΩ		55	70	V _{GS} =18V, I _D =20A	Fig.3, 5, 6
				75		V _{GS} =18V, I _D =20A, T _j =175°C	
Transconductance	g _f	S		10.5		V _{DS} =20V, I _D =20A	

■ Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C _{iss}	pF		1850		V _{DS} =400V, V _{GS} =0V, T _j =25°C, f=1MHz, V _{AC} = 25mV	Fig.10
Output capacitance	C _{oss}			208			
Reverse capacitance	C _{rss}			33			
C _{oss} stored energy	E _{oss}	uJ		24		V _{DS} =400V, V _{GS} =-4/18V, I _D =20A	Fig.12
Gate source charge	Q _{gs}	nC		30			
Gate drain charge	Q _{gd}			43			
Gate charge	Q _g			121			
Short-Circuit Withstand Time	t _{sc}	us		6		V _{GS} =-4/18V, V _{DS} =400V, I _{sc} =400A R _G =30Ω	
Internal Gate Resistance	R _{G(int)}	Ω		1.2		f=1MHz, V _{AC} = 25mV	

■ Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	t _{d(on)}	ns		6		V _{DD} =400V, V _{GS} =-4+18V, I _D =20A, L=100uH , R _{G(ext)} = 2.7Ω	Fig.17, 18
Rise time	t _r			14.5			
Turn off delay time	t _{d(off)}			17.5			
Fall time	t _f			8			
Turn on switching energy	E _{on}	uJ		92		V _{GS} =-4/18V, V _{DS} =400V, I _D =20A, L=100uH , R _{G(ext)} = 2.7Ω	Fig.17, 18
Turn off switching energy	E _{off}			16.5			

■ **Body diode characteristics** (T_c=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V _{SD}	V		4.0		V _{GS} =0V, I _{SD} =10A	Fig.8
Continuous diode forward current	I _s	A		40.5		V _{GS} =0V, T _c =25°C	
Reverse recovery time	t _{rr}	nS		58		V _{DS} =400V, V _{GS} =0V, I _{SD} =10A, di/dt=300A/uS	
Reverse recovery charge	Q _{rr}	nC		122			
Peak reverse recovery current	I _{rrm}	A		3.75			

Note 1: When using SiC Body Diode the maximum recommended V_{GS} = -5V

■ **Thermal Characteristics** (T_a=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	R _{θJ-C}	°C/W	0.64

■ **Typical Characteristics**

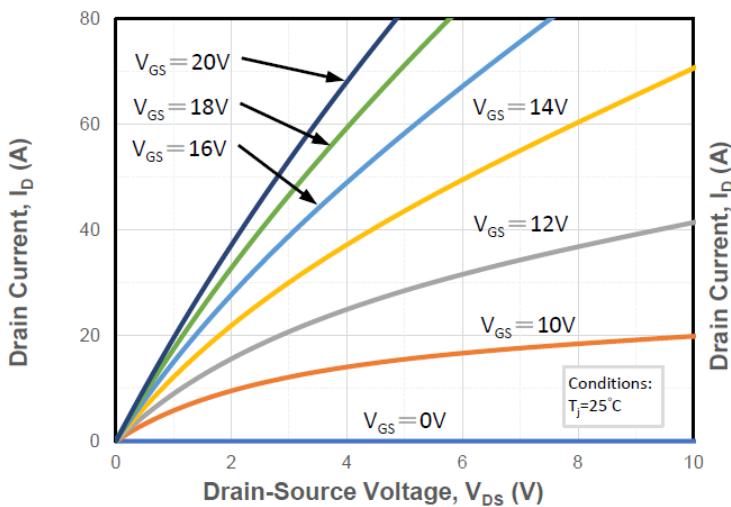


Figure 1. Output Characteristics T_j = 25°C

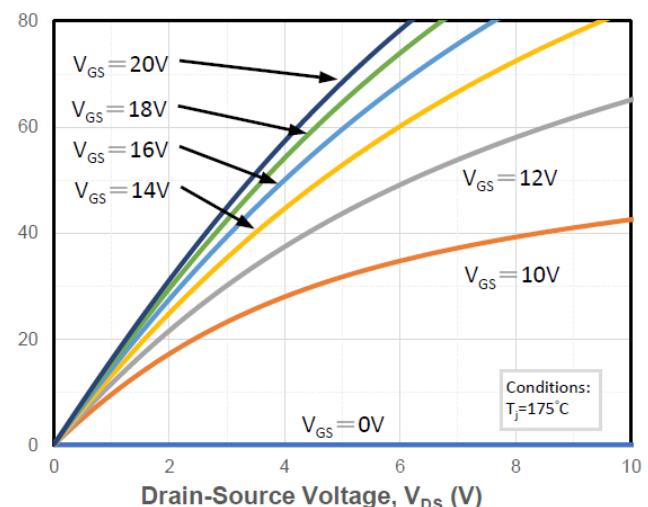
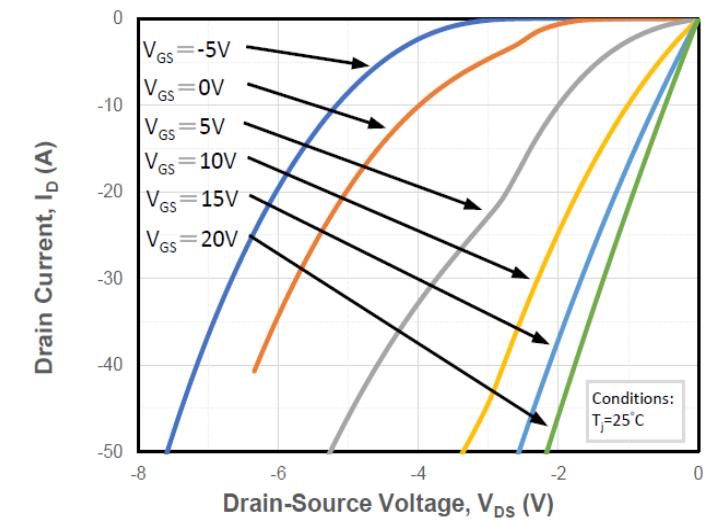
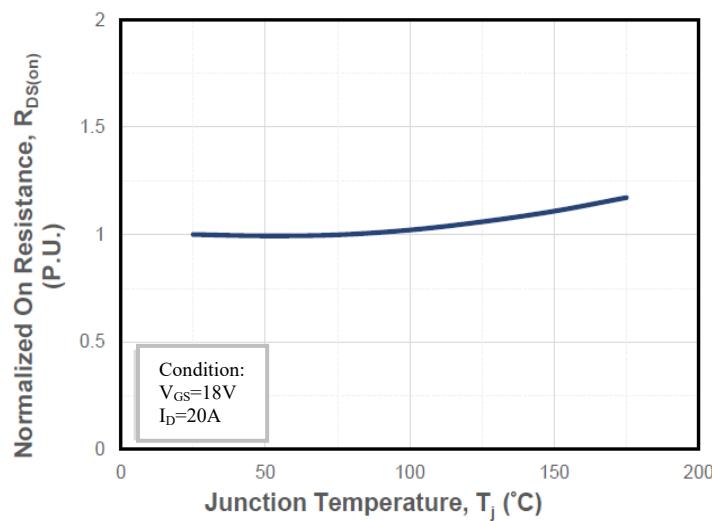
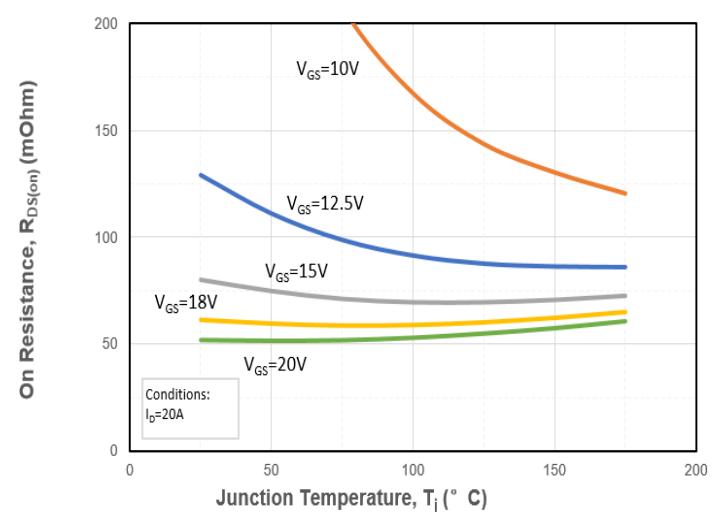
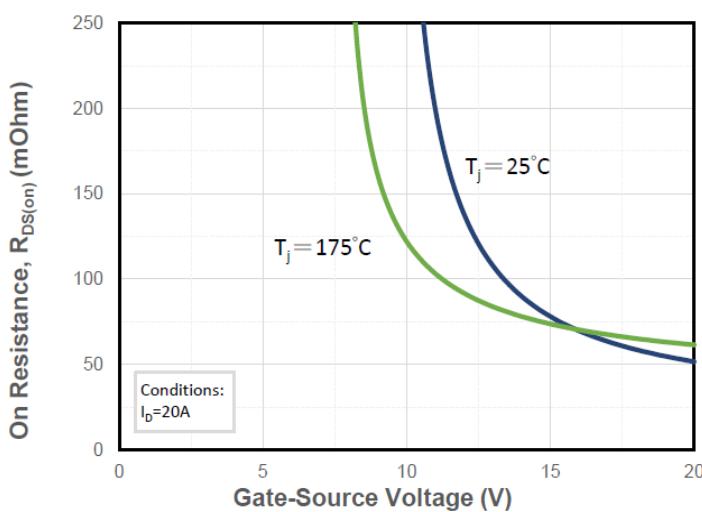
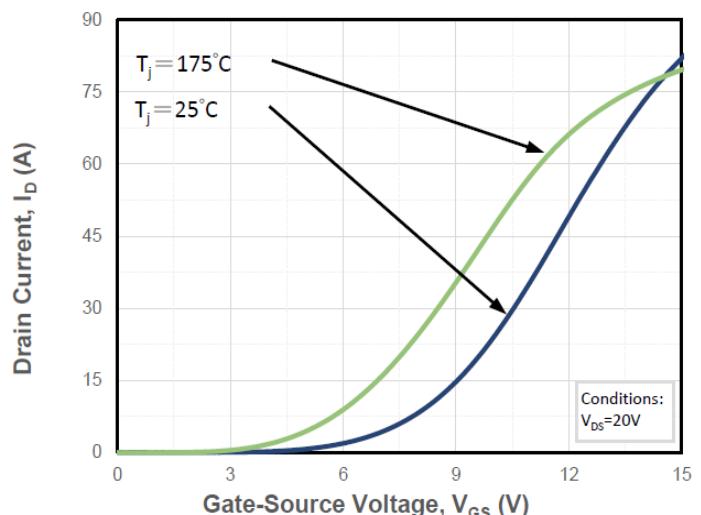
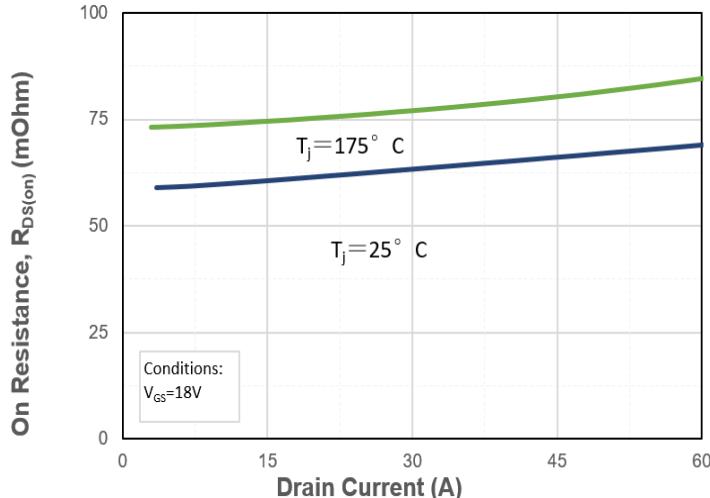


Figure 2. Output Characteristics T_j = 175°C



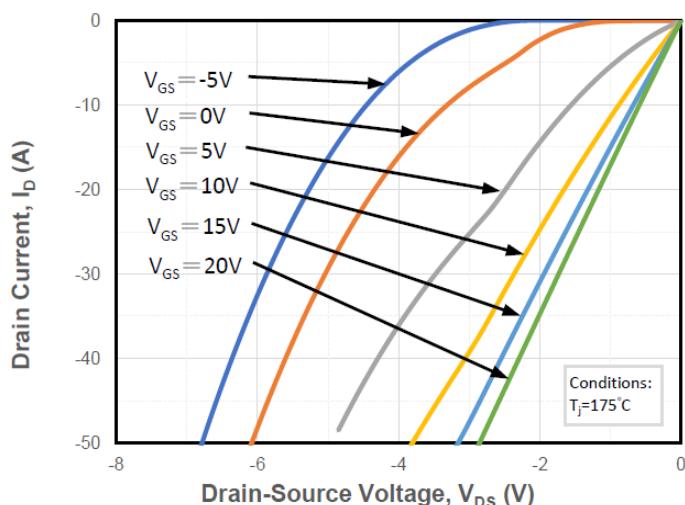


Figure 9. Reverse Output Characteristics at $T_j = 175^\circ\text{C}$

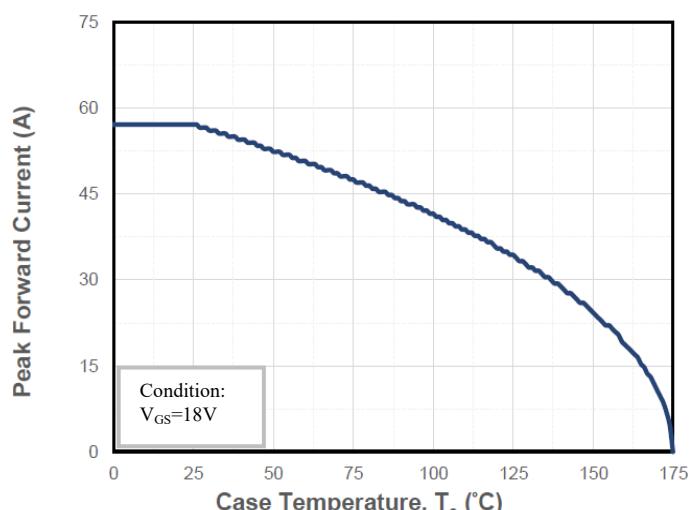
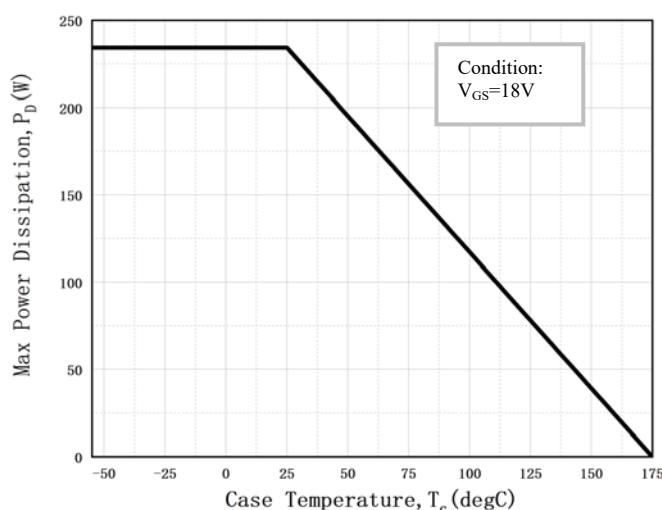
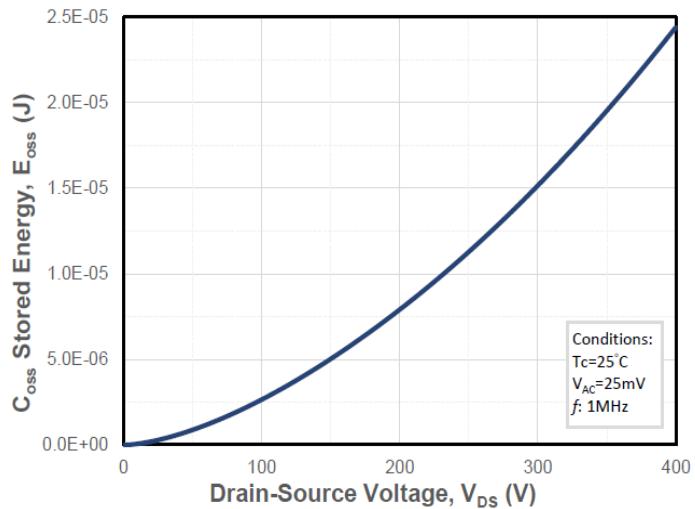
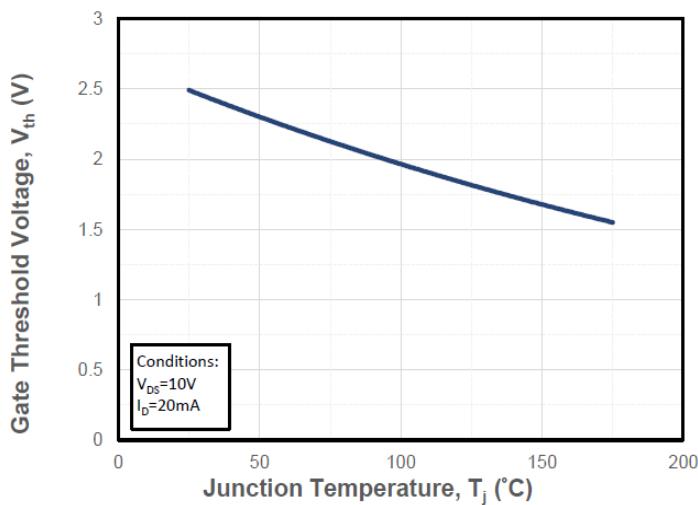
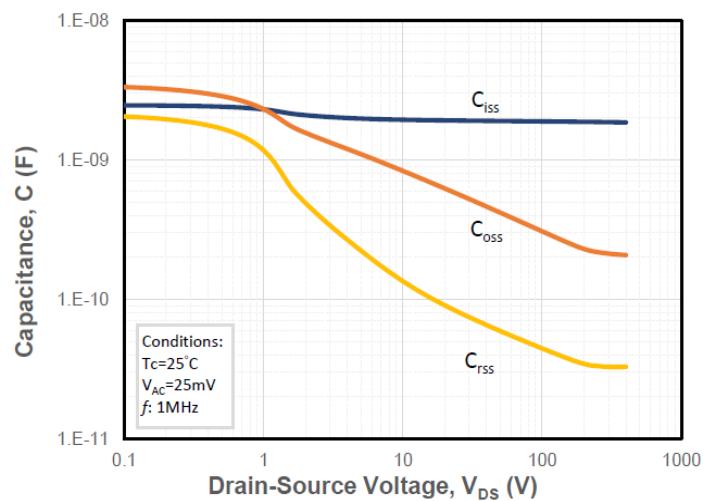


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature Figure 14. Drain Current Derating vs. Case Temperature

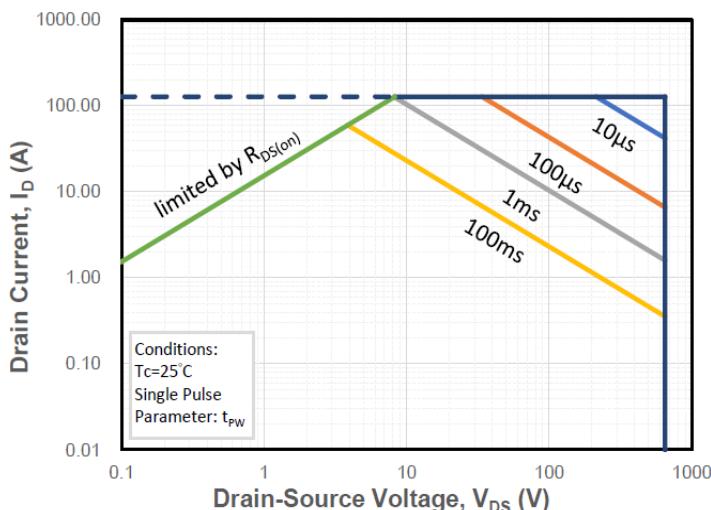


Figure 15. Safe Operating Area

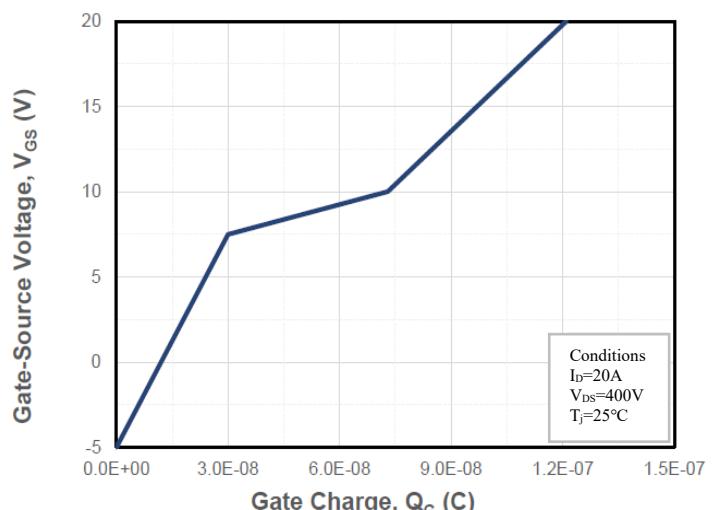


Figure 16. Gate Charge Characteristics

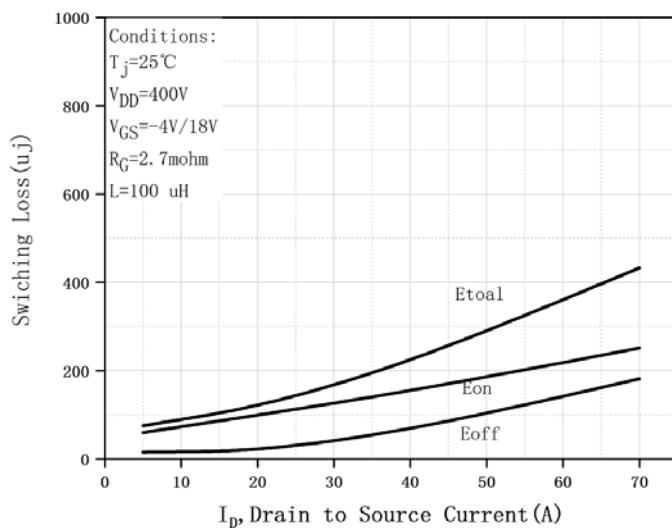


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

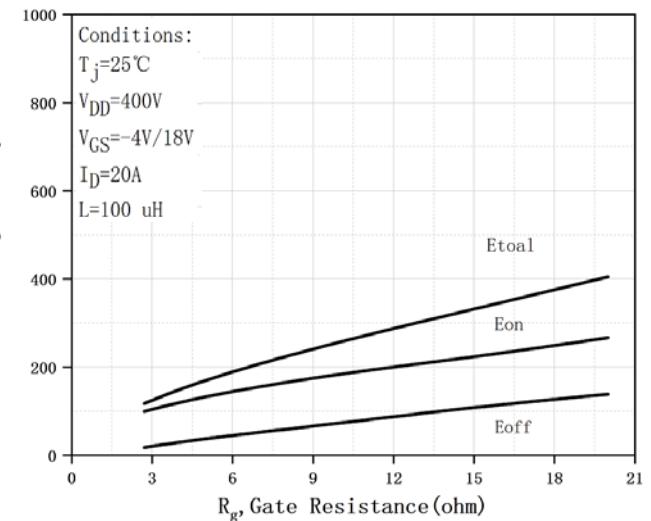


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor ($R_g(\text{ext.})$)

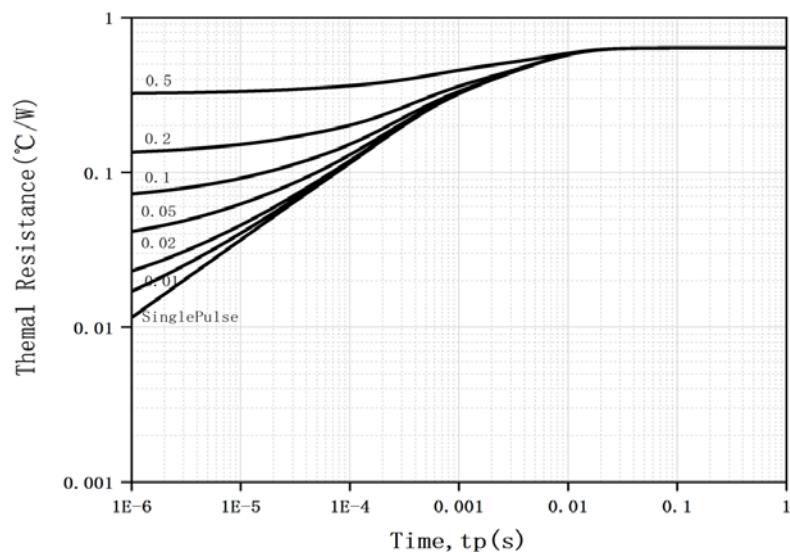


Figure 19. Transient Junction to Case Thermal Impedance

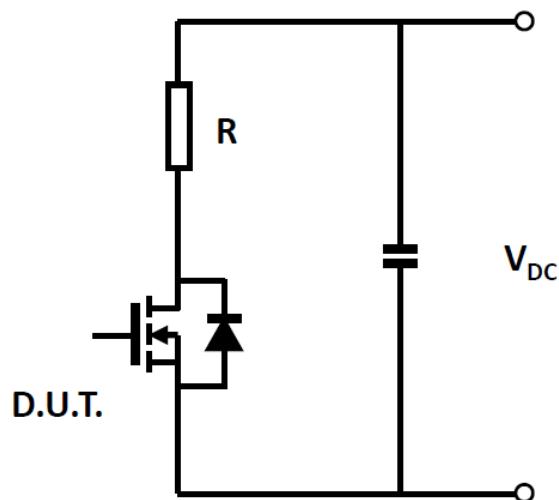


Figure 20. Schematic of Resistive Switching

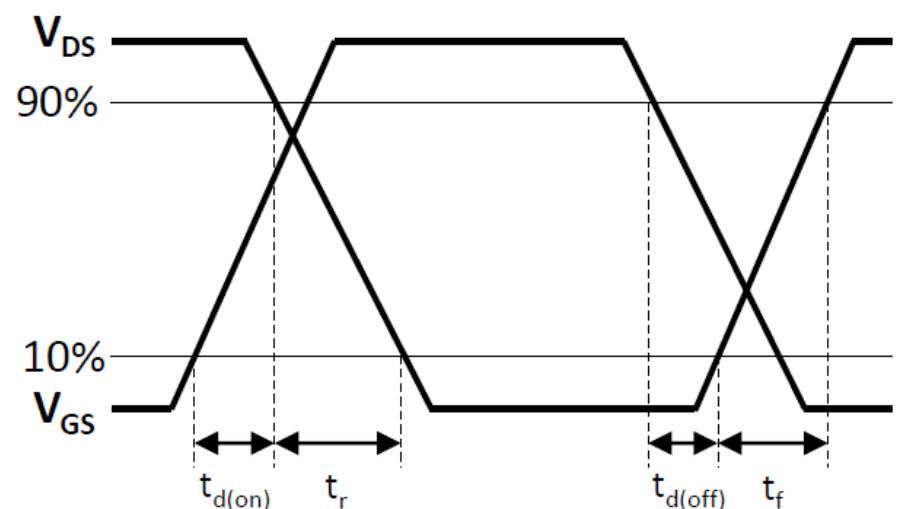
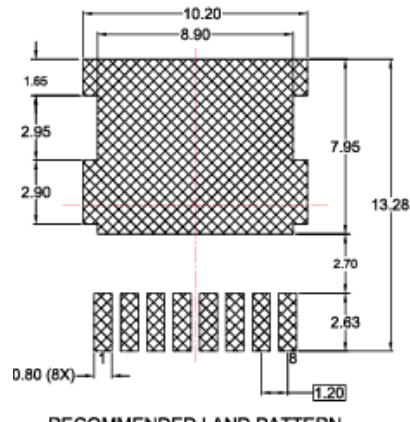
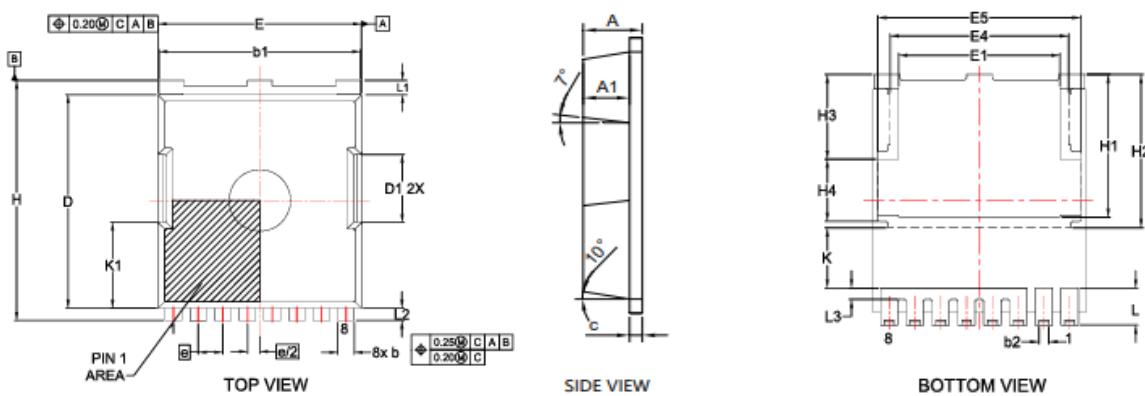


Figure 21. Switching Times Definition

■Outline Dimensions



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	2.20	2.3	2.40
A1	1.70	1.80	1.90
b	0.70	0.80	0.90
b1	0.70	0.80	0.90
b2	0.36	0.46	0.56
c	0.40	0.50	0.60
D	10.28	10.38	10.48
D1		3.30	
E	9.80	9.90	10.00
E1	8.0	8.10	8.20
E4		8.40	
E5		9.40	
e		1.20 BSC	
e2		0.60 BSC	
H	11.58	11.68	11.78
H1	6.56	6.66	6.76
H2	7.05	7.15	7.25
H3		3.50	
H4		3.25	
K	2.70	2.80	2.90
K1		4.18	
L	1.63	1.73	1.83
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L3	0.30	0.40	0.50
E	8 REF		

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