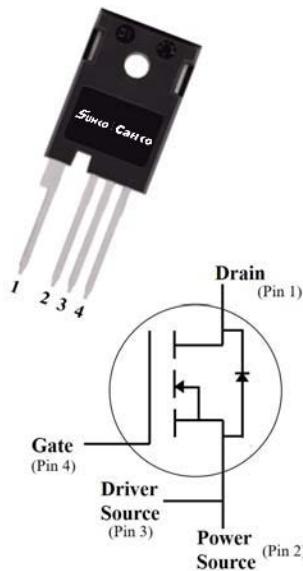


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	1200V
I_D (25°C)	85A
$R_{DS(on)}$	25mΩ

**Features**

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free,

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TO247-4L
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code				D212025NCFGH	
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	1200	$V_{GS}=0\text{ V}$, $I_D=100\mu\text{A}$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-10/+25	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-5/+20	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	I_D	A	85	$V_{GS}=20\text{V}$, $T_c=25^\circ\text{C}$	Fig.14
Continuous drain current @ $T_c=110^\circ\text{C}$			57	$V_{GS}=20\text{V}$, $T_c=110^\circ\text{C}$	
Pulsed drain current	$I_D(\text{pulsed})$	A	360	Pulse width tp limited by $T_{j,\text{max}}$	Fig.15
Avalanche energy, Single Pulse	E_{AS}	J	1.0	$V_{DD}=100\text{V}$, $L=30\text{mH}$	
Power Dissipation	P_{TOT}	W	375	$T_c=25^\circ\text{C}$, $T_j = 175^\circ\text{C}$	Fig.13
Operating junction and Storage temperature range	T_j , T_{stg}	°C	-55 to +175		
Soldering temperature	T_L	°C	260	1.6mm (0.063") from case for 10s	
Mounting torque	T_M	Nm	1.0	M3 screw Maximum of mounting process: 3	

■ Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	V _{GS(th)}	V	1.5	2.3	4.5	V _{DS} =V _{GS} , I _D = 50mA	Fig.4, 11
Drain source breakdown voltage	V _{(BR)DSS}	V	1200			V _{GS} =0, I _D =100uA	
Zero gate voltage drain current	I _{DSS}	uA		<1	50	V _{DS} =1200V, V _{GS} = 0V	
				10	500	V _{DS} =1200V, V _{GS} = 0V, Tj= 175°C	
Gate source leakage current	I _{GSS}	nA			250	V _{GS} = 20V, V _{DS} =0V	
Current drain source on-state resistance	R _{DS ON}	mΩ		25	33	V _{GS} =20V, I _D =40A	Fig.3, 5, 6
				45		V _{GS} =20V, I _D =40A, Tj=175°C	
Transconductance	g _f	S		20		V _{DS} =20V, I _D =40A	

■ Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C _{iss}	pF		5890		V _{DS} =800V, V _{GS} =0V, Tj=25°C, f=1MHz, V _{AC} = 25mV	Fig.10
Output capacitance	C _{oss}			238			
Reverse capacitance	C _{rss}			41			
C _{oss} stored energy	E _{oss}	uJ		96			Fig.12
Gate source charge	Q _{gs}	nC		109		V _{DS} =800V, V _{GS} =-5/20V, I _D =40A	Fig.16
Gate drain charge	Q _{gd}			106			
Gate charge	Q _g			366			
Internal Gate Resistance	R _{G(int)}	Ω		1.0	5.0	f =1MHz, V _{AC} = 25mV	

■ Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on switching energy	t _{d(on)}	ns		17		V _{DD} =800V, V _{GS} =-5/+20V, I _D =40A, L=100uH, R _{G(ext)} = 2.7Ω	Fig.17, 18
Turn off switching energy	t _r			65			
Turn on delay time	t _{d(off)}			50			
Rise time	t _f			22			
Turn off delay time	E _{on}	uJ		1050			
Fall time	E _{off}			341			

■ **Body diode characteristics** (T_c=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V _{SD}	V		4.0		V _{GS} =0V, I _{SD} =20A	Fig.8
Continuous diode forward current	I _s	A		62		V _{GS} =0V, T _c =25°C	
Reverse recovery time	t _{rr}	nS		79		V _{DS} =800V, V _{GS} =0V, I _{SD} =20A, dI/dt=300A/uS	
Reverse recovery charge	Q _{rr}	nC		340			
Peak reverse recovery current	I _{rrm}	A		8.2			

■ **Thermal Characteristics** (T_a=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	R _{θJ-C}	°C /W	0.4

■ **Typical Characteristics**

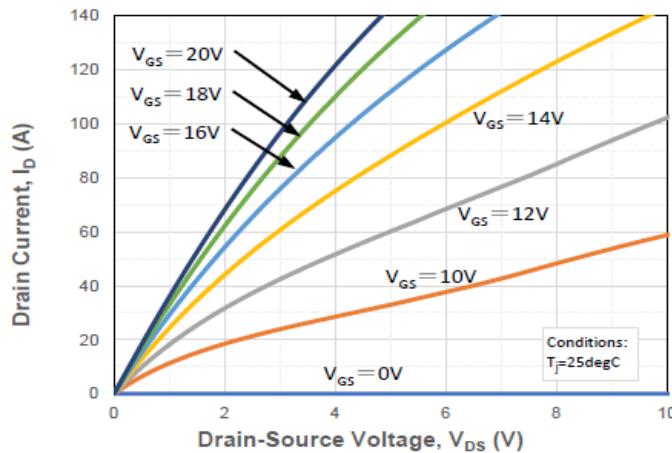


Figure 1. Output Characteristics T_j = 25°C

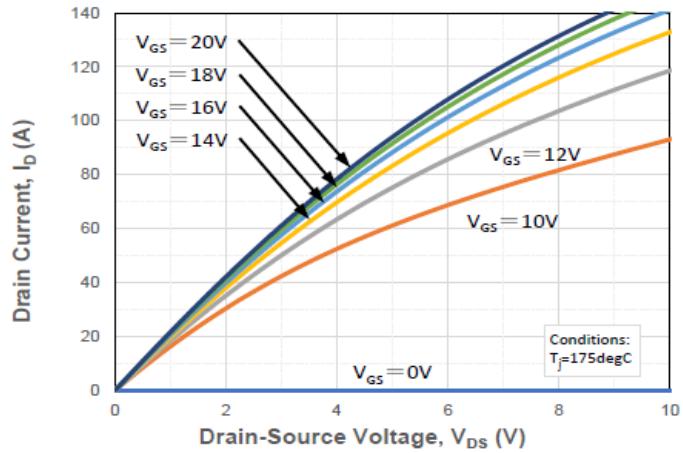


Figure 2. Output Characteristics T_j = 175°C

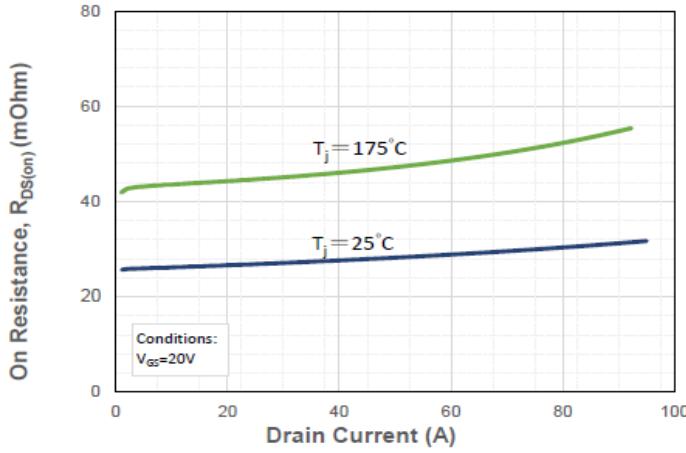


Figure 3. On-resistance vs. drain current

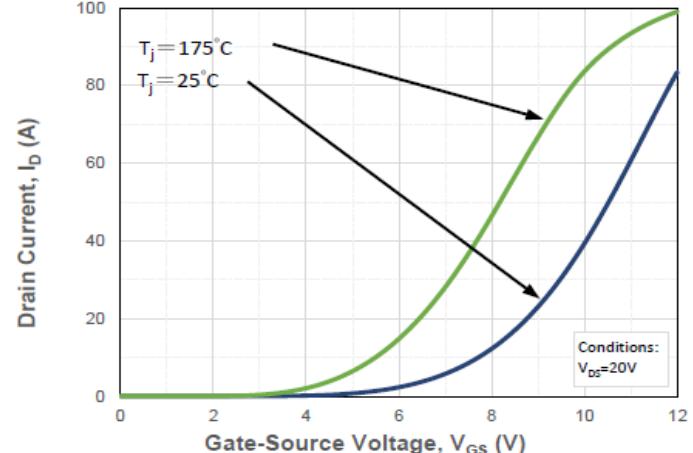


Figure 4. Transfer Characteristics for various T_j

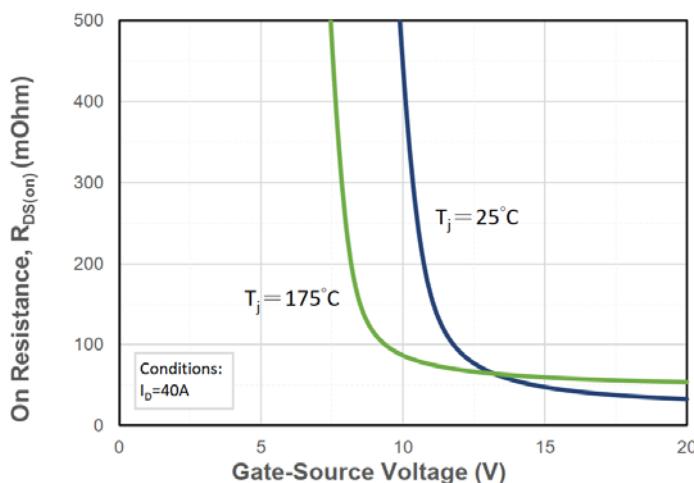


Figure 5. On-resistance vs. gate voltage for various T_j

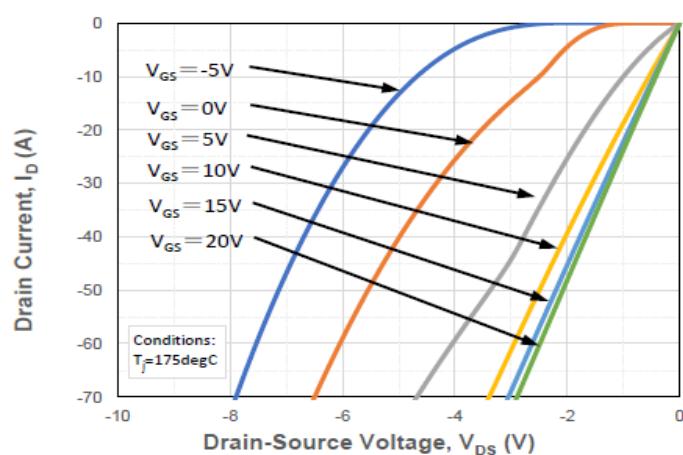
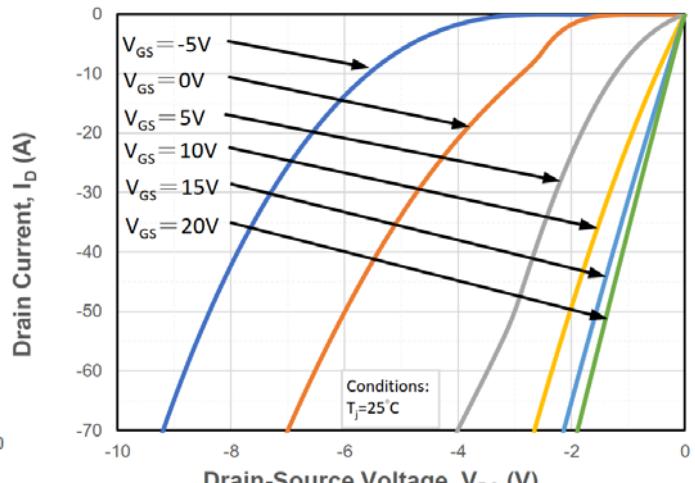
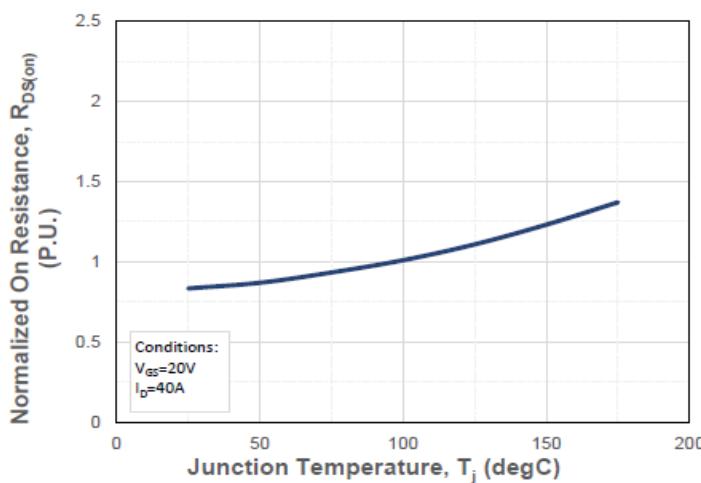
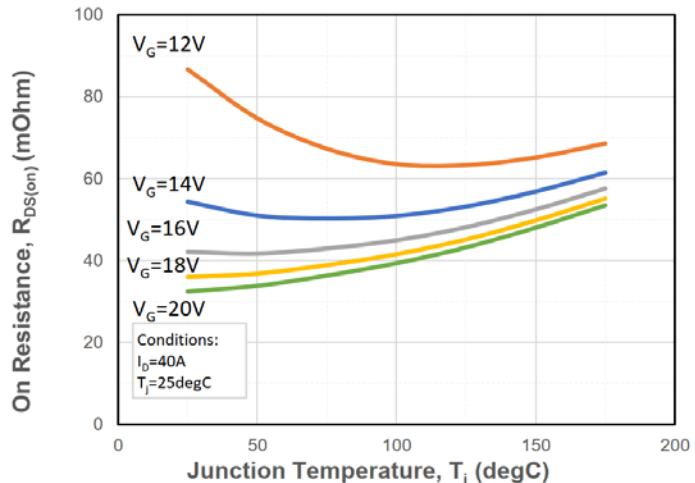
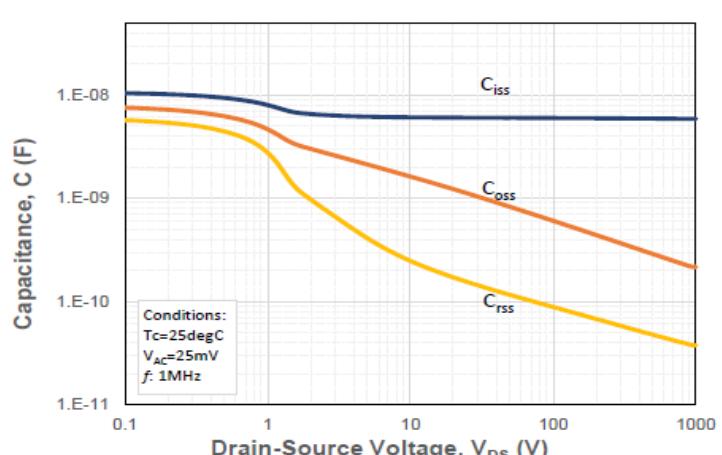


Figure 9. Reverse Output Characteristics at $T_j = 175^\circ\text{C}$



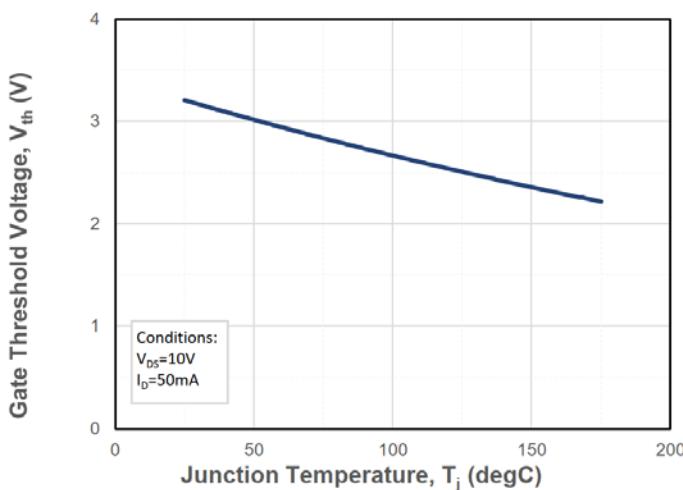


Figure 11. Threshold voltage vs. temperature

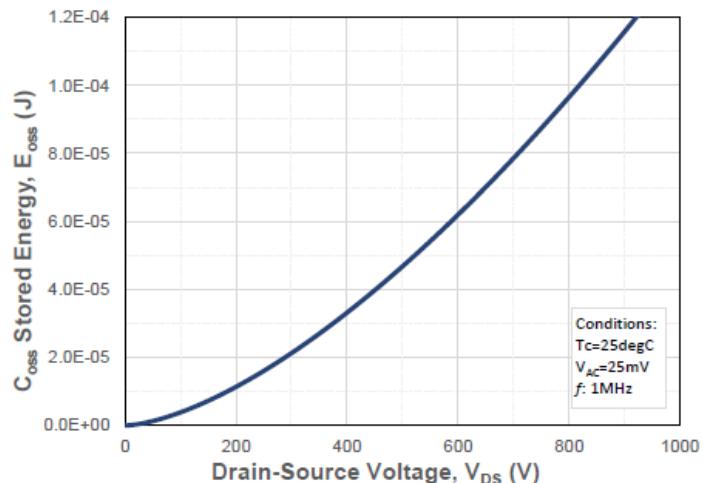


Figure 12. Output Capacitor Stored Energy

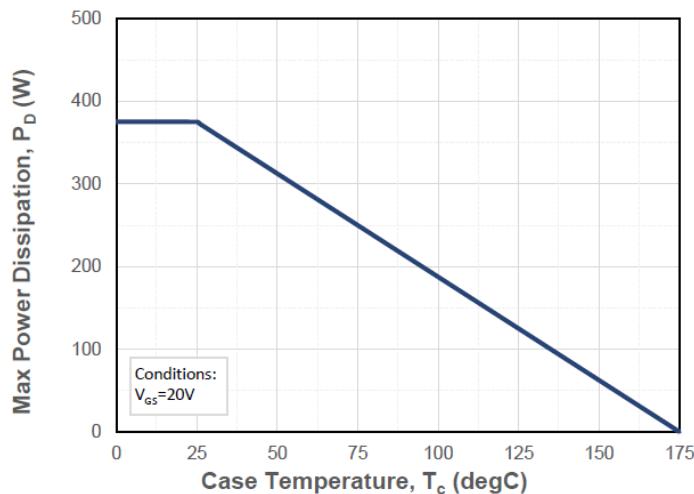


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

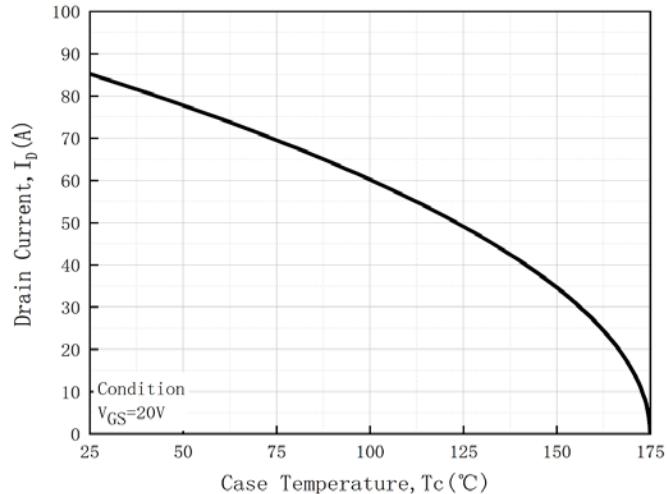


Figure 14. Drain Current Derating vs. Case Temperature

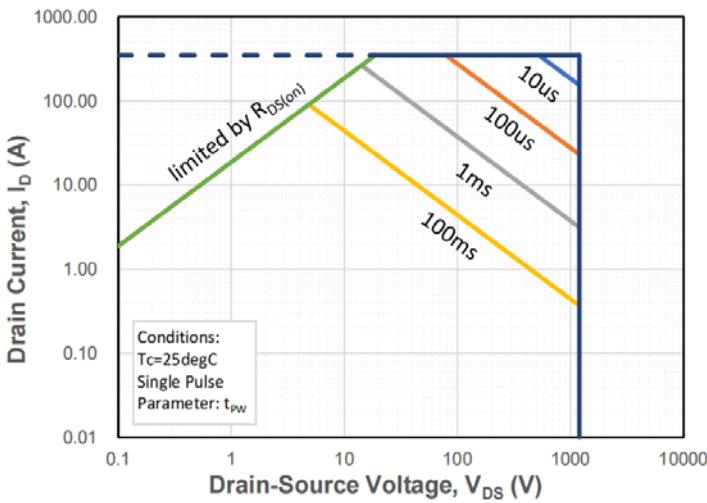


Figure 15. Safe Operating Area

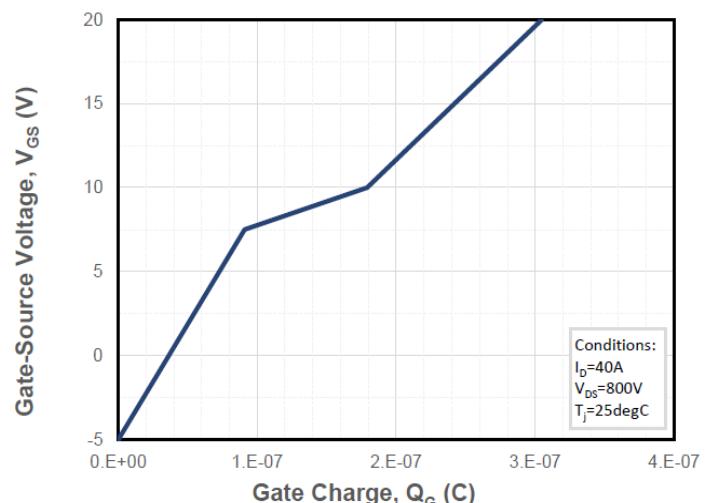


Figure 16. Gate Charge Characteristics

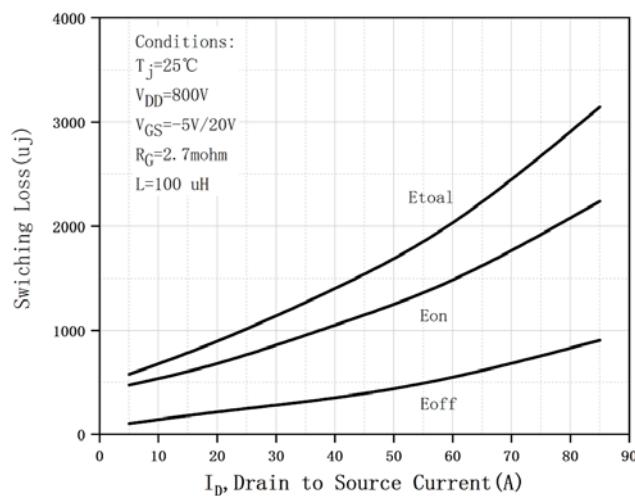


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

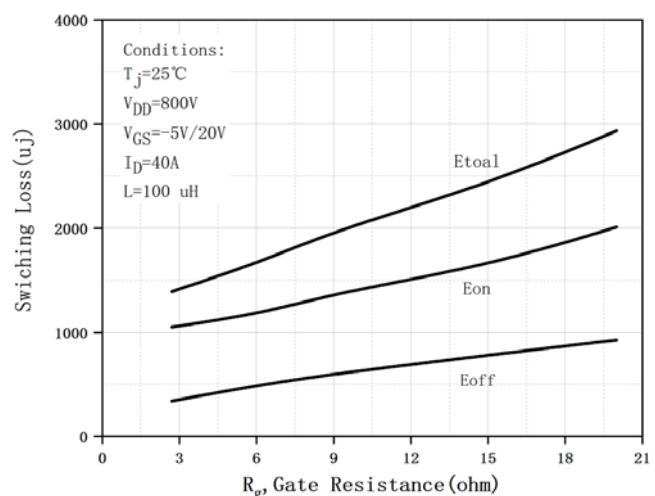


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor (R_G(ext.))

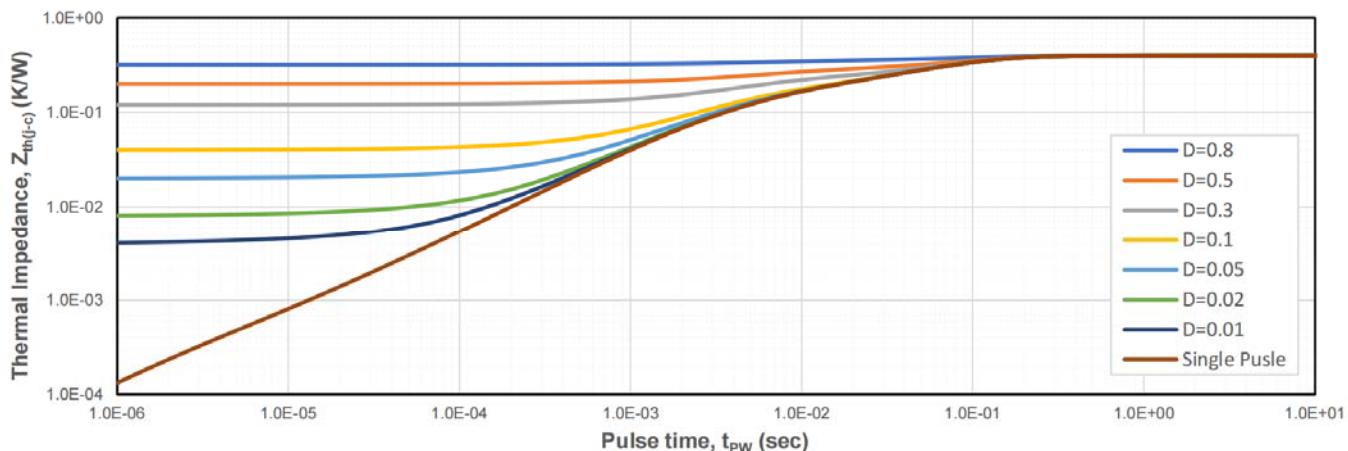


Figure 19. Transient Junction to Case Thermal Impedance

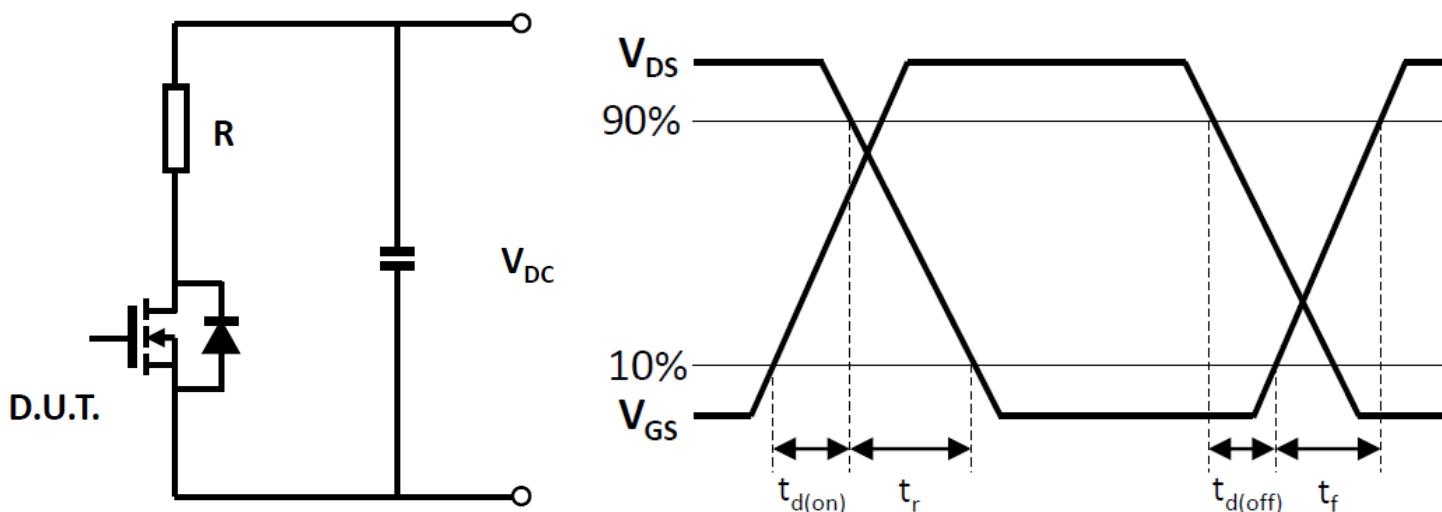
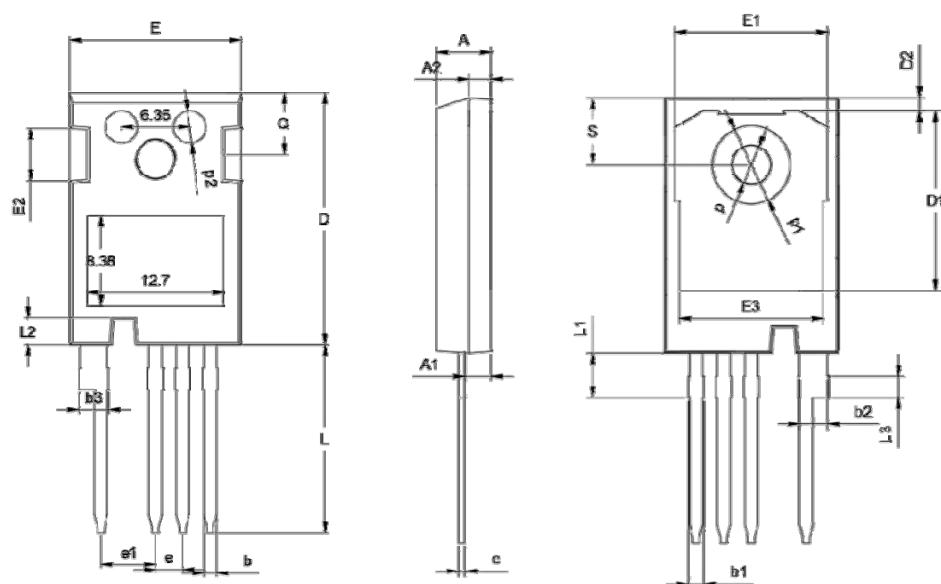


Figure 20. Schematic of Resistive Switching

Figure 21. Switching Times Definition

■Outline Dimensions



TO247-4L			
Dim	Min	Norm	Max
A	4.80	5.00	5.20
A1	2.30	2.40	2.50
A2	1.88	1.98	2.08
b	1.10	1.20	1.30
b1	1.20	/	1.50
b2	2.35	2.55	2.75
b3	2.45	/	2.85
c	0.55	0.60	0.65
D	23.3	23.45	23.6
D1	16.25	16.55	16.85
D2	1.00	/	1.30
e	TYP2.54		
e1	TYP5.06		
E	15.75	15.90	16.05
E1	13.80	/	14.20
E2	4.40	4.75	5.10
E3	13.00	/	13.45
L	17.34	17.49	17.64
L1	4.00	/	4.30
L2	2.35	/	2.65
L3	TYP1.98		
Q	5.60	5.80	6.00
S	6.05	/	6.30
p	TYP3.58		
p1	TYP7.18		
p2	TYP3.00		

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