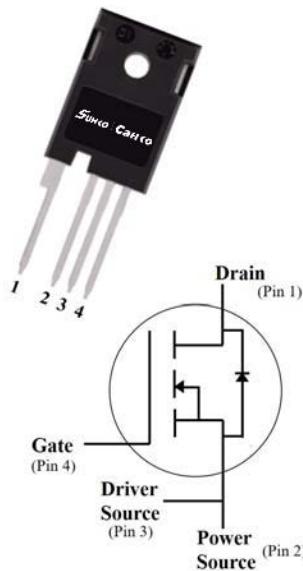


Silicon Carbide Power MOSFET (N-Channel Enhancement)

V_{DS}	1200V
$I_D(25^\circ C)$	39A
$R_{DS(on)}$	80mΩ

**Features**

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free,

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TO247-4L
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ C$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code				D212080NCFG1	
Drain source voltage @ $T_j=25^\circ C$	$V_{DS,max}$	V	1200	$V_{GS}=0 V, I_D=100\mu A$	
Gate source voltage @ $T_j=25^\circ C$	$V_{GS,max}$	V	-8/+22	Absolute maximum values	Note1
Gate source voltage @ $T_j=25^\circ C$	$V_{GS,op}$	V	-4/+18	Recommended operational values	Note2
Continuous drain current @ $T_c=25^\circ C$	I_D	A	39	$V_{GS}=18V, T_c=25^\circ C$	Fig.18
Continuous drain current @ $T_c=100^\circ C$			28	$V_{GS}=18V, T_c=100^\circ C$	
Pulsed drain current	$I_{D(pulsed)}$	A	80	Pulse width t_p limited by $T_{j,max}$	Fig.23
Power Dissipation	P_{TOT}	W	214	$T_c=25^\circ C, T_j = 175^\circ C$	Fig.17
Operating junction and Storage temperature range	T_j, T_{stg}	°C	-55 to +175		
Soldering temperature	T_L	°C	260	1.6mm (0.063") from case for 10s	
Mounting torque	T_M	Nm	0.6	M3 screw Maximum of mounting process: 3	

■ Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	2.3	2.9	3.6	$V_{DS}=V_{GS}$, $I_D=5\text{mA}$	Fig.4, 11
				2.2		$V_{DS}=V_{GS}$, $I_D=5\text{mA}$, $T_j=175^\circ\text{C}$	
Drain source breakdown voltage	$V_{(BR)DSS}$	V	1200			$V_{GS}=0$, $I_D=100\mu\text{A}$	
Zero gate voltage drain current	I_{DSS}	μA		1	10	$V_{DS}=1200\text{V}$, $V_{GS}=0\text{V}$	Fig.16
Gate source leakage current	I_{GSS}	nA			100	$V_{GS}=18\text{V}$, $V_{DS}=0\text{V}$	
Current drain source on-state resistance	$R_{DS\text{ ON}}$	$\text{m}\Omega$		77	85	$V_{GS}=18\text{V}$, $I_D=20\text{A}$	Fig.5, 6, 7
				122		$V_{GS}=18\text{V}$, $I_D=20\text{A}$, $T_j=175^\circ\text{C}$	
Internal gate resistance	R_g	Ω		3.1		$f=1\text{MHz}$	
Diode forward voltage	V_{SD}	V		3.9		$V_{GS}=-4\text{V}$, $I_{SD}=10\text{A}$	Fig.8
				3.2		$V_{GS}=0\text{V}$, $I_{SD}=10\text{A}$ $T_j=175^\circ\text{C}$	Fig.9
Transconductance	g_f	S		10		$V_{DS}=16\text{V}$, $I_D=20\text{A}$	Fig.4
				9.2		$V_{DS}=16\text{V}$, $I_D=20\text{A}$, $T_j=175^\circ\text{C}$	

■ Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	C_{iss}	pF		890		$V_{DS}=1000\text{V}$, $V_{GS}=0\text{V}$, $T_j=25^\circ\text{C}$, $f=1\text{MHz}$, $V_{AC}=25\text{mV}$	Fig.13, 14
Output capacitance	C_{oss}			58			
Reverse capacitance	C_{rss}			4			
Coss stored energy	E_{oss}	μJ		34		$V_{DS}=800\text{V}$, $V_{GS}=-4/18\text{V}$, $I_D=20\text{A}$	Fig.15
Gate source charge	Q_{gs}	nC		12			Fig.12
Gate drain charge	Q_{gd}			11			
Gate charge	Q_g			41			

■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on switching energy	E _{on}	uJ		112		V _{DD} =800V, V _{GS} =-4/+18V, I _D =20A, R _g =0Ω, L=120uH	Fig.21, 22
Turn off switching energy	E _{off}			25			
Turn on delay time	t _{d(on)}	ns		8.8		V _{DD} =800V, V _{GS} =-4/+18V, I _D =20A, R _g =0Ω, L=220uH	Fig.21, 22
Rise time	t _r			10.5			
Turn off delay time	t _{d(off)}	ns		15		V _{DD} =800V, V _{GS} =-4/+18V, I _D =20A, R _g =0Ω, L=220uH	Fig.21, 22
Fall time	t _f			8.4			

■Body diode characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V _{SD}	V		3.9		V _{GS} =-4V, I _{SD} =10A	Fig.8
				3.2		V _{GS} =0V, I _{SD} =10A, T _j =175°C	Fig.9
Continuous diode forward current	I _s	A		39		Tc=25°C	Note1
Reverse recovery time	trr	nS		41		V _R =800V, V _{GS} =-4V, I _D =20A, dI/dt=700A/uS	
Reverse recovery charge	Qrr	nC		405			
Peak reverse recovery current	Irrm	A		20			

Note 1: When using SiC Body Diode the maximum recommended V_{GS} = -4V

Note 2: MOSFET can also safely operate at 0/18 V

■Thermal Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	R _{θJ-C}	°C/W	0.7

■ Typical Characteristics

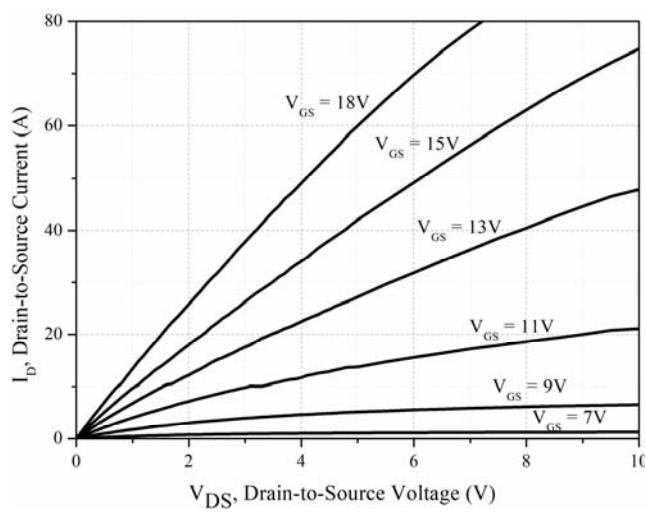
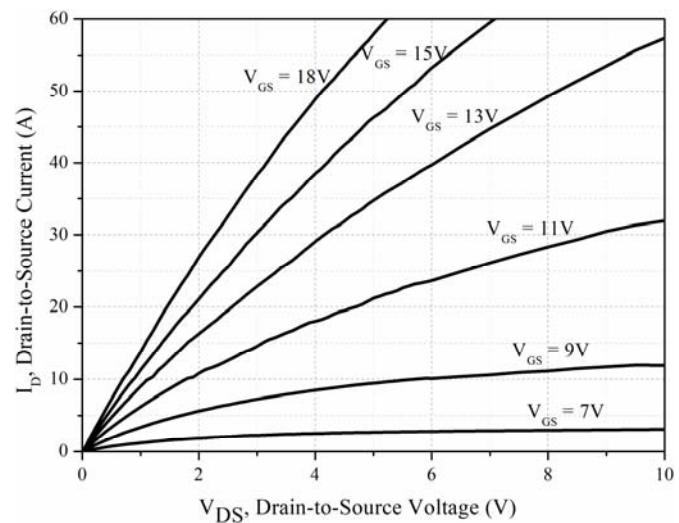
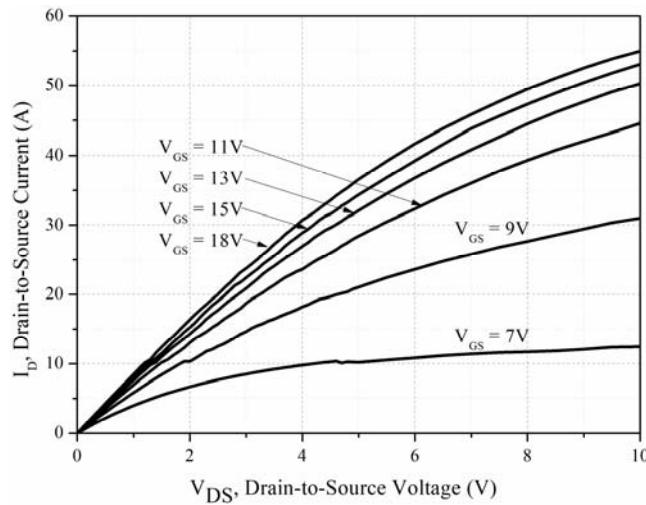
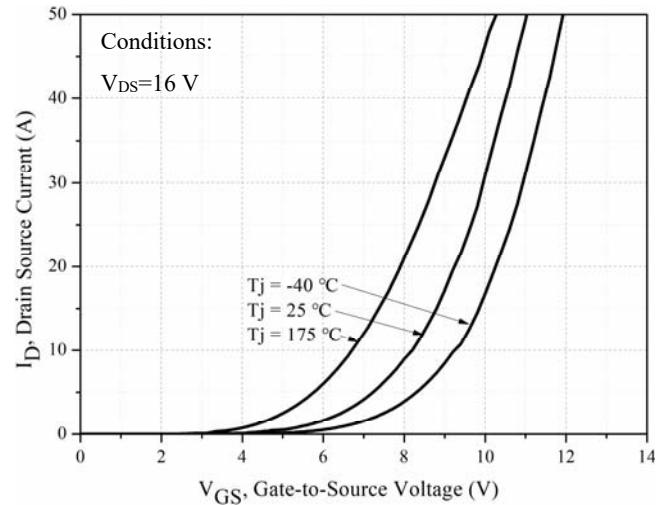
Figure 1. Output Characteristics T_j = -40°CFigure 2. Output Characteristics T_j = 25°CFigure 3. Output Characteristics T_j = 175°C

Figure 4. Transfer Characteristics for various junction temperature

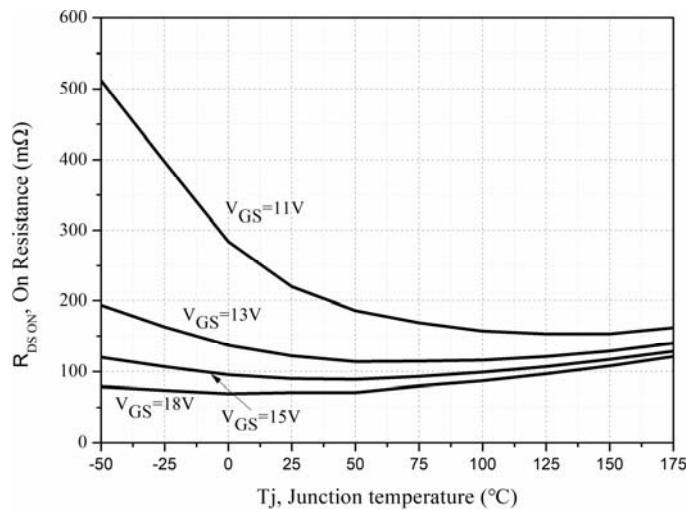


Figure 5. On-resistance vs. temperature for various gate voltage

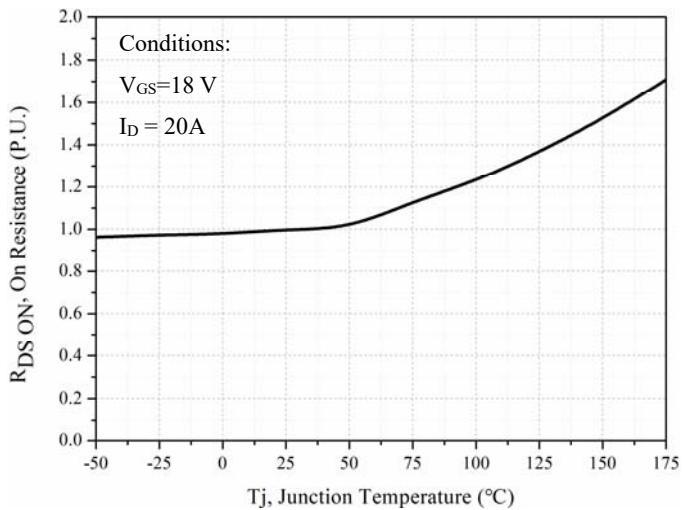


Figure 6. Normalized on-resistance vs. temperature

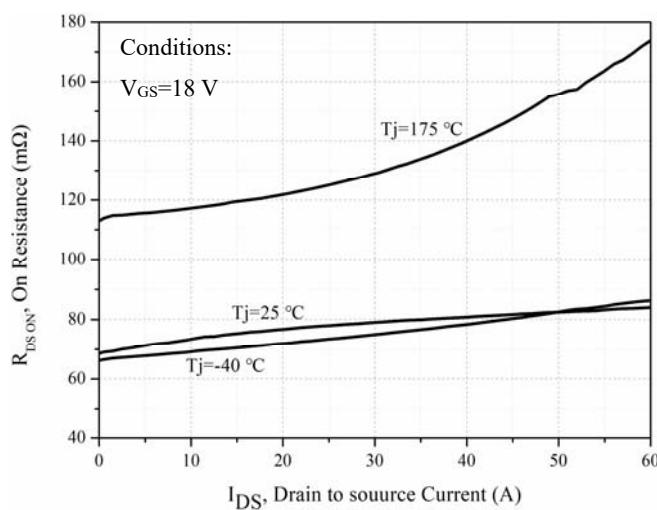


Figure 7. On-resistance vs. drain current

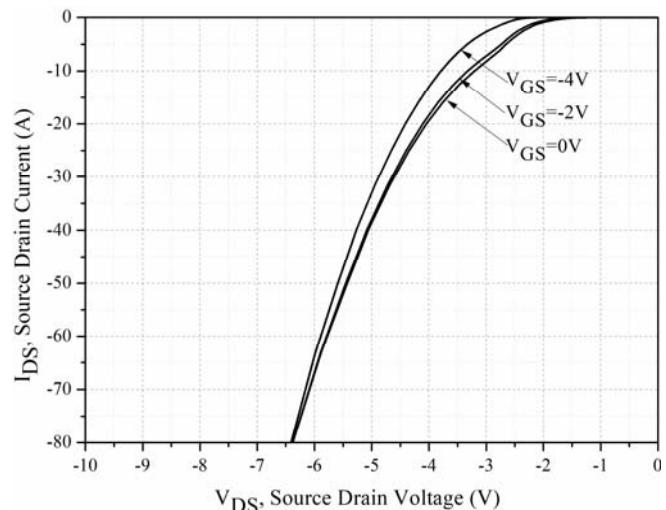
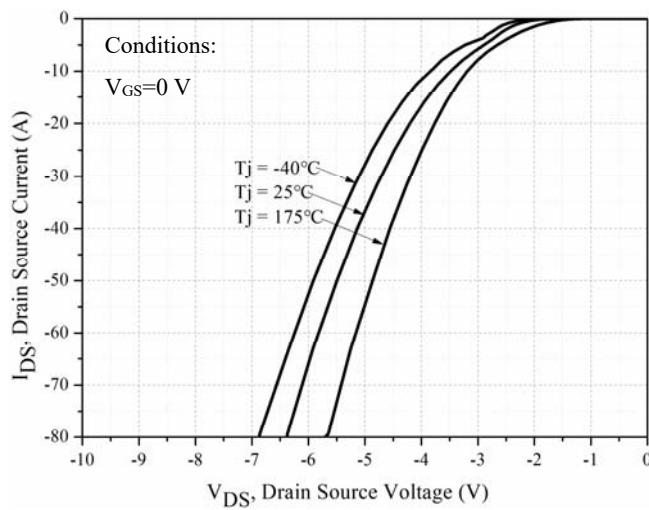
Figure 8. Body diode characteristic at $T_j = 25 \text{ }^{\circ}\text{C}$ 

Figure 9. Body diode characteristic

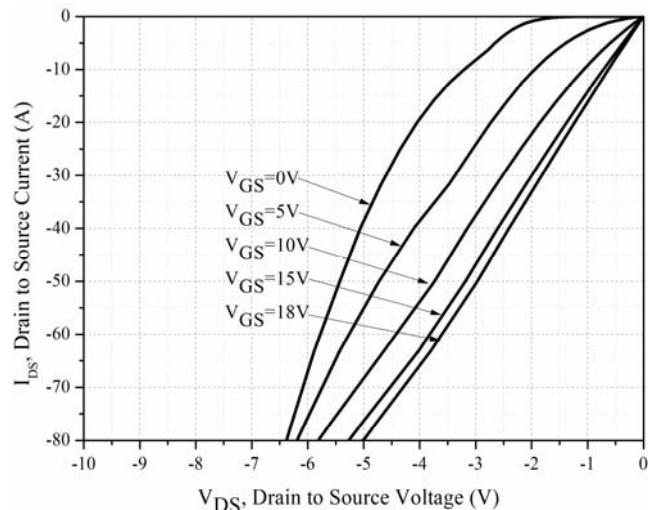
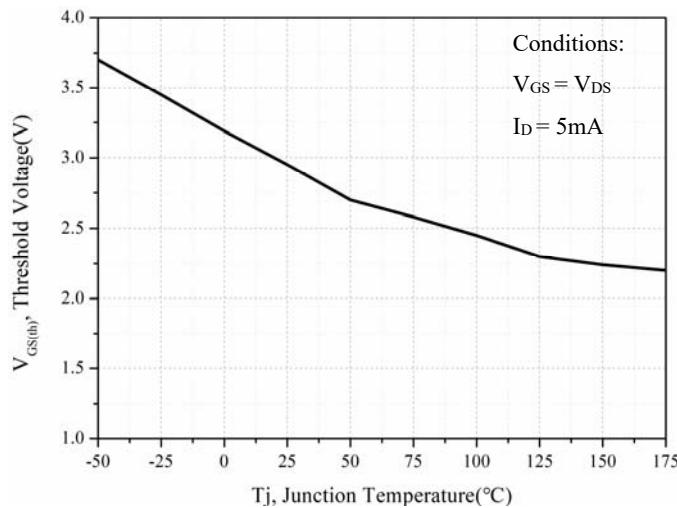
Figure 10. 3rd quadrant characteristic at $T_j = 25 \text{ }^{\circ}\text{C}$ 

Figure 11. Threshold voltage vs. temperature

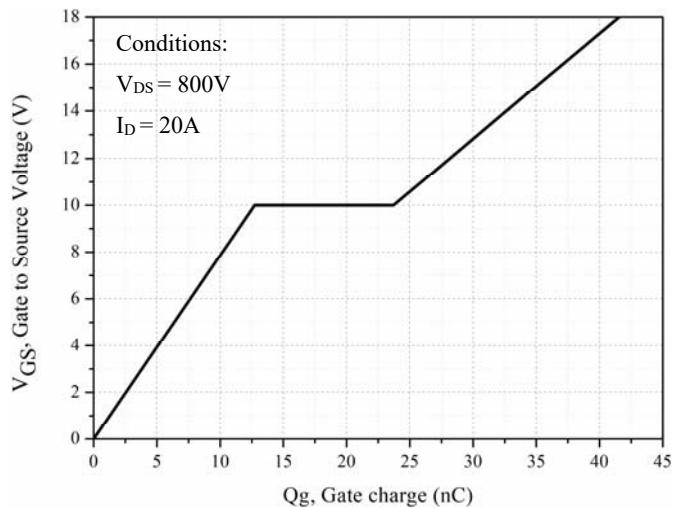


Figure 12. Gate charge characteristic

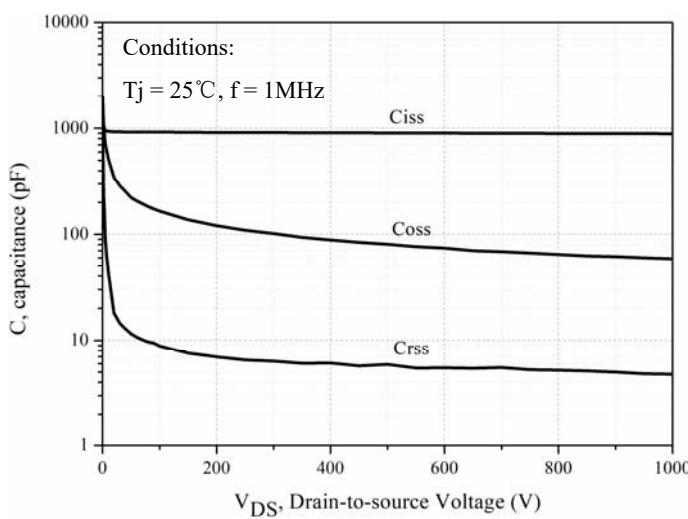


Figure 13. Capacitances vs. drain source voltage (0-1000V)

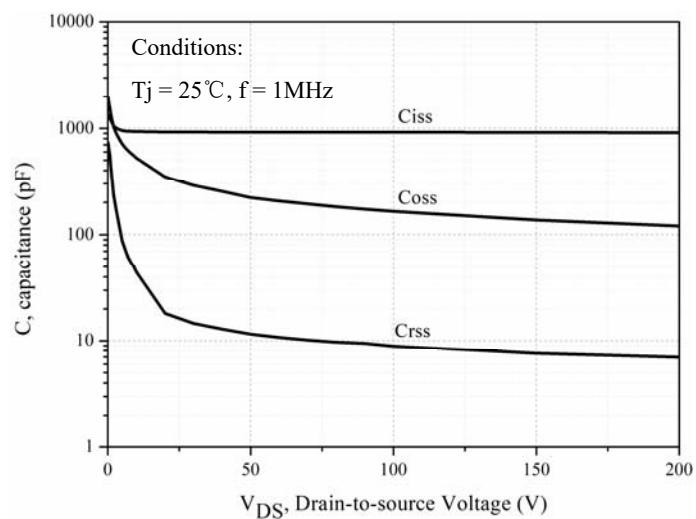


Figure 14. Capacitances vs. drain source voltage (0-200V)

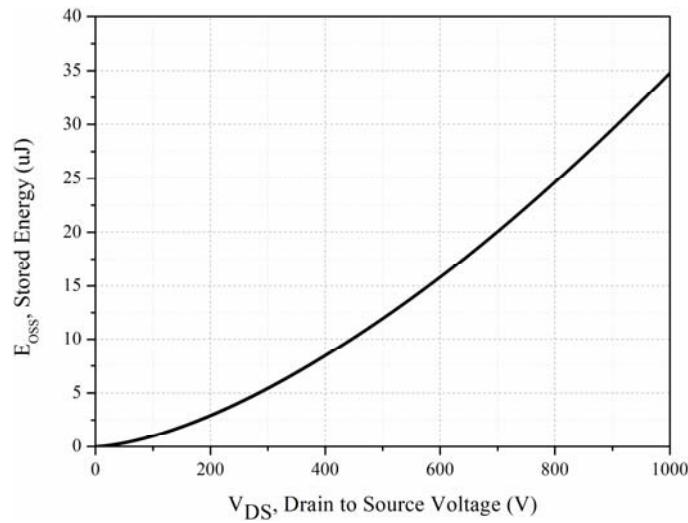


Figure 15. Output capacitor stored energy

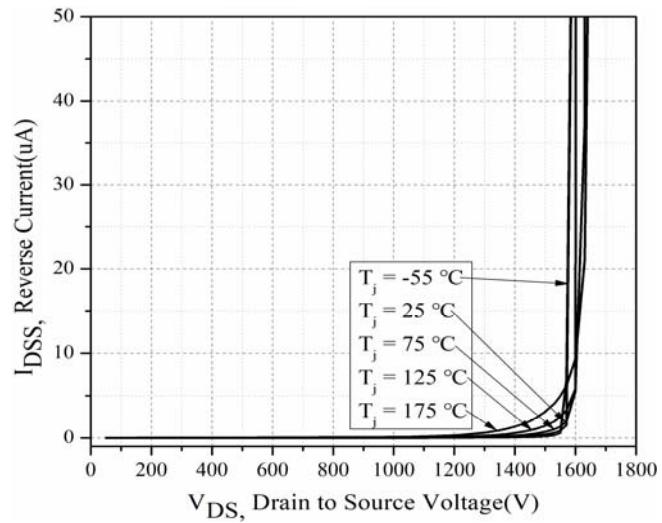
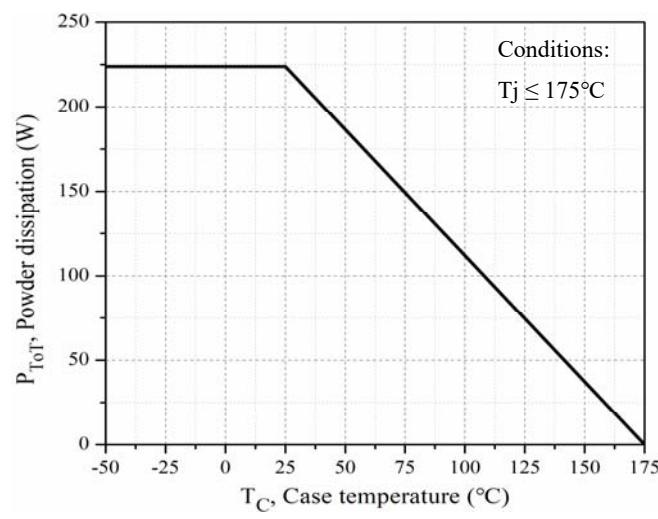
Figure 16. Reverse characteristics vs. T_j 

Figure 17. Maximum power dissipation derating vs. case temperature

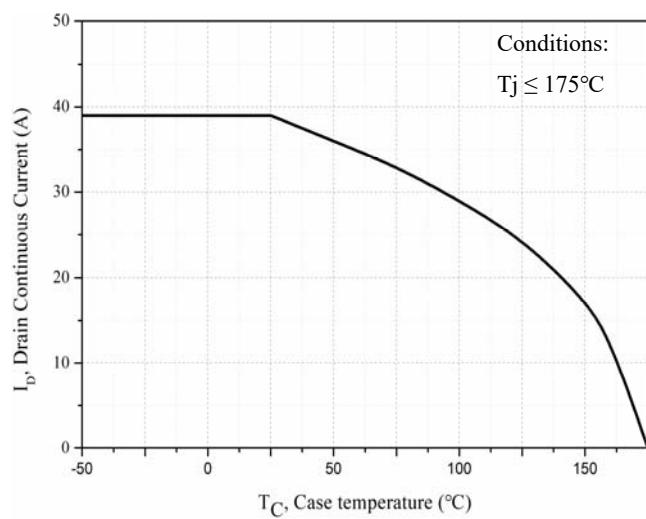
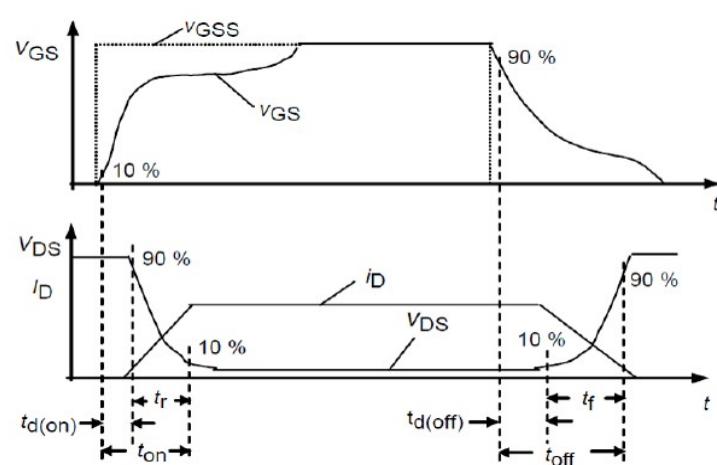
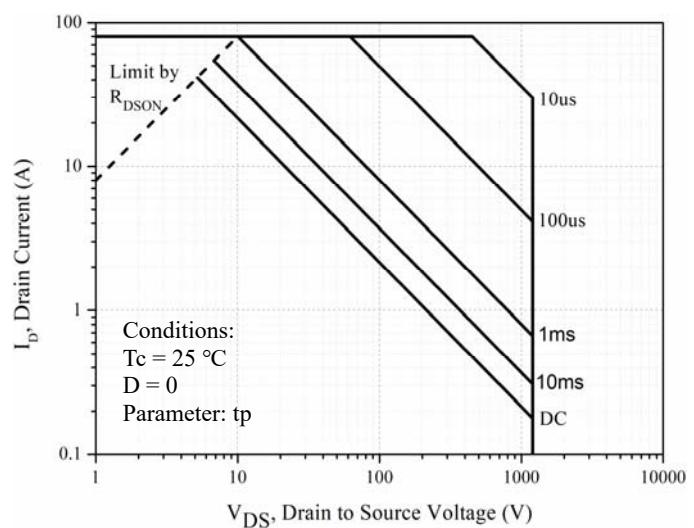
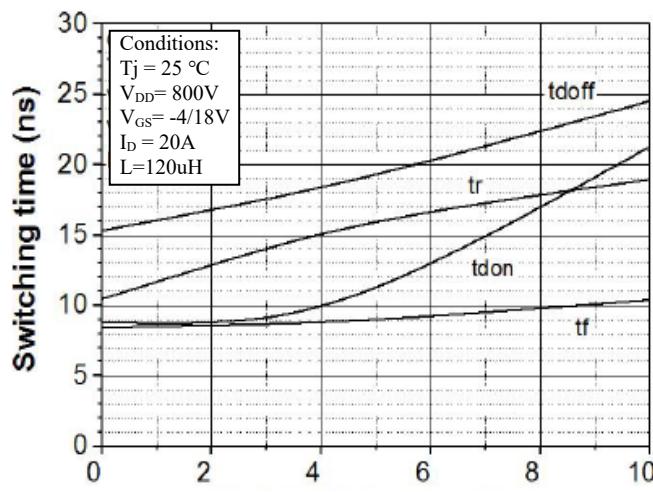
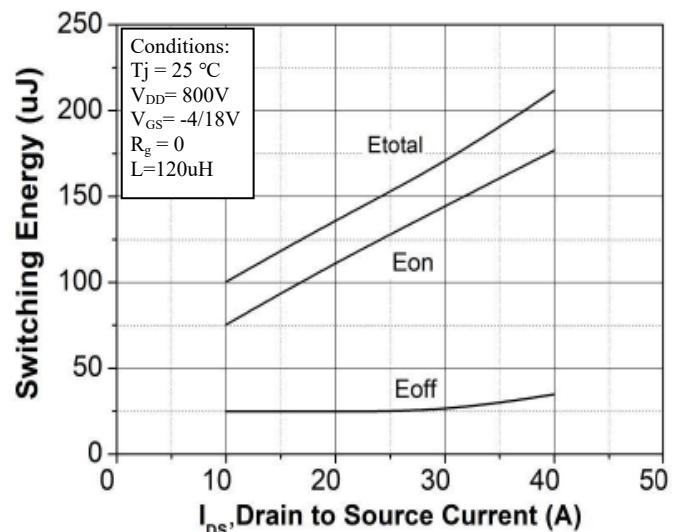
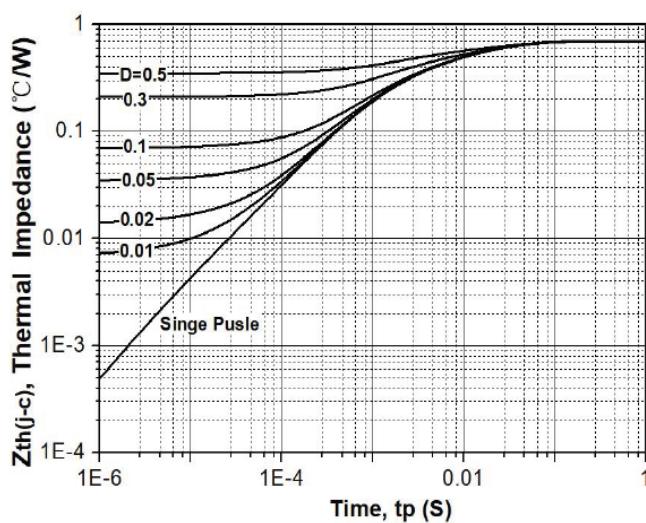


Figure 18. Continuous drain current derating vs. case temperature



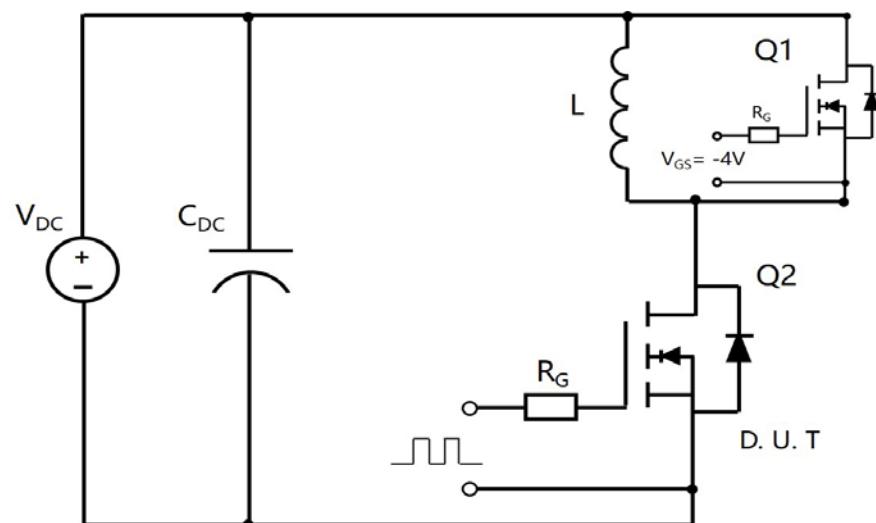
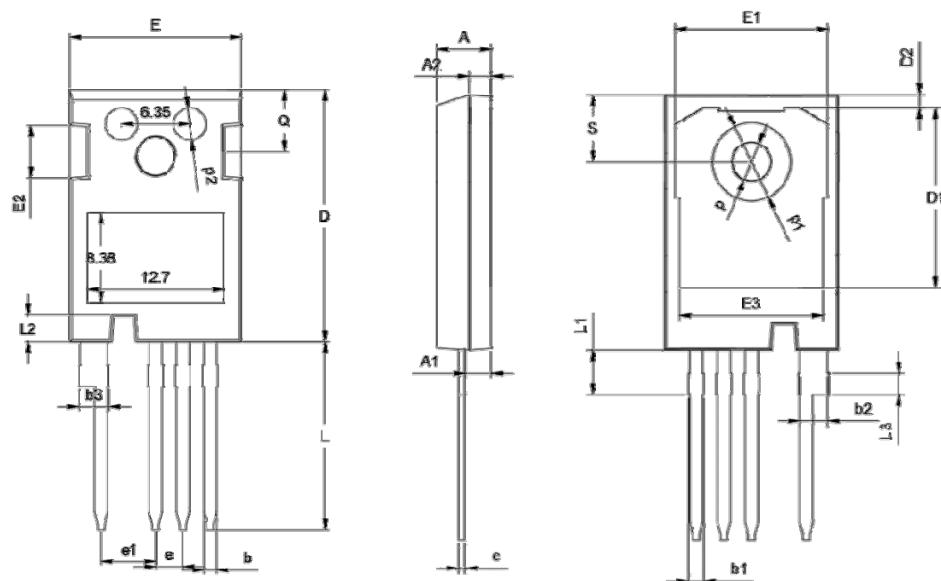


Figure 24. Clamped Inductive Switching Waveform Test Circuit

■Outline Dimensions



TO247-4L			
Dim	Min	Norm	Max
A	4.80	5.00	5.20
A1	2.30	2.40	2.50
A2	1.88	1.98	2.08
b	1.10	1.20	1.30
b1	1.20	/	1.50
b2	2.35	2.55	2.75
b3	2.45	/	2.85
c	0.55	0.60	0.65
D	23.3	23.45	23.6
D1	16.25	16.55	16.85
D2	1.00	/	1.30
e	TYP2.54		
e1	TYP5.06		
E	15.75	15.90	16.05
E1	13.80	/	14.20
E2	4.40	4.75	5.10
E3	13.00	/	13.45
L	17.34	17.49	17.64
L1	4.00	/	4.30
L2	2.35	/	2.65
L3	TYP1.98		
Q	5.60	5.80	6.00
S	6.05	/	6.30
p	TYP3.58		
p1	TYP7.18		
p2	TYP3.00		

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