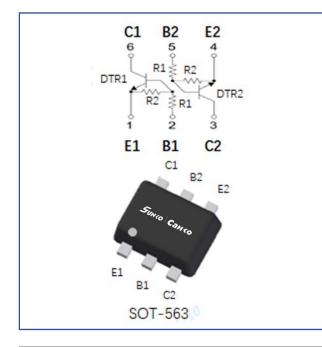


Dual NPN Digital Transistors (Built-in Resistors)



Features

- Moisture sensitivity level 1
- Halogen free and
- Surface mount package ideally suited for auto matic Insertion

Application

- Signal amplification
- Switching circuit

Mechanical data

- Package: SOT-563
- **Terminals**: Tin plated leads, solderable per J-STD-002 and JESD22-B102

■ Maximum Ratings (T_a=25°C Unless otherwise specified)

Item	Symbol	Unit	Conditions	Value
Device marking code				H9
Supply voltage	V _{cc}	V		50
Input voltage	V _{IN}	V		-6 to +40
Output current	lo	mA		100
Power dissipation	P _D	mW		150
Junction temperature	TJ	°C		-55 to +150
Storage temperature	T _{STG}	°C		-55 to +150



Electrical Characteristics (Ta=25°C Unless otherwise specified)

Item	Symbol	Unit	Conditions	Min	Тур	Мах
Innut voltage	VI _(off)	V	V _{cc} =5V,Ic=100uA	0.3		
Input voltage	VI _(on)	V	V _o =0.3V,Ic=1mA			1.4
Output voltage	V _{O(on)}	V	I _o /li =5mA/0.25 mA			0.3
Input current	I,	mA	V _i =5V			0.88
Output current	I _{O(off)}	uA	V _{cc} =50V, Vi=0			0.5
DC current gain	Gı		V _o =5V, I _o =5mA	68		
Input resistance	R ₁	kΩ		7	10	13
Resistance ratio	R ₂ /R ₁			3.7	4.7	5.7
Transition frequency	f⊤	MHz	V _{CE} =10V,I _E =5mA,f=100MHz		250	

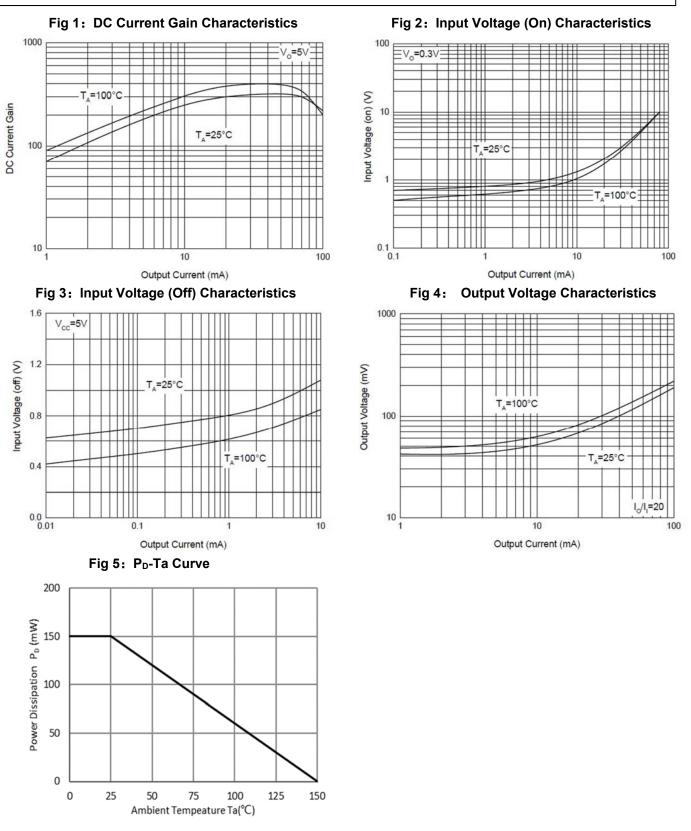
Thermal Characteristics

Parameter	Symbol	Unit	Value
Thermal resistance, junction-to-ambient	Røj-A ⁽¹⁾	°C/W	833
Thermal resistance, junction-to-case	R ₀ J-C ⁽¹⁾	°C/W	667

Note:

(1) Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 25.4mm*25.4mm copper pad areas

Characteristics

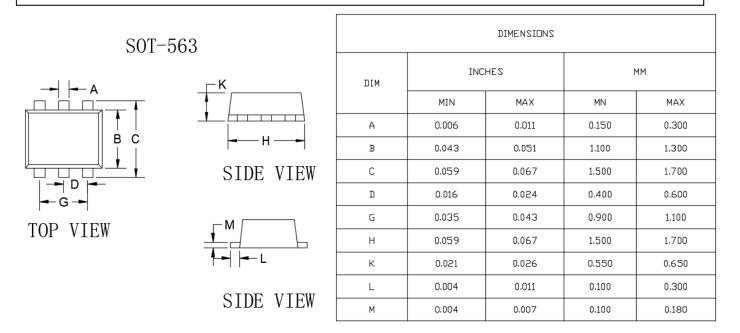




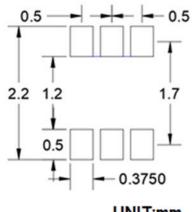
Ordering Information

Preferred P/N	Packing	Unit weight(g)	Minimum	Inner box	Outer carton	Delivery mode	
	code		package(pcs)	quantity(pcs)	quantity(pcs)	,	
EMH9	F2	Approximate 0.0035	3000	30000	120000	7" reel	

Outline Dimensions



Suggested Pad Layout



UNIT:mm



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