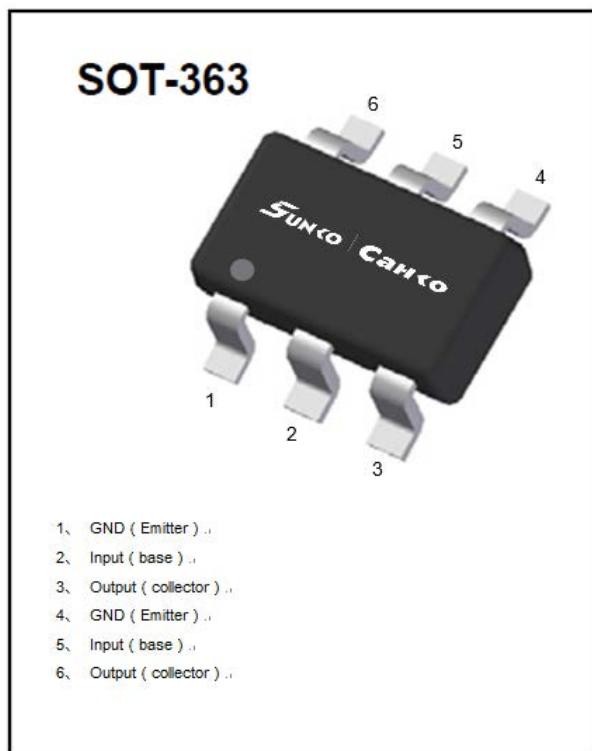


## Dual NPN Digital Transistors (Built-in Resistors)



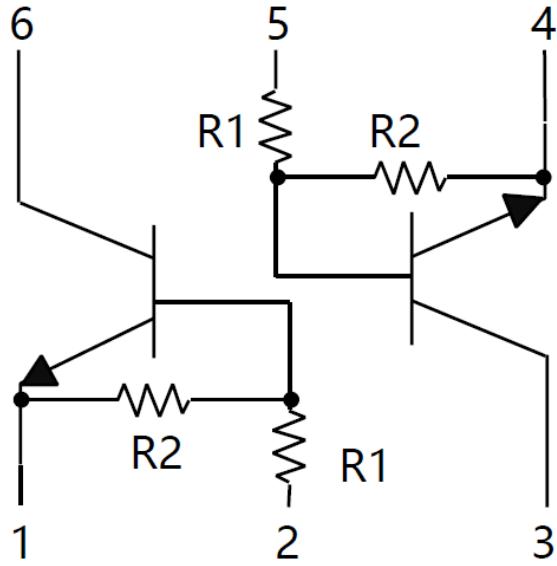
### Features

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

### Mechanical Data

- **Package:** SOT-363
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:**H20

### ■Equivalent circuit



**■Maximum Ratings (Ta=25°C Unless otherwise specified)**

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V <sub>CC</sub>	V		50
Input Voltage	V <sub>IN</sub>	V		-10 to +12
Output Current	I <sub>O</sub>	mA		100
Power Dissipation	P <sub>D</sub>	mW		200
Junction Temperature (Single)	T <sub>J</sub>	°C		150
Storage Temperature	T <sub>STG</sub>	°C		-55 to +150

**■Electrical Characteristics (Ta=25°C unless otherwise specified)**

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	V <sub>I(off)</sub>	V	V <sub>CC</sub> =5V, I <sub>C</sub> =100uA	0.5	-	-
	V <sub>I(on)</sub>	V	V <sub>O</sub> =0.3V, I <sub>C</sub> =20mA	-	-	3
Output voltage	V <sub>O(on)</sub>	V	I <sub>O</sub> /I <sub>i</sub> =10mA/0.5 mA	-	-	0.3
Input current	I <sub>I</sub>	mA	V <sub>I</sub> =5V	-	-	3.8
Output current	I <sub>O(off)</sub>	uA	V <sub>CC</sub> =50V, V <sub>i</sub> =0	-	-	0.5
DC current gain	G <sub>I</sub>		V <sub>O</sub> =5V, I <sub>O</sub> =20mA	20	-	-
Input resistance	R <sub>1</sub>	kΩ		1.54	2.2	2.86
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>			0.8	1	1.2
Transition frequency	f <sub>T</sub>	MHz	V <sub>CE</sub> =10V, I <sub>E</sub> =5mA, f=100MHz	-	250	-

**■ Ordering Information (Example)**

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
UMH20N	F2	Approximate 0.009g	3000	30000	120000	7" reel

## ■ Characteristics (Typical)

Fig. 1 - DC Current Gain Characteristics

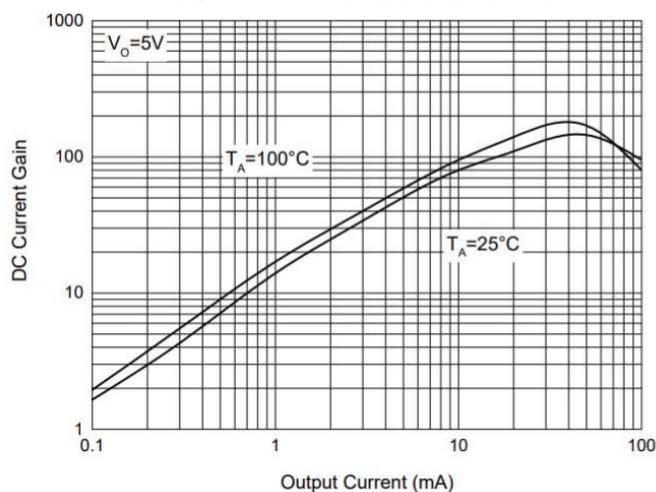


Fig. 2 - Input Voltage (on) Characteristics

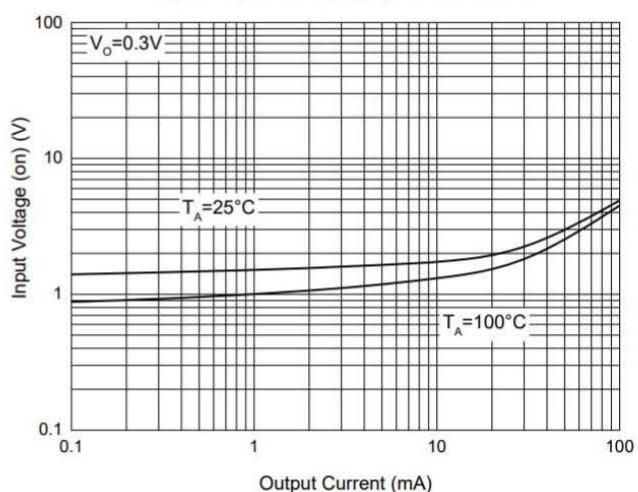


Fig. 3 - Input Voltage (off) Characteristics

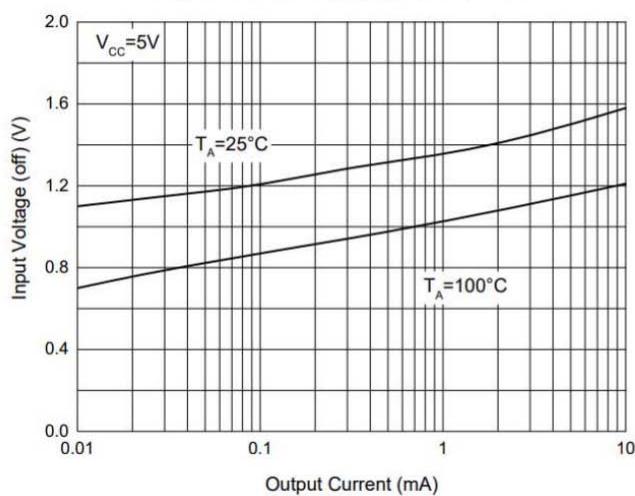


Fig. 4 - Output Voltage Characteristics

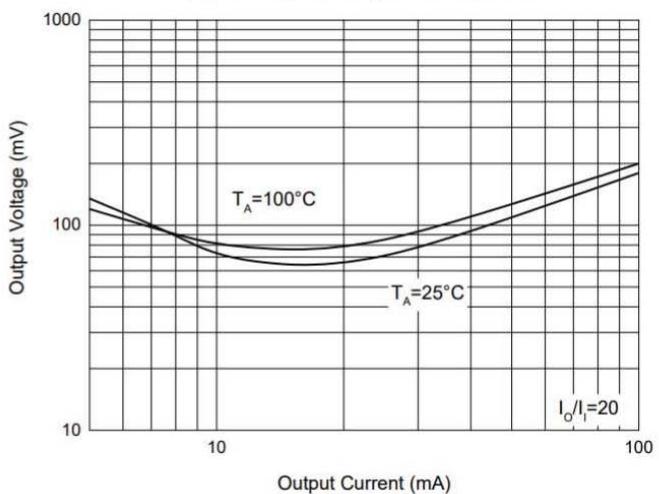
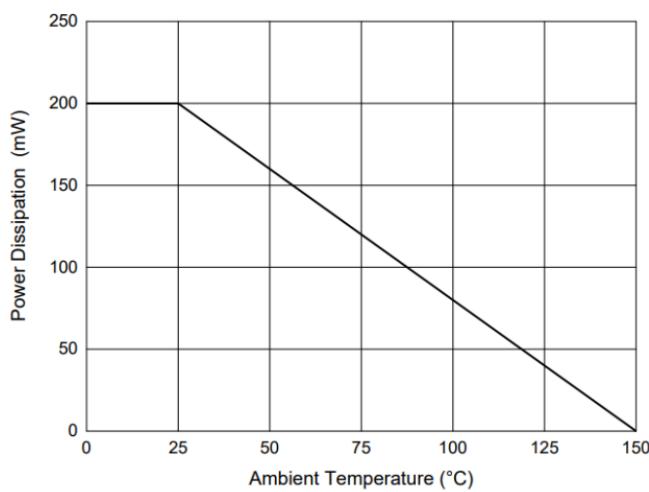
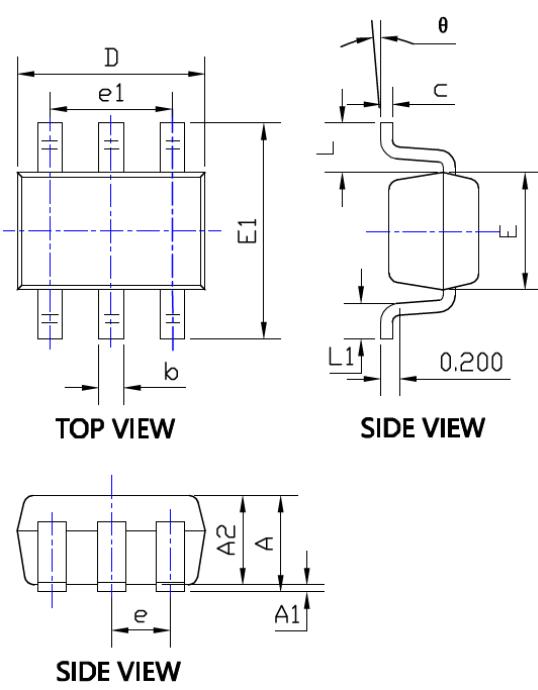


Fig. 5 - Power Derating Curve

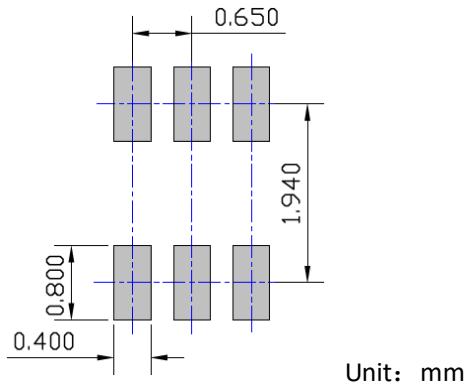


### ■ SOT-363 Package Outline Dimensions



SYMBOL	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.035	0.043	0.900	1.100
A1	0.000	0.004	0.000	0.100
A2	0.035	0.039	0.900	1.000
b	0.006	0.014	0.150	0.350
c	0.004	0.010	0.100	0.250
D	0.071	0.087	1.800	2.200
E	0.045	0.053	1.150	1.350
E1	0.085	0.096	2.150	2.450
e	0.026TYP		0.650TYP	
e1	0.047	0.055	1.200	1.400
L	0.021REF		0.525REF	
L1	0.010	0.018	0.260	0.460
θ	0°	8°	0°	8°

### ■ SOT-363 Suggested Pad Layout



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